

Practical 8
Circuit Block Diagram

Part A

Objective

Understand Encoders Decoders and Block Circuit Diagrams

1. Complete block diagram below

A

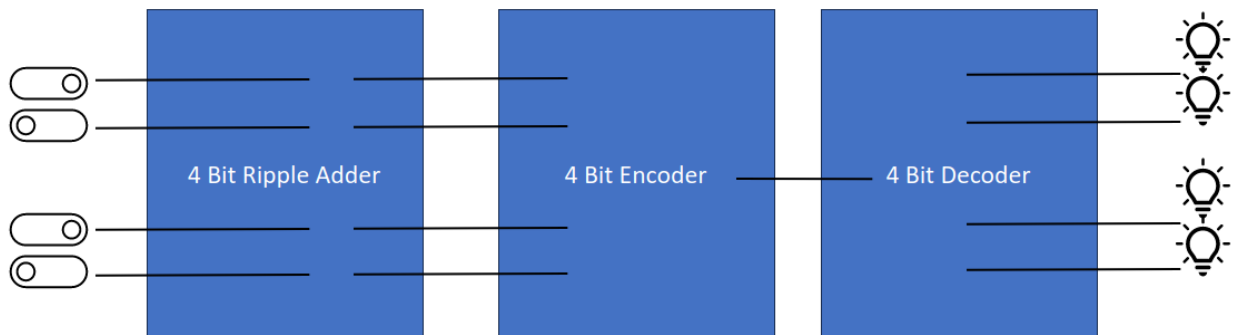
Within computer architecture data encoding and decoding lines are utilised to transfer between systems

(i) Draw a transistor level diagram and truth table for a 2 to 1 line encoder

(ii) Draw a transistor level diagram and truth table for a 1-to-2-line decoder

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2. Using logic.ly complete logic gate level circuit for the block circuit diagram below



Hand up this practical report at the end of session and ensure it has been checked

Student Name		Student Number	
Date		Checked	
Group	A / B		