

## ELECTRONICS AND ELECTRICAL ENGINEERING LAB

IFE, Information Technology, sem. 2

		Team number							
		1	2	3	4	5	6	7	8
Weeks	1	Introduction							
	2	M	M	M	M	M	M	M	M
	3	M	M	M	M	M	M	M	M
	4	M	M	M	M	M	M	M	M
	5	D	D	D	D	D	D	D	D
	6	T1	T1	T1	T1	T1	T1	T1	T1
	7	T1	T1	T1	T1	T1	T1	T1	T1
	8	W	EL3	T2	EL1	W	EL2	T2	F1
	9	W	EL2	T2	F1	W	EL3	T2	EL1
	10	K	W	EL3	O	K	W	EL2	Z
	11	EL1	W	EL2	O	F1	W	EL3	Z
	12	Z	K	W	EL3	EL2	K	W	O
	13	Z	F1	W	EL2	EL3	F1	W	O
	14	Additional repetition time							
	15	Additional repetition time							

Metrology (M)

Diodes (D)

Transistors 1 (T1)

Transistors 2 (T2)

Passive filters (F1)

Operational amplifier (W)

Comparator (K)

Power supply (Z)

Optoelectronic components (O)

Digital logic components (EL1)

Combinational logic (EL2)

Combinational logic (EL3)