IEEE Spectrum article on "The Trojan Proof Chip"

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Jerry Kickenson Signature Track · 7 hours ago

I thought it interesting to post a link to an IEEE Spectrum article in the February issue: Stopping Hardware Trojans in Their Tracks.

The authors (from Stanford) use two techniques: split manufacturing and obfuscation of checker circuitry. The transistors and main layers of the chip are built by an untrusted foundry, but the last layer that includes the checker circuitry is added by a trusted foundry. To guard against design trojans, the checker design is left to programmable circuitry not defined until afater main part of ship is manufactured. Interesting approach - it has limitations, and split manufacturing adds costs, but for sensitive chips it can be worth it.

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