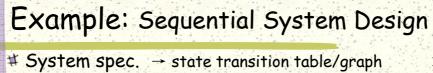
## Sequential System Design

- # Given: system description
- # Goal: logic diagram, Boolean function expression
  - 1. System specification
  - 2. State transition table construction
  - 3. State minimization and encoding
  - 4. Flip-flop selection
  - 5. Excitation/output table derivation
  - 6. Logic simplification
  - 7. Logic diagram drawing



→ state minimization/encoding

→ flip-flop selection

→ excitation/output table derivation

	0/0	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	<b>'1</b>
	Q(†)	Q(†+1)	* T
	0	0	0
6	0	1	1
	1	0	1
- 1			

State		In	In State		inputs		Out	
A	В	×	A	В	TA	ТВ	У	
0	0	0	0	1	0	1	0	
 0	0	1	0	0	0	0	1	,
 0	1	0	1	0	1	1	1	*
0	1	1	1	1	1	0	0	
1	0	0	0	0	1	0	1	
1	0	1	0	1	1	1	0	
 1	1	0	1	1	0	0	0	2 8
 1	1	1	1	0	0	1	1	

