

INSTRUCTIONS
16 TOTAL
Each splits the bytes into what's required
Performs their operation
(IF needed increment the program counter)

KEY:
rx = register, a valuw 0-7 for our
bb = a byte
aaa = an address

Registers
8 bit r0-r7
Program counter: 16 bit reg,
stores address of current instruction

Timer: 8 bit reg for storing timer
value

Address - 16 bit reg for storing an
address

Memort, single bit flag that
determines whether memory should
be ram or ROM-- 0 ram 1 rom

Ram
TOTAL 4kb
4096 bytes
32768 bits

Rom starts at 0x0 , reads only,
throws an error if a write operation is
attempted in rom

CPU 500 instructions a second
each cycle the computer reads 2
bytes from the ROM at program
counter and p+1 and passes to CPU
TO execute
the 2 bytes make 1 instruction
p should always be even

All numbers are base 16

Timer, when it's not 0 , the cpu
decrements the value by 1 every
16ms

Bytes will be

4 kilobytres
8 bits each

8x8 Display
with a frame
buffer of 64
bytes

