Figure 1-7 The testbench — design environment
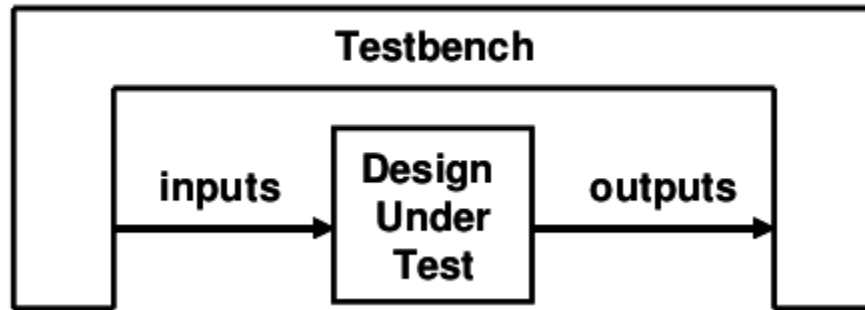

Testbench

inputs → **Design Under Test** → outputs

Figure 1-11 Testbench with scenario layer

Scenario	Generator	*Environment*
Functional	Agent → Scoreboard ← Checker	
Command	Driver Assertions Monitor	
Signal	DUT	