

[illegible]

The image displays a power supply circuit diagram. The main circuit consists of a power input section (VIN) connected to a DC-DC converter (RT8272). The converter's output (VOUT) is regulated to +3.3V. The circuit includes various capacitors (C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15, C16) and resistors (R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40, R41, R42, R43, R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59, R60, R61, R62, R63, R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R90, R91, R92, R93, R94, R95, R96, R97, R98, R99, R100). The output is connected to a load (R100) and a diode (D1). The circuit is powered by a +VDC source and grounded (GND).

AUDIO

The schematic diagram illustrates the audio amplifier circuit centered around the TAS5825M IC (U3). The circuit is powered by a +3V3 supply and a +VDC supply. The +3V3 supply is connected to pins 6 (DVDD), 7 (VR_DIG), 19 (AVDD), 18 (GVDD), 22 (PVDD), 21 (PVDD), and 4 (PVDD). The +VDC supply is connected to pins 30 (OUTA+), 29 (OUTA-), 24 (OUTB+), 27 (OUTB-), and 28 (BST_B-). The IC has several control pins: LRCLK (12), SCLK (13), SDIN (14), SDA (15), SCL (16), ADR (8), GPIO0 (9), GPIO1 (10), GPIO2 (11), PDN (17), and AGND (20). The output pins are OUTA+ (30), OUTA- (29), OUTB+ (24), OUTB- (27), and BST_B- (28). The circuit includes various capacitors (C17, C18, C19, C20, C21, C22, C27, C28, C29, C34, C35, C23, C24, C25, C30, C31, C32, C33) and resistors (R21, R22, R23, R24, R25). The output signals are connected to J10 and J11. The J10 connector is labeled with pins 1 (GND), 2 (TAS_IPDN), 3 (TAS_SCL), 4 (TAS_SDA), 5 (TAS_DI), 6 (TAS_BCK), 7 (TAS_FS), 8 (TAS_DO), 9 (TAS_MUTE), and 10 (TAS_FAULT). The J11 connector is labeled with pins 1 (GND), 2 (OUTA+), and 3 (OUTA-). The J12 connector is labeled with pins 1 (GND), 2 (OUTB+), and 3 (OUTB-). The circuit also includes two MIC_I2S_SKT connectors (MK1 and MK2) for microphone input. The MIC_I2S_SKT connectors are connected to the TAS5825M IC via L/R, FS, BCK, and DO pins. The L/R pin is connected to GND, the FS pin is connected to +3V3, the BCK pin is connected to MIC_BCK, and the DO pin is connected to MIC_DO.

MECHANICAL

FID1 FID2 FID3 FID4 FID5 FID6

H1 H2 H3 H4

Author: D. Murgia
Itz embedded
 Sheet: /
 File: BassOwl_Hat.sch
Title: BassOwl Hat

Size: A4	Date: 2020-09-10	Rev: 1.2
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