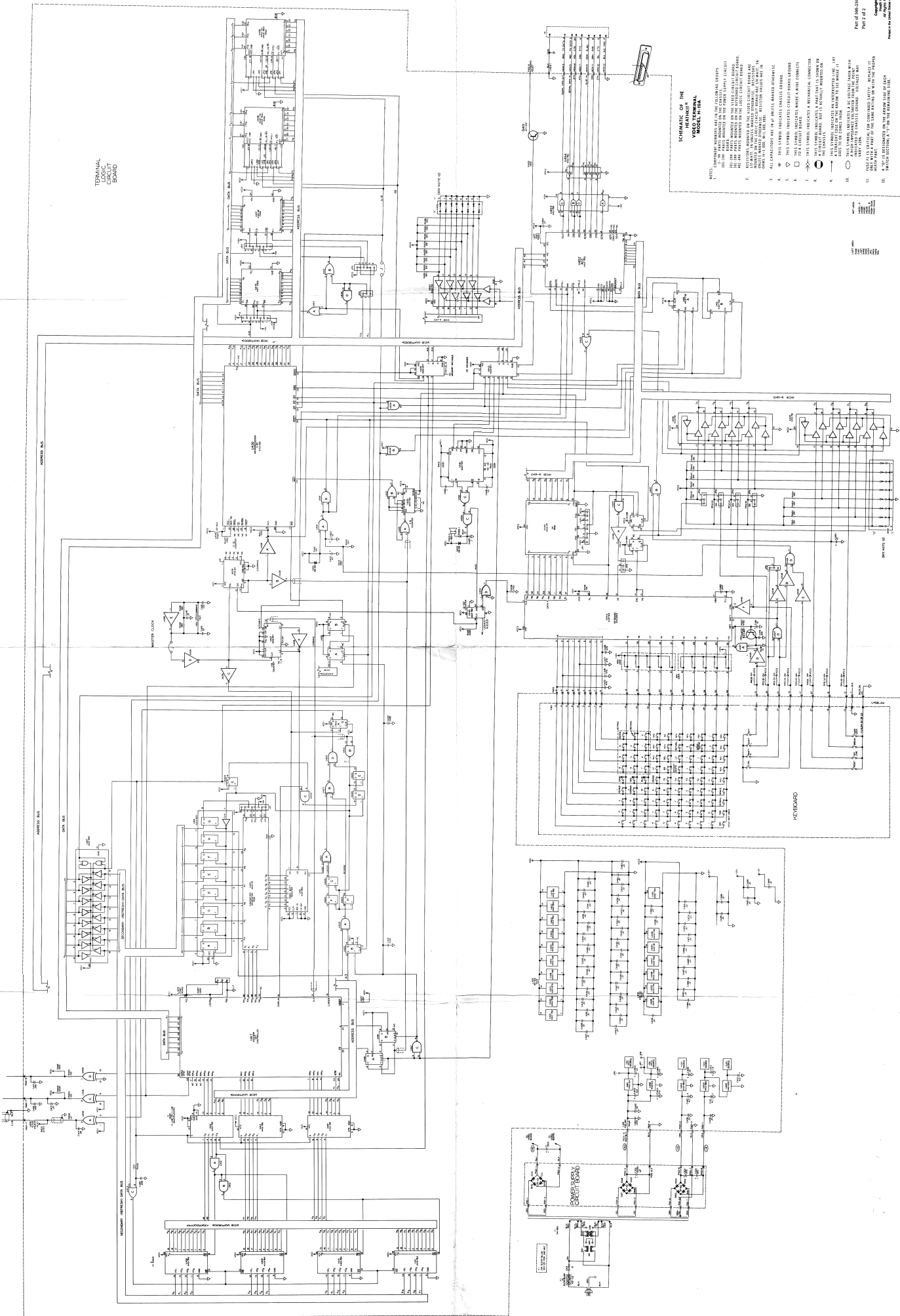


# TECHNICAL CIRCUIT DRAWING



## SCHEMATIC OF THE RECEIVER VISUAL UNIT

1. THIS SCHEMATIC IS A LOGIC DIAGRAM OF THE RECEIVER VISUAL UNIT. IT SHOWS THE LOGIC OF THE UNIT AND THE CONNECTIONS BETWEEN THE LOGIC AND THE OTHER PARTS OF THE UNIT.
2. THE LOGIC IS SHOWN IN THE LOGIC SYMBOLS. THE LOGIC SYMBOLS ARE THE LOGIC GATES, LOGIC FLIP-FLOPS, LOGIC MULTIPLEXERS, LOGIC DEMULTIPLEXERS, LOGIC DECODERS, LOGIC ENCODERS, LOGIC COMPARATORS, LOGIC ALGEBRA, LOGIC ARITHMETIC, LOGIC SHIFT REGISTERS, LOGIC COUNTERS, LOGIC TIMERS, LOGIC OSCILLATORS, LOGIC GENERATORS, LOGIC DETECTORS, LOGIC FILTERS, LOGIC AMPLIFIERS, LOGIC COMPARATORS, LOGIC ALGEBRA, LOGIC ARITHMETIC, LOGIC SHIFT REGISTERS, LOGIC COUNTERS, LOGIC TIMERS, LOGIC OSCILLATORS, LOGIC GENERATORS, LOGIC DETECTORS, LOGIC FILTERS, LOGIC AMPLIFIERS.
3. ALL CONNECTIONS ARE IN THE LOGIC SYMBOLS. THE LOGIC SYMBOLS ARE THE LOGIC GATES, LOGIC FLIP-FLOPS, LOGIC MULTIPLEXERS, LOGIC DEMULTIPLEXERS, LOGIC DECODERS, LOGIC ENCODERS, LOGIC COMPARATORS, LOGIC ALGEBRA, LOGIC ARITHMETIC, LOGIC SHIFT REGISTERS, LOGIC COUNTERS, LOGIC TIMERS, LOGIC OSCILLATORS, LOGIC GENERATORS, LOGIC DETECTORS, LOGIC FILTERS, LOGIC AMPLIFIERS.
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