µA78MG • µA79MG

4-TERMINAL POSITIVE AND NEGATIVE AD JUSTABLE VOLTAGE REGULATORS

FAIRCHILD LINEAR INTEGRATED CIRCUITS

GENERAL DESCRIPTION - The µA78MG and µA79MG are 4 Terminal Adjustable Voltage Regula tors. They are designed to deliver continuous load currents of up to 500 mA with a maximum input voltage of 40 V for the positive regulator 76MG and - 40 V for the negative regulator 79MG. Output current capability can be increased to greater than 10 A through use of one or more external transistors. The output voltage range of the 78MG positive voltage regulator is 5 V to 30 V and the output voltage range of the negative 79MG is -30 V to -2.2 V. For systems requiring both a positive and negative, the 78MG and 79MG are excellent for use as a dual tracking regulator. These 4-terminal voltage regulators are constructed using the Fairchild Planar* process

- OUTPUT CURRENT IN EXCESS OF 0.5 A
- #A78MG POSITIVE OUTPUT VOLTAGE 5 TO 30 V
- μΑ79MG NEGATIVE OUTPUT VOLTAGE -30 V TO -2.2 V
- INTERNAL THERMAL OVERLOAD PROTECTION INTERNAL SHORT CIRCUIT CURRENT PROTECTION
- OUTPUT TRANSISTOR SAFE AREA PROTECTION POWER MINI DUAL IN-LINE PACKAGE

ARSOLLITE MAXIMUM BATINGS

Input Voltage HA78MG, HA79MGC any -40V "A79MG "A79MGC Control Pin Voltage 0 < V < VOUT #A78MG, #A78MGC µA79MG, µA79MGC -VOUT < -V < 0 Power Dissipation Internally Limited Operating Junction Temperature Range (Note 1) Military (µA78MG, µA79MG) -55°C to 150°C 0°C to 150°C Commercial (µA78MGC, µA79MGC) Storage Temperature Range -65°C to +150°C 4-Pin TO-39 -55°C to +150°C Power Mini DIP and Power TAB Lead Temperature Power TAB and Power Mini DIP (Soldering, 10 s) 230°C

"A79MG

CONNECTION DIAGRAMS (TOP VIEWS) POWER MINI DIP PACKAGE OUTLINE S POWER TAB PACKAGE OUTLINE 8Z PACKAGE CODE UI

4-Pin TO-39(Soldering 60 s)

ORDER INFORMATION uA 79MG "A79MGU10

IN (3) CONT (1) OUT (2) ORDER INFORMATION

4-LEAD TO-39

PACKAGE OUTLINE 5K

COMM (4)

PART NO uA79MG PA79MGHM "A 79MOC #A79MGHC

ended for direct electrical connection

"A78MC CONNECTION DIAGRAMS (TOP VIEWS)

> POWER MINI DIP PACKAGE OUTLINE 9V PACKAGE CODE T2



ORDER INFORMATION TYPE uA78MGT20 uA78MGC

POWER TAB PACKAGE OUTLINE 82

PACKAGE CODE U



ORDER INFORMATION HA78MGC #A78MGU10

4-PIN TO-39 PACKAGE OUTLINE 5K

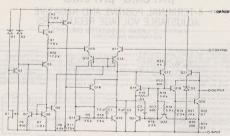


ORDER INFORMATION TYPE µA78MGHM **µА7ВМ**С "AZBMGHO

eat sink tabs connected to common through device substrate.

FAIRCHILD LINEAR INTEGRATED CIRCUITS . #A78MG . #A79MG

79AIG EQUIVALENT CIRCUIT



Resistor values in Ω unless otherwise noted

μΑ79MG (C. HC. HM)

ELECTRICAL CHARACTERISTICS Unless otherwise specified, the following strecifications apply: 0°C < T_J < 125°C for µA79MGHG, V_IN = -10 V, IQUT = 350 mA, Test Circuit 2,

PARRMETER	CONDITION (Note 1)			MIN	TYP	MAX	UNITS	
Input Voltage Range	Tj = 25°C		-40	7 10 10 10 10 10 10 10 10 10 10 10 10 10	7.0	V		
Output Voltage Range	VIN = VOUT - 5 V		-	-30	17/41	-2.23	V	
Output Voltage Tolerance	V _{OUT} - 15 V < V _{IN} < V _{OUT} - 3 V. T _J = 25° 5 mA < I _{OUT} < 350 mA P _D < 5 W, V _{INMAX} = -38 V		T1 = 25°C	-30	THE PARTY AND	4.0	NVOUT	
				EV SA	-	5.0	%(VOUT	
				10 22	BATTI	5.0	MIVOUT	
Line Regulation	TJ = 25°C, IOUT = 200 mA, VOUT > -10 V					-		
	(V _{OUT} - 20 V) < V _{IN} < (V _{OUT} - 2.5 V) T _J = 25°C, l _{OUT} = 200 mA, V _{OUT} < -10 V (V _{OUT} - 15 V(< V _{IN} < (V _{OUT} - 3 V) (V _{OUT} - 7 V) < V _{IN} < (V _{OUT} - 3 V)			A PLANTER	HE SHE W	1.0	%(VOUT)	
				Control of the Contro	No or day	Tanasa in Laboratoria		
				- Neoral		0.75	"IVOUT	
				PAID MOOT	10000	0.67	SIVOUT)	
Load Regulation	V _{IN} = V _{OUT} - 7 V, 5 mA < I _{OUT} < 500 mA T _J = 25°C			PERSONAL PROPERTY.	MOST STORY	10	%(VOUT)	
April 19 and 19					1	TRUSK STAND		
Control Pin Current Quiescent Current	T _J = 25°C				pr 0/9/20	3.0	μΛ	
	A ROUGH TAXON				CALL AND ST	2.0	μΛ	
Quiescent Current	T _J = 25°C			The A	0.5	1.5	mA	
MR SWALLING BENEVE	12.1			42	I PL	2.5	. mA	
Ripple Rejection		TJ = 25 C. 101		54	65		dB	
181 197	VOUT = -5 V, f = 120 Hz IOUT = 100 mA		50	I. S. T. L.S.		dB		
Output Noise Voltage	10 Hz < f < 100 kHz, V _{OUT} = −5 V				125		μV	
Dropout Voltage	(Note 2)	μA79M0	SHM			2.5	v	
		μA79M0	(HC and C)		-7	2.3		
Short Circuit Current	V _{IN} = -35 V			Day I digital	100		mA	
eak Output Current					650	DO SOAKS	mA	
Average Temperature	V _{OUT} " -5 V			CUCLAMAN AS	-0.4	F CLEGGER N	mV/"C	
Coefficient of	IOUT * 5 mA							
Output Voltage	Constitute the breast to	Mort William	1000			Walter St.		
Control Pin Voltage	Tj = 25°C			- 2.32	-2.23	-2.14	V	
(Reference)				-2.35		-2.11	V	

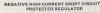
NOTE: The convention for Negative Regulators is the Algebraic value, thus -15 is less than -10 V.

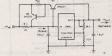
FAIRCHILD LINEAR INTEGRATED CIRCUITS . #A78MG . #A79MG

TYPICAL APPLICATIONS FOR 79MG

Bypass capacitors are recommended for stable operation of the µA79MG over the input voltage and output current ranges. Output bypass capacitors will improve the transient response of the regulator.

The bypass capacitors, 12 µF on the input, 1 µF on the output should be ceramic or solid tentalum which have good high frequency characteristics. If aluminum electrolytics are used, their values should be 10 µF or larger. The bypass capacitors should be mounted with the shortest leady, not if possible, directly around the region terminals.





-30 V TO -2.2 V ADJUSTABLE REGULATOR



BASIC NEGATIVE REGULATOR

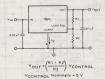


CONTRACT SEASON

NEGATIVE HIGH CURRENT VOLTAGE REGULATOR EXTERNAL SERIES PASS



78MG TEST CIRCUIT 1



79MG TEST CIRCUIT 2



Recommended R2 current ≈ 1 mA ∴R2 = 5 kΩ(78MG) R2 = 2.2 kΩ (79MG)

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TYPICAL APPLICATIONS FOR µA78MG

In many "AZBMG applications, compensation opacitors may not be required. However, for stable operation of the regulator over all input voltage and output current range, bypassing of the input and output (0.33 µF and 0.1 µF, respectively) is recommended, lingui bypassing is necessary if the regulator is located for from the filter capacitor of the power supply, Spyssing for output will improve the transient reposit of the resultion.

