

Dr. Kartik S. Pandya obtained his Ph.D in Electrical Engineering from the M.S. University of Baroda in the year 2013. He obtained M.E. and B.E in Electrical Engineering with first class with distinction from the L.D. College of Engineering, Ahmedabad in 2001 and 1999 respectively. Currently he is working as a Full Professor in the Department of Electrical Engineering, CSPIT, CHARUSAT. He joined CHARUSAT as a lecturer in the year 2001. His research area includes Computational Intelligence optimization Methods, Restructured Power Systems and Smart Grid. His Proposed CI algorithms entitled “Levy-DEEPSO”, “Entropy Enhanced Covariance Matrix Adaptation Evolution Strategy”, “Improved Chaotic Differential Evolutionary Particle Swarm Optimization”, “Enhanced Velocity Differential Evolutionary Particle Swarm Optimization” and “Improved_Chaoctic_Differential Evolutionary Particle Swarm Optimization” secured 3rd, 2nd, 3rd, 2nd and 4th ranks respectively in IEEE and WCCI international competitions at USA and Brazil. He has been awarded with “Outstanding Contribution in Reviewing” in “International Journal of Electrical Power and Energy Systems” published by Elsevier. His team had successfully completed a consultancy project of ABB, entitled “Prototype Development of Switch-Sync Simulator Demo Kit”. He is the MTECH Coordinator, NAAC-IQAC, AAA, CDB Coordinator, NIRF ranking coordinator, University level coordinator for SIRO and PFRI, UIIC member and Program committee member of Gala Industry University Meet, Research Selection Coordinator, BOS and Faculty Board member of EE department.