


Staff information

Name	Hiteshkumar Natvarlal Patel
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Brief	<p>Mr. Hitesh Patel received B.E. Degree in E.C from North Gujarat Uni. & M.E degree in E.C. Engineering from Gujarat University. He has secured First rank in all over Gujarat in Master of Engineering. He has received Gold Medal for Securing First rank in M.E. He is pursuing his PhD in area of Embedded antenna Design. He is presently serving in the department of EC engineering at Charotar University of Science and Technology Changa since October 2008. He has an expertise in Circuits and Networks, Digital electronics, Microprocessors, Advanced Microcontrollers, Embedded Systems Design, Real Time Operating System, Digital System Design and Verification & IoT. He has conducted more than 20 workshops in the area of Microcontrollers, ARM Processor, Arduino, PCB design, Embedded C, Arduino C, Python programming, Verilog and System Verilog Programming, RTOS, Raspberry Pi & Internet of Things. He has guided 15+ PG Students. He has also guided more than 100 UG level projects. He has published 20 research papers in national & international journals/conference. He has completed some major project.</p>
Email address	Hiteshpatel.ec@charusat.ac.in
Linked in address	https://www.linkedin.com/in/hitesh-patel-ba611968/
Website or blog address	https://sites.google.com/a/ecchanga.ac.in/hitesh/

Areas of Interest	Embedded System Design, Digital system design and verification, Internet of Things.
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