



## List of Experiments

Name of Teacher: Prof. Amit Parmar

Date: 26/07/2021

Subject: IT250 Digital Electronics

Semester : 3

Exp. No	Aim of Experiment
1.	Virtual Digital Electronics lab, TinkerCad introduction and Tool working
2.	To study and verify Digital Logic gates using ICs.
3.	To study & verify logic circuit using AOI Logic & De Morgan's theorem
4.	To study & verify NAND & NOR gate as Universal gates.
5.	To study & verify the design of Simplification of Boolean Functions Using K-Map Techniques.
6.	To study & verify the design of half adder & half subtractor combinational circuit.
7.	To study & verify the design of full adder combinational circuit.
8.	To study & verify the design of BCD to Excess-3 code convertor.
9.	To study & verify the design of 4-bit Magnitude comparator.
10.	To study & verify the design of Multiplexer & Decoder.
11.	To study & verify the design of operation of flip flop.
12.	To study & verify the design of operation of Shift register.