#### **FPGA LAB**

#### **MINI PROJECT**

# IMPLEMENTATION OF MORSE CODE DETECTION ON FPGA

Submitted in the partial fulfilment of the requirements of the degree of

**Bachelor of Technology** 

By:

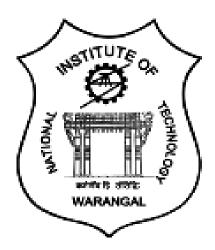
**Abhishek Nalla-21ECB0B37** 

N.Sri Darshan-21ECB0B38

P.Mokshagna Reddy-21ECB0B39

**Guided by:** 

Dr.P.Prithvi & Dr.V.Narendar



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

NATIONAL INSTITUTE OF TECHNOLOGY, WARANGAL

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## **ABSTRACT**:

The objective of this project is to design and implement a Morse code detector using a field-programmable gate array (FPGA) and to display the transmitted characters on a 7-segment display. The input signals will be obtained through a push button interface. The FPGA will be programmed using Verilog hardware description language (HDL) to achieve the desired functionality.

Morse code is a communication system that represents alphanumeric characters using a sequence of dots and dashes. In this project, push buttons on the FPGA are used to input Morse code signals. According to the Morse code standard, a dot corresponds to one unit, and a dash corresponds to three units. The space between parts of the same letter is one unit, the space between letters is three units, and the space between different words is seven units. In binary code, these spaces are represented by low voltage (0), while dots and dashes are represented by high voltage (1).

The main concept is to establish a specific time period range to distinguish the signal as a dot, a dash, or a variety of spaces. When the push button is pressed (positive edge) or released (negative edge), a new clock cycle begins, and the duration of each clock pulse determines the sequence of dots and dashes. An asynchronous finite-state machine (FSM) is utilized to detect the input sequence, and the FPGA generates the output, which can be displayed on an LCD screen or a 7-segment display.

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# **CERTIFICATE**

This is to certify that the dissertation work entitled IMPLEMENTATION OF MORSE CODE DETECTION ON FPGA is a Bonafide record of seminar work carried out by team members ABHISHEK NALLA(B37),N SRI DARSHAN(B38),P MOKSHAGNA REDDY(B39) submitted to the faculty of "Electronics and Communication Engineering Department", in partial fulfilment of the requirements for the award of the degree of Bachelor of Technology in "Electronics and Communication Engineering" at National Institute of Technology, Warangal during the academic year (2022-23).

#### Dr.V. Narendar

**Assistant Professor** 

**Department of Electronics and** 

**Communication Engineering** 

National Institute of Technology,

Warangal

#### Smt.P. Prithvi

**Assistant Professor** 

**Department of Electronics and** 

**Communication Engineering** 

National Institute of Technology,

Warangal

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### **1.INTRODUCTION:**

A continuous wave decoder, commonly known as a Morse code detector, is a tool or software designed to convert Morse code signals into text or audible sounds. Morse code is a form of communication that employs dots and dashes to represent letters and numerals.

#### International Morse Code

- 1. The length of a dot is one unit.
- 2. A dash is three units.
- 3. The space between parts of the same letter is one unit.
- 4. The space between letters is three units.
- 5. The space between words is seven units.

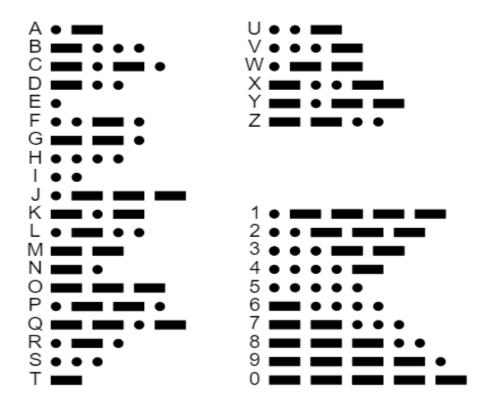


Fig.1-Interntional Morse Code

For radio operators, amateur radio enthusiasts, and emergency services personnel, Morse code detectors play a crucial role in decoding Morse code signals transmitted via radio waves. By analysing the audio or radio frequency signals, these tools can

recognize the patterns of dots and dashes that correspond to each letter or numeral in Morse code.

Morse code detectors come in various forms, ranging from hardware devices that connect to a radio or computer to software programs that run on a computer or mobile device. These tools have multiple applications, such as decoding emergency signals, monitoring Morse code transmissions, and enhancing Morse code communication proficiency.

A different variant of Morse code detector is a visual decoder that employs a flashing light or a set of LEDs to symbolize the dots and dashes of Morse code. This method enables the user to interpret the Morse code signal by observing the sequence of flashes.

Lastly, there exist computer algorithms capable of detecting Morse code signals from audio or video recordings. These algorithms utilize machine learning methods to identify the pattern of dots and dashes and convert them into written text.

In conclusion, Morse code detectors prove to be valuable instruments for amateur radio operators, emergency responders, and anyone who relies on Morse code communication. These devices are relatively straightforward to construct or acquire at a low cost, and they offer a dependable means to decipher Morse code signals.

# **2.BACKGROUND:**

The origins of Morse code can be traced to the early 1800s when Samuel Morse, an American artist and inventor, created a telegraph system that employed a combination of dots and dashes to signify letters and numerals. This innovation enabled the swift transmission of messages across great distances, thereby transforming the field of communication during that era.

With the growing popularity of Morse code, several tools were invented to recognize and comprehend the signals. Initially, Morse code detectors were basic instruments that employed electromechanical parts to transform the dots and dashes into visible or audible cues.

Towards the end of the 1800s, advancements in electronic components, such as vacuum tubes, led to the production of advanced Morse code detectors. These devices were capable of boosting and filtering the signals, thereby facilitating the differentiation of the dots and dashes from the surrounding noise.

Morse code played a critical role in military communication during World War II, and specialized Morse code detectors were devised specifically for use by military personnel. These tools were frequently compact and mobile, enabling soldiers to transmit messages via Morse code across extended distances.

In the subsequent years, Morse code persisted as a communication method used by amateur radio enthusiasts, and a range of Morse code detectors were designed for this

application. With the emergence of digital technology, computer algorithms were created to identify and interpret Morse code signals in audio or video recordings.

In the present day, Morse code detectors are still employed by amateur radio operators, emergency personnel, and individuals requiring Morse code communication. Despite technological advancements, the fundamental concepts of detecting and deciphering Morse code signals remain unaltered.

# 3.DESIGN AND WORKING

#### **3.1 CLOCK:**

In order to create a 1Hz clock, we utilized the 50MHz clock available in the FPGA and implemented a clock divider. By employing a counter and a comparator, we were able to count up to 25,000,000 before resetting to zero. The count was then compared to 24,999,999, and the comparator generated a '1' output when both values matched. This output was directed to a flip flop, which toggled its output every half second, effectively generating a 1Hz clock signal.

#### 3.2 FSM IN THE TRANSMITTER:

This particular FSM operates as a Moore machine with inputs that consist of 0s or 1s, and is designed to capture input data at each positive edge-triggered clock pulse. The input data is interpreted as either a dot, dash, or character space, depending on the timing of button presses or releases. Specifically, if a

single '1' is detected, flanked by zeros (010), the machine records a dot, whereas if three '1's are detected, flanked by zeros (01110), the machine records a dash.

The output of the FSM consists of two bits, which are as follows:

00: denotes the space between dashes and dots in a single letter (when the input is 0)

01: denotes a dot (when the input is 010)

10: denotes a dash (when the input is 01110)

11: denotes a character space (when the input is 000)

Finally, this 2-bit output is transmitted to a receiver for further processing.

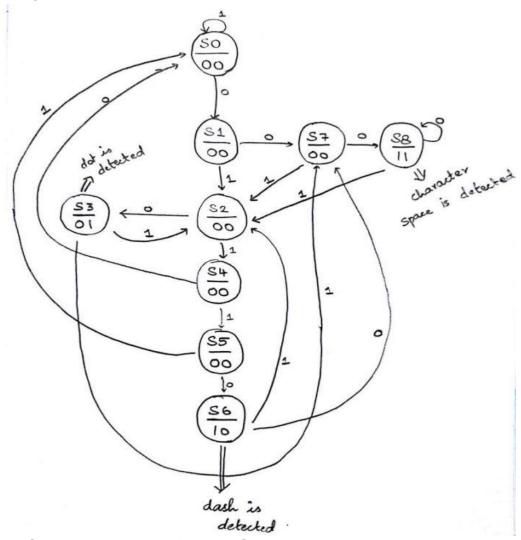


Fig.2-State diagram of the FSM present in the transmitter

#### 3.3 FSM in the Receiver:

The receiver employs a Mealy machine as its FSM, which receives 2-bit inputs that are the outputs of the previous FSM. Similar to the previous FSM, the input is captured at every positive edge-triggered clock pulse. The primary function of this FSM is to detect a sequence of dots and dashes and generate the corresponding output. Upon receiving a character space input, the output is changed according to the corresponding state, and the state is reset.

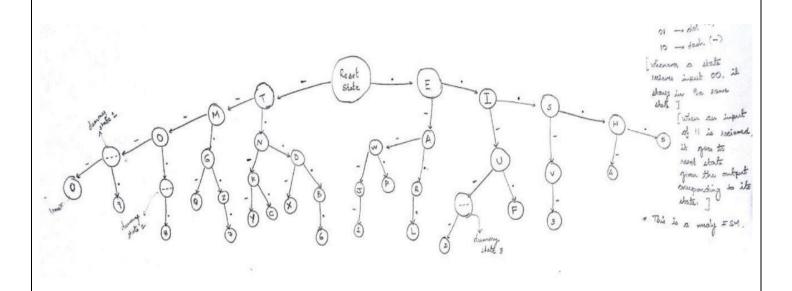


Fig.3- State diagram of the FSM present in the receiver

For the LED Display , we make use of the following table:

charac tra	In LED	When for 7 regment	In her	Morre Lede
A		p0001000	#08	
В	1	01100000	#60	
C	E	00110001	#31	
D	디	0100 0010	#42	
E	E	00110000	#30	
F	F	00111000	# 38	••
G	L	00100001	#21	
Н	1-1	0110 1000	#68	••••
I	1	01111001	#79	
5	ال	01000011	#43	•
K	H	01001000	#48	

charact -er	In LED Display	Value for 7 segment	In hex	Mouse code
L	\_	a 1 1 1 0 0 0 1	# 71	
M	ī	0 0101010	# 2A	
N	1-1	0110 1010	# 6A	
٥		01100010	#62	
P	17	00011000	# 18	
φ	9	0000 1100	#0C	
R	1	00111001	#39	• - • :
S	5	00100110	#24	• • • • •
Т	1=	a111 0000	of #	-
υ		01000001	#41	• • -
٧	נו	0101 0100	#54	4
W		00100011	#23	•,
×	1-1	10100 1000	# 48	
4	4	0100 0100	#44	

character	Ju LLD Desplaye	Nature for 7 sognouts	In her	Morse code
Z	3	00010010	#12	
0		00000001	#101	
1	1	01001111	#47	
2.	2	00010010	#12	
3	$\exists$	00000110	# 06	
ц	4	01001100	#40	
5	5	00100100	# 24	
6	5	00100000	# 20	
٦	7	00001111	# of	
8	8	00000000	#%	
9	9	0 0 0 0 0 100	#014	

Fig .4- 7-Segment Display tables

# 4.CODE:

```
module rec_fsm(mid_clk, s_out, clk, rst,
                                               serial_inp, en, dot);
module
           clock_divider(in_clk,
                                     rst,
                                               input serial_inp;
out_clk);
                                               input rst;
input in_clk;
                                               input clk;
input rst;
output reg out_clk;
                                               reg [7:0]state = 8'hff;
localparam cnst = 25000000;
                                               wire [1:0]p in;
reg [31:0] count;
                                               output reg [7:0]s_out;
always@(posedge in_clk, posedge rst)
                                               output mid_clk;
                                               output [3:0]en;
begin
                                               output dot;
if(rst == 1'b1)
count <= 32'b0:
                                               parameter [7:0]reset_state=8'hff;
else if (count == cnst - 1)
count <= 32'b0;
                                               parameter [7:0]a = 8'h00;
else
                                               parameter [7:0]b = 8'h01;
count <= count + 1;
                                               parameter [7:0]c = 8'h02;
end
                                               parameter [7:0]d = 8'h03;
                                               parameter [7:0]e = 8'h04;
always@(posedge in_clk, posedge rst)
                                               parameter [7:0]f = 8'h05;
begin
                                               parameter [7:0]g = 8'h06;
if(rst == 1'b1)
                                               parameter [7:0]h = 8'h07;
out clk \le 1'b0;
else if(count == cnst - 1)
                                               parameter [7:0]i = 8'h08;
                                               parameter [7:0]j = 8'h09;
out_clk <= ~out_clk;
                                               parameter [7:0]k = 8'h0a;
else
out_clk <= out_clk;
                                               parameter [7:0]l = 8'h0b;
                                               parameter [7:0]m = 8'h0c;
end
endmodule
                                               parameter [7:0]n = 8'h0d;
                                               parameter [7:0]0 = 8'h0e;
                                               parameter [7:0]p = 8'h0f;
```

```
parameter [7:0]q = 8'h10;
                                              .rst(rst),
parameter [7:0]r = 8'h11;
                                              .parallel_out(p_in),
parameter [7:0]s = 8'h12;
                                              .clk(clk) //may be prob
parameter [7:0]t = 8'h13;
                                              );
parameter [7:0]u = 8'h14;
parameter [7:0]v = 8'h15;
                                              assign en=4'b1110;
parameter [7:0]w = 8'h16;
                                              assign dot = 1'b1;
parameter [7:0]x = 8'h17;
parameter [7:0]y = 8'h18;
                                              always@(posedge mid_clk)
parameter [7:0]z = 8'h19;
                                              begin
parameter [7:0]zero = 8'h20;
parameter [7:0] one = 8'h21;
                                              case(state)
parameter [7:0]two = 8'h22;
parameter [7:0]three = 8'h23;
                                              reset state: //for reset state
parameter [7:0] four = 8'h24;
                                              case(p_in)
parameter [7:0] five = 8'h25;
                                              2'b00:begin
parameter [7:0]six = 8'h26;
                                              state<=state;
parameter [7:0]seven = 8'h27;
                                              s out<=8'hff;end
parameter [7:0]eight = 8'h28;
parameter [7:0]nine = 8'h29;
                                              2'b01:begin
                                              state<=e;
parameter [7:0]ds1 = 8'h2a;
                                              s out<=8'hff;end
parameter [7:0]ds2 = 8'h2b;
parameter [7:0]ds3 = 8'h2c;
                                              2'b10:begin
                                              state<=t;
                                              s_out<=8'hff;end
clock_divider clk_rec(
.in_clk(clk), //may be prob
                                              2'b11:begin
.rst(rst),
                                              state<=reset_state;
.out_clk(mid_clk)
                                              s out<=8'hff;end
);
                                              endcase
                                              a: //for a
trans_fsm trans_rec(
.serial_inp(serial_inp),
                                              case(p_in)
```

2'b00:begin endcase

state<=state;

s out<=8'hff;end c: //for c

case(p\_in)

2'b01:begin 2'b00:begin

state<=r; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=w; state<=reset\_state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'h08;end s\_out<=8'hff;end

endcase

2'b11:begin

b: //for b state<=reset\_state;

 $case(p_in)$  s\_out<=8'h31;end

2'b00:begin endcase

state<=state:

s\_out<=8'hff;end d: //for d

case(p\_in)

2'b01:begin 2'b00:begin

state<=six; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=reset\_state; state<=b;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=x;

s\_out<=8'h60;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'h42;end s\_out<=8'hff;end

endcase

2'b11:begin

e: //for e state<=reset\_state;

case(p\_in) s\_out<=8'h38;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end g: //for g

case(p\_in)

2'b01:begin 2'b00:begin

state<=i; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=a; state<=z;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=q;

 $s_{out} <= 8'h30;end$   $s_{out} <= 8'hff;end$ 

endcase

2'b11:begin

f: //for f state<=reset\_state;

case(p\_in) s\_out<=8'h21;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end h: //for h

case(p\_in)

2'b01:begin 2'b00:begin

state<=reset\_state; state<=state;

s out<=8'hff;end s out<=8'hff;end

case(p\_in)

2'b01:begin 2'b00:begin

state<=five; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=four; state<=reset\_state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=one;

s\_out<=8'h68;end s\_out<=8'hff;end

endcase

2'b11:begin

i: //for reset state state<=reset\_state;

 $case(p_in)$   $s_out <= 8'h43;end$ 

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end k: //for k

case(p\_in)

2'b01:begin 2'b00:begin

state<=s; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=u; state<=c;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=y;

s\_out<=8'h79;end s\_out<=8'hff;end

endcase

2'b11:begin

j: //for j state<=reset\_state;

s\_out<=8'h48;end s\_out<=8'hff;end

endcase

2'b11:begin

I: //for I state<=reset\_state;

case(p\_in) s\_out<=8'h2a;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end n: //for reset state

case(p\_in)

2'b01:begin 2'b00:begin

state<=reset\_state; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=reset\_state; state<=d;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=k;

s\_out<=8'h71;end s\_out<=8'hff;end

endcase

m: //for m

2'b11:begin

case(p\_in) s\_out<=8'h6a;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end o: //for o

case(p\_in)

state<=reset\_state;

2'b01:begin 2'b00:begin

state<=g; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=o; state<=ds1;

s_out<=8'hff;end	s_out<=8'hff;end
2'b10:begin	2'b01:begin
state<=reset_state;	state<=reset_state;
s_out<=8'hff;end	s_out<=8'hff;end
2'b11:begin	2'b10:begin
state<=reset_state;	state<=reset_state;
s_out<=8'h63;end	s_out<=8'hff;end
endcase	
	2'b11:begin
p: //for p	state<=reset_state;
case(p_in)	s_out<=8'h0c;end
2'b00:begin	endcase
state<=state;	
s_out<=8'hff;end	r: //for r
	case(p_in)
2'b01:begin	2'b00:begin
state<=reset_state;	state<=state;
s_out<=8'hff;end	s_out<=8'hff;end
2'b10:begin	2'b01:begin
state<=reset_state;	state<=I;
s_out<=8'hff;end	s_out<=8'hff;end
2'b11:begin	2'b10:begin
state<=reset_state;	state<=reset_state;
s_out<=8'h18;end	s_out<=8'hff;end
endcase	
	2'b11:begin
q: //for q	state<=reset_state;
case(p_in)	s_out<=8'h39;end
2'b00:begin	endcase

state<=state;

s: //for o state<=reset\_state;

 $case(p_in)$  s\_out<=8'h70;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end u: //for u

case(p\_in)

2'b01:begin 2'b00:begin

state<=h; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=v; state<=f;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=ds3;

 $s_{\text{out}} = 8 \text{ h24;end}$   $s_{\text{out}} = 8 \text{ hff;end}$ 

endcase

2'b11:begin

t: //for t state<=reset\_state;

 $case(p_in)$  s\_out<=8'h41;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end v: //for v

case(p\_in)

2'b01:begin 2'b00:begin

state<=n; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=m; state<=reset\_state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=three; state<=reset\_state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'h54;end s\_out<=8'hff;end

endcase

2'b11:begin

w: //for o state<=reset\_state;

case(p\_in) s\_out<=8'h48;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end y: //for y

case(p\_in)

2'b01:begin 2'b00:begin

state<=p; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=i; state<=reset\_state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'h23;end s\_out<=8'hff;end

endcase

2'b11:begin

x: //for x state<=reset\_state;

case(p\_in) s\_out<=8'h44;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end z: //for z

case(p\_in)

2'b01:begin 2'b00:begin

state<=state;

s\_out<=8'hff;end two: //for 1

case(p\_in)

2'b01:begin 2'b00:begin

state<=seven; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'h12;end s\_out<=8'hff;end

endcase

2'b11:begin

one: //for 1 state<=reset\_state;

 $case(p_in)$   $s_out <= 8'h12;end$ 

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end three: //for 3

case(p\_in)

2'b01:begin 2'b00:begin

state<=reset\_state; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'h4f;end s\_out<=8'hff;end

endcase

2'b11:begin 2'b10:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'h06;end s\_out<=8'hff;end

endcase

2'b11:begin

four: //for 4 state<=reset\_state;

 $case(p_in)$  s\_out<=8'h24;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end six: //for 6

case(p\_in)

2'b01:begin 2'b00:begin

state<=reset\_state; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'h4c;end s\_out<=8'hff;end

endcase

2'b11:begin

five: //for 5 state<=reset\_state;

case(p\_in) s\_out<=8'h20;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end seven: //for 7

case(p\_in)

2'b01:begin 2'b00:begin

state<=reset\_state; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b01:begin 2'b00:begin

state<=reset\_state; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'h0f;end s\_out<=8'hff;end

endcase

2'b11:begin

eight: //for 8 state<=reset\_state;

 $case(p_in)$  s\_out<=8'h08;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end zero: //for 1

case(p\_in)

2'b01:begin 2'b00:begin

state<=reset\_state; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'h00;end s\_out<=8'hff;end

endcase

2'b11:begin

nine: //for 1 state<=reset\_state;

 $case(p_in)$  s\_out<=8'h01;end

endcase

2'b11:begin

ds1: //for ds1 state<=reset\_state;

case(p\_in) s\_out<=8'hff;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end ds3: //for 1

case(p\_in)

2'b01:begin 2'b00:begin

state<=eight; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

state<=reset\_state; state<=reset\_state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b11:begin 2'b10:begin

state<=reset\_state; state<=two;

s\_out<=8'hff;end s\_out<=8'hff;end

endcase

2'b11:begin

ds2: //for ds2 state<=reset\_state;

case(p\_in) s\_out<=8'hff;end

2'b00:begin endcase

state<=state;

s\_out<=8'hff;end default: //for default

case(p\_in)

2'b01:begin 2'b00:begin

state<=nine; state<=state;

s\_out<=8'hff;end s\_out<=8'hff;end

2'b10:begin 2'b01:begin

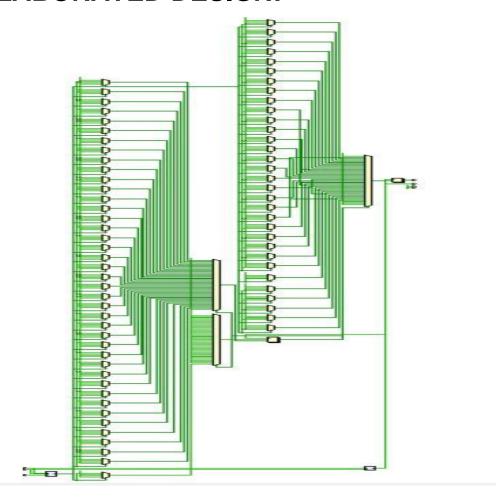
state<=zero; state<=reset\_state;

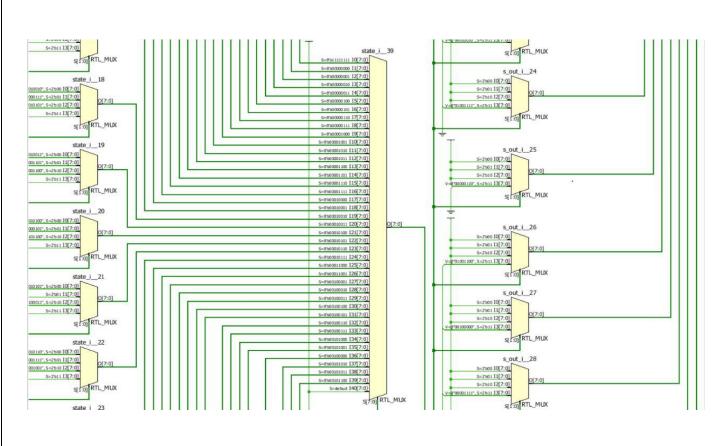
s\_out<=8'hff;end s\_out<=8'hff;end

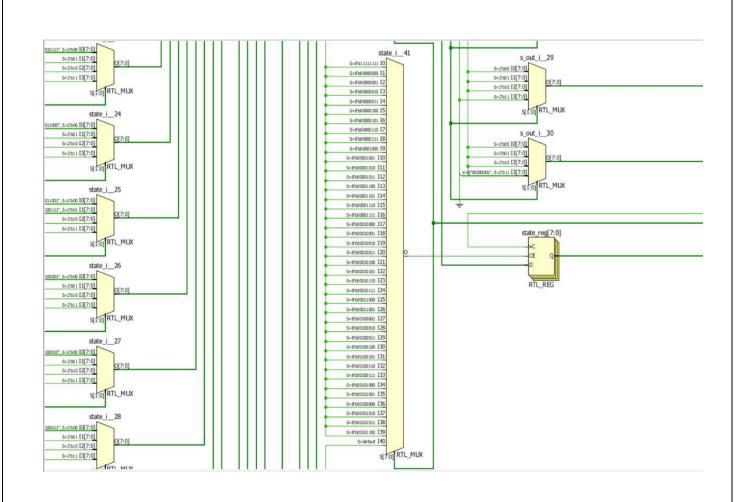
```
parameter [3:0] s8 = 4'b1000;
2'b10:begin
                                              clock_divider clk_trans(
state<=reset_state;
s_out<=8'hff;end
                                              .in_clk(clk),
                                              .rst(rst),
2'b11:begin
                                              .out_clk(mid_clk)
state<=reset_state;
                                              );
s_out<=8'hff;end
endcase
                                              always@(posedge mid_clk)
endcase
end
                                              begin
endmodule
                                              /*if(rst) //check
                                              parallel_out<=2'b00;*/
module
                  trans_fsm(serial_inp,
                                              case(state)
parallel_out, clk,rst);
                                              s0:
input serial_inp;
                                              begin
input clk;
                                              parallel_out<=2'b00;
input rst;
                                              if(~serial_inp)
                                              state<=s1:
wire mid_clk; //we changed this
                                              else
reg [3:0]state=4'b0000;
                                              state<=s0;
//reg [3:0]next_state=4'b0000;
                                              end
output reg [1:0]parallel_out;
                                              s1:
                                              begin
parameter [3:0] s0 = 4'b0000;
                                              parallel_out<=2'b00;
parameter [3:0] s1 = 4'b0001;
                                              if(~serial_inp)
parameter [3:0] s2 = 4'b0010;
                                              state<=s7;
parameter [3:0] s3 = 4'b0011;
                                              else
parameter [3:0] s4 = 4'b0100;
                                              state<=s2:
parameter [3:0] s5 = 4'b0101;
                                              end
parameter [3:0] s6 = 4'b0110;
parameter [3:0] s7 = 4'b0111;
```

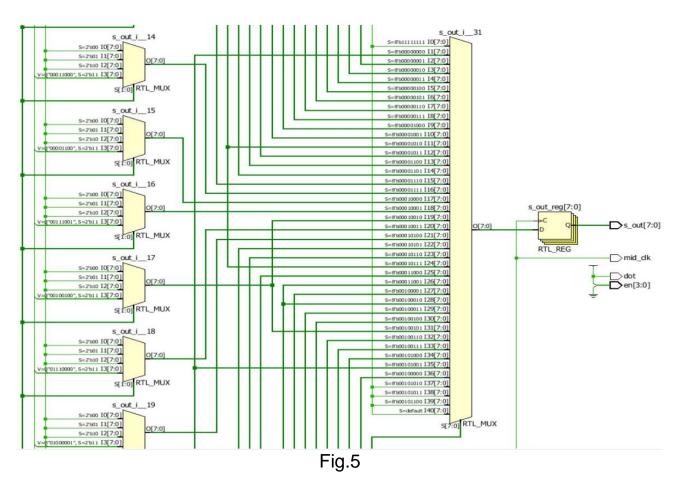
```
s2:
                                              end
begin
parallel_out<=2'b00;
                                              s6:
if(~serial_inp)
                                              begin
                                              parallel_out<=2'b10;
state<=s3;
else
                                              if(~serial_inp)
state<=s4;
                                              state<=s7;
end
                                              else
                                              state<=s2;
s3:
                                              end
begin
parallel_out<=2'b01;
                                              s7:
if(~serial_inp)
                                              begin
state<=s7;
                                              parallel_out<=2'b00;
                                              if(~serial_inp)
else
state<=s2;
                                              state<=s8;
end
                                              else
                                              state<=s2;
s4:
                                              end
begin
parallel_out<=2'b00;
                                              s8:
if(~serial_inp)
                                              begin
state<=s0;
                                              parallel_out<=2'b11;
else
                                              if(~serial_inp)
state<=s5;
                                              state<=s8;
end
                                              else
                                              state<=s2;
s5:
                                              end
begin
                                              endcase
parallel_out<=2'b00;
                                              end
                                              endmodule
if(~serial_inp)
state<=s6;
else
state<=s0;
```

#### **ELABORATED DESIGN:**









### **SIMULATIONS:**

#	Name	Value	0.000 ns	20.000 ns	40.000 ns
1	> 😽 in[15:0]	1111000000	(1111000000110001	1111000000010110	(1111000000011110
2	> 🕸 out[15:0]	1101010100	(11010000000000000000000000000000000000	1011011011011000	1010110110110000

#	Name	Value	80.000 ns	100.000 ns	1
1	> 😽 in[15:0]	1111000000	1111000000100110 1111000000100101	1111000001000110	χ
2	> 💖 out[15:0]	1101010100	1010101101100000 101010101000000	1101101101101000	χ

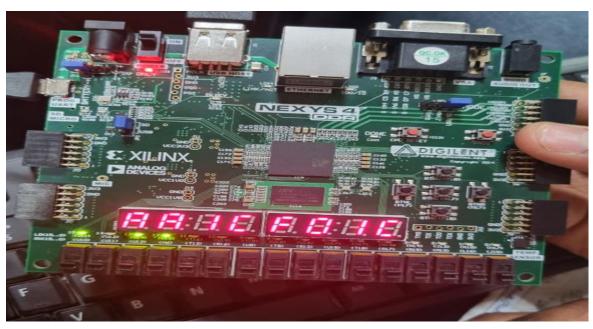
#	Name	Value	120.000 ns	130.000 ns	140.000 ns	150.000 ns
1	> 💗 in[15:0]	1111000000	X	1111000000010101	1	111000000011101
2	> 💖 out[15:0]	1101010100	X	1101101011000000	1	.0110110000000000

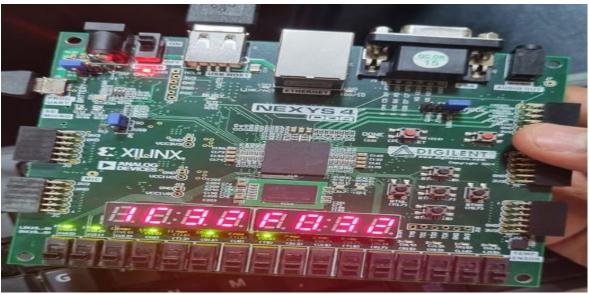
#	Name	Value	180.000 ns	200.000 ns	220.000 ns
1	> 😽 in[15:0]	1111000000	X1111000000101101	1111000000111100	X1111000001000011
2	> 🐶 out[15:0]	1101010100	10110100000000000	1010110000000000	(10100000000000000)

#	Name	Value	360.000 ns	380.000 ns	400.000 ns
1	> 😽 in[15:0]	1111000000	X11110000000	11110000001	X11110000001X
2	> 🐶 out[15:0]	1101010100	X10101000000	11010100000	X10101101000X

#	Name	Value	450.000 ns	500.000 ns
1	> 😽 in[15:0]	1111000000	X11110X11110X11110.	\(11110\)
2	> 💖 out[15:0]	1101010100	X10101X11011X11011.	\(11010\)

### **FPGA IMPLEMENTATION:**







### **5.APPLICATIONS:**

Here are some applications of Morse code detectors:

- 1. Amateur radio communication: Morse code is still widely used by amateur radio operators around the world. Morse code detectors can be used to decode signals sent by these operators.
- 2. Military and emergency communication: Morse code is a standard means of communication in military and emergency situations. Morse code detectors can be used by soldiers and emergency responders to decode distress signals and other important messages.
- 3. Morse code training: Morse code is still taught in some schools and used by some organizations for communication. Morse code detectors can be used by students and trainees to practice and improve their skills.
- 4. Historical research: Morse code has a rich history, and some researchers may use Morse code detectors to decode historical messages sent through telegraph or other means.
- 5. Electronic experimentation: Morse code detectors can be used by electronics enthusiasts to experiment with radio communication and Morse code encoding and decoding.

Overall, Morse code detectors have a wide range of applications in various fields, including communication, emergency response, education, research, and experimentation.

### **6.CONCLUSION:**

In conclusion, Morse code detectors play an important role in various fields such as amateur radio communication, military and emergency communication, Morse code training, historical research, and electronic experimentation. They allow users to decode Morse code signals sent over the radio or through other means of communication, helping to ensure that important messages are properly received and understood. Morse code may be an old form of communication, but it still has practical uses today, and Morse code detectors continue to be valuable tools for those who need to communicate using this system.

### **7.REFERENCES:**

Here are some references related to Morse code detectors:

- 1. "Morse code decoders and related technologies" by Dr. J.N. Pearce, Radio Science Bulletin, No. 322, June 2007.
- 2. "A CW Decoder Using the Goertzel Algorithm" by John L. Melton, QST Magazine, January 2001.
- 3. "A Compact and Simple Morse Code Decoder" by Owen Duffy, Electronics Australia Magazine, January 2006.
- 4. "Arduino-Based Morse Code Decoder" by Javier Galán, Ham Radio Magazine, January 2016.
- 5. "Design and Implementation of a Morse Code Decoder Using FPGA" by Karim Mohamed and Ahmed Zahran, Journal of Signal and Information Processing, Vol. 4, No. 3, August 2013.

These references provide information about different types of Morse code detectors, their design and implementation, and their applications. They can be helpful for researchers, engineers, and enthusiasts interested in understanding and building Morse code detectors.