DAH Quiz.

range : 40 MeV -> 2.0 TeV => 2.0 ×10 = 40 ×10 = 50,000 40 x10 goes 5 x10 times in to 2.0 x10 -. If 40 x10 Was I bit it would take logz (5x104) bits to cover the logz (5x10) = 15.669 round up to next integer => 16 It would take 16 bits.

2. Pc F8 574 AN expander 1/0 chip.

0x38 => (0,0,0)

(0,0,0)=0, (1,1,1) would have a value of 7. (difference of 8)

(38)16 = (56)10 56+8 = (63) 10 = (3F)16

[The slave address would be 0x3F]

Hexadecivel numbers workin base 16 and introduce letters to represent numbers in base 10.

| baselo | bace 16 Hexadedmel |
|----------------------|-----------------------|
| Decinal | |
| のしているとののなるの | 0 |
| 7 | _ |
| _ | ~ |
| 3 | 3 |
| 4 | 4 |
| 5 | 5 |
| <u>6</u> | 6 |
| * | 7 |
| 8 | 8 |
| 9 | 9 |
| 10 | <u> </u> |
| 11 | ß |
| 12 | C |
| 11 12 13 14 | D |
| 14 | E |
| 15 | f |
| 15 | 23456729ABCDEF10 |
| • | ; |
| | • |

A PCF 8574AN Chiz requires 2 of the response R's 4Plo I'c pins. The response is spoots 7 bit addresses which leeds to 128 (27) unique addresses This allows for a raspourry ? to condrol 128 PCF8574AN chips.