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SPECIFICATION

CUSTOMER :	
MODULE NO.:	WF43QTIBEDBNB#
	3
APPROVED BY:	
(FOR CUSTOMER USE ONLY)	
	PCB VERSION: DATA:

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
			葉虹蘭

ISSUED DATE: 2016/10/04



MODLE NO:

REC	ORDS OF REV	ISION	DOC. FIRST ISSUE
VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2016/09/21		First issue
A	2016/10/04		Modify Summary.

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1.Module Classification Information

W F Q T I Е D В N 43 В В 1 2 (3) 4 (5) 6 7 8 9 (10) (11) (12) (13)

1	Brand: WINSTAR	DISPLAY COR	PORATION							
2	Display Type : F→	TFT Type, J→Cı	ıstom TFT							
3	Display Size: 4.3" TFT									
4	Model serials no.									
(5)	Backlight Type:	F→CCFL, Whit	te	T→LED, V	Vhite	~ (
		S→LED, High	→LED, High Light White							
6	LCD Polarize	C→Transmissiv	→Transmissive, N. T, 6:00 ; I→Transmissive, W. T, 6:00							
	Type/ Temperature	F→Transmissiv	e, N.T,12:00 ; I	L→Transmis	ssive, W.T,12:0	00				
	range/ Gray Scale	N→Transmissiv	ve, Super W.T, 6	6:00	70					
	Inversion Direction	Q→Transmissiv	ve, Super W.T, 1	12:00						
		X→Transmissiv	ve, W.T, VATF	Γ						
		V→Transmissiv	ve, Super W.T, V	VA TFT						
		R→Transmissiv	ve, Super W.T, O	O-TFT	, ,					
		Z→Transmissiv	e, W.T, O-TFT		/					
		A→Transmissiv	e, N.T, IPS TF	T						
		Y→Transmissiv	ve, W.T, IPS TF	T						
7	A: TFT LCD		17	G: TFT-	+FR					
	B: TFT+FR+CON'	TROL BOARD	() Y.	H: TFT-	+D/V BOAR	D				
	C:TFT+FR+A/DI			I: TFT+	FR+D/V BO	ARD				
	D:TFT+FR+A/DI	BOARD+CONT	ROL BOARD	J: TFT+	POWER BD					
	E: TFT+FR+POW		,							
	F: TFT+CONTRO	L BOARD								
8	Resolution:		T	1	T					
	A: 128160 B:3202	234 C:320240	D:480234	E:480272	F: 640480	G: 800480				
	H:1024600 I:32048	80 J:240320	K:800600	L:240400	M:1024768	P:1280800				
	S:480128 T:8003	20								
9	D: Digital L: LV	/DS								
10	Interface: N:w	ithout control bo	ard A:8Bit	B: 16Bi	t					
11)	TS: N: Without T	S T: resistive	e touch panel	C: capa	citive touch pa	nel (G-F-F)				
	G: capacitive	touch panel(G-G	j)							
12	Version									
13	Special Code	#:Fit in with RC	OHS directive re	gulations						
<u> </u>	<u> </u>	l .								

2.Summary

TFT 4.3" is a TN transmissive type color active matrix TFT liquid crystal display that use amorphous silicon TFT as switching devices. This module is a composed of a TFT_LCD module, It is usually designed for indusrial application and this module follows RoHs,



WF43QTIBEDBNB#

3.General Specifications

Item	Dimension	Unit
Size	4.3	inch
Dot Matrix	480 x RGBx272(TFT)	dots
Module dimension	106.7 x 83.98 x 7.1	mm
Active area	95.04 x 53.856	mm
Dot pitch	0.066 x 0.198	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	12 o'clock	
Gray Scale Inversion Direction	6 o'clock	
Aspect Ratio	16:9	
Backlight Type	LED, Normally White	
Controller IC	SSD1963	
Interface	Digital 8080 family MPU 8bit/16bit	
With /Without TP	Without TP	
Surface	Glare	

^{*}Color tone slight changed by temperature and driving voltage.

4.Absolute Maximum Ratings

Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	TOP	-20	_	+70	$^{\circ}\! \mathbb{C}$
Storage Temperature	TST	-30	_	+80	$^{\circ}\!\mathbb{C}$

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. $\leq 60^{\circ}$ C, 90% RH MAX. Temp. $> 60^{\circ}$ C, Absolute humidity shall be less than 90% RH at 60° C

5.Electrical Characteristics

5.1. Operating conditions: (CON3.Pin1=GND, Pin2=VDD)

Item	Symbol	Condition	Min	Тур	Max	Unit	Remark
Supply Voltage For LCM	VDD	_	3.0	3.1	3.3	V	-
Supply Current For LCM	IDD	_	_	200	300	mA	Note1

Note 1: This value is test for VDD =3.3V, Ta=25°C only

5.2. Backlight driving conditions (CON3.Pin33,34=VLED-, Pin35,36=VLED+)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Operation Current For LED Driver	VLED+=3.3V	270	-	405	mA	Note 1,2
Power Consumption	VLED+=3.3V	891	-	1337	mW	Note 1,2
Supply Voltage For LED Driver	VLED+	3.3	-	5	V	Note 1,2
LED Life Time		-	50,000	-	Hr	Note 2,3,4

Note 1: Base on VLED= 3.3V for the back light driver IC specification

Note 2 : Ta = 25 °C

Note 3: Brightness to be decreased to 50% of the initial value

Note 4: The single LED lamp case

6.DC CHARATERISTICS

Parameter	Symbol		Rating	Unit	Condition	
Tarameter	Symbol	Min	Min Typ Max		Omt	Condition
Low level input voltage	VIL	0	-	0.3VDD	V	
High level input voltage	V _{IH}	0.7VDD	-	VDD	V	

7.Interface timing

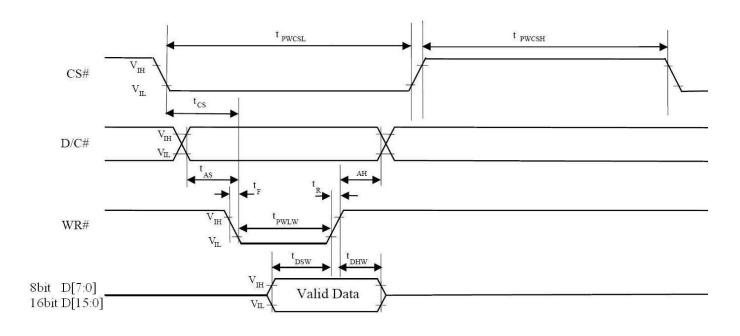
7.1. 8080 Mode 8bit/16bit

The 8080 mode MCU interface consist of CS#, D/C#, RD#, WR#, Data Bus signals. This interface use WR# to define a write cycle and RD# for read cycle. If the WR# goes low when the CS# signal is low, the data or command will be latched into the system at the rising edge of WR#. Similarly, the read cycle will start when RD# goes low and end at the rising edge of RD#.

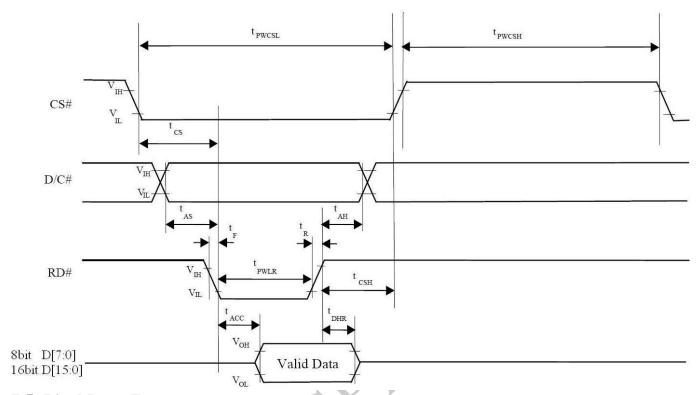
7.2. 8080 Mode Write Cycle

Symbol	Parameter	Min	Тур	Max	Unit
fMCLK	System Clock Frequency	1	-	110	MHz
tMCLK	System Clock Period	1/ fMCLK	-	, - X	ns
tPWCSH	Control Pulse High Width Write	13	1.5* tMCLK		ns
ti vvesii	Read	30	3.5* tMCLK		
DIVICAL	Control Pulse Low Width Write (next write cycle)	13	1.5* tMCLK	,	
tPWCSL	Write (next read cycle) Read	80 80	9* tMCLK 9* tMCLK	• 5	ns
tAS	Address Setup Time	1		-	ns
tAH	Address Hold Time	2 🗸	-	-	ns
tDSW	Write Data Setup Time	4			ns
tDHW	Write Data Hold Time	1	-	-	ns
tPWLW	Write Low Time	12			ns
tDHR	Read Data Hold Time	1	-	-	ns
tACC	Access Time	32			ns
tPWLR	Read Low Time	36	-	-	ns
tR	Rise Time	-		0.5	ns
tF	Fall Time	-	-	0.5	ns
tCS	Chip select setup time	2		-	ns
tCSH	Chip select hold time to read signal	3	-	_	ns

7.3. Parallel 8080-series Interface Timing Diagram(Write Cycle)



7.4. Parallel 8080-series Interface Timing Diagram(Read Cycle)



7.5. Pixel Data Format

Interface	Cycle	D[15]	D[14]	D[13]	D[12]	D[11]	D[10]	D[9]	D[8]	D[7]	D[6]	D[5]	D[4]	D[3]	D[2]	D[1]	D[0]
16 bits (565 format)	1 st	R5	R4	R3	R2	R1	G5	G4	G	G2	G1	G0	B5	B4	В3	B2	B1
340	1 st	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0
16 bits	2 nd	B7	B6	B5	B4	ВЗ	B2	B1	В0	R7	R6	R5	R4	R3	R2	R1	R0
	3 rd	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	В3	B2	B1	В0
	1 st									R7	R6	R5	R4	R3	R2	R1	R0
8 bits	2 nd									G7	G6	G5	G4	G3	G2	G1	G0
	3 rd									B7	B6	B5	B4	ВЗ	B2	B1	В0

8.Optical Characteristics

Item		Symbol	Condition.	Min	Тур.	Max.	Unit	Remark
Response time		Tr+ Tf	$\theta = 0^{\circ}$, $\Phi = 0^{\circ}$	-	30	45	.ms	Note 3
Contrast ratio		CR	At optimized viewing angle	250	350	-	-	Note 4
Color Chromaticity	White	Wx	$\theta = 0^{\circ} , \Phi = 0$	0.28	0.30	0.33		Note 2.5
Color Chromaticity	wille	Wy	$V=0$, $\Psi=0$	0.31	0.33	0.36		Note 2,5
***	Hor.	ΘR		55	65	-		
Viewing angle (Gray Scale Inversion	HOI.	Θ Γ	CD > 10	55	65	-	Dog	Note 1
Direction)	Ver.	ΦТ	CR ≥ 10	45	55	-	Deg.	Note 1
Direction)	ver.	ΦВ		45	55	-		
Brightness			-	400	500	1	cd/m ²	Center of display

 $Ta{=}25{\pm}2^{\circ}\!\!\mathrm{C}$, VLED /ILED = 3.3V /270mA

Note 1: Definition of viewing angle range

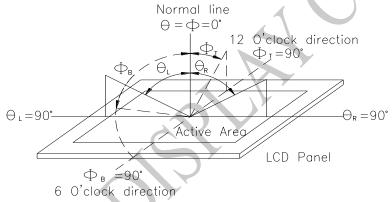


Fig. 8.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 or BM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

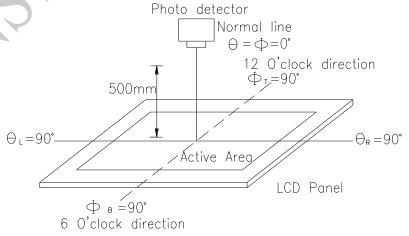
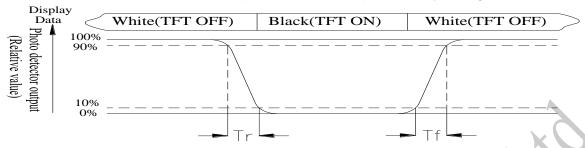


Fig. 8.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time, Tr, is the time between photo detector output intensity changed from 90% to 10%. And fall time, Tf, is the time between photo detector output intensity changed from 10% to 90%



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

Contrast ratio (CR) = Luminance measured when LCD on the "White" state

Luminance measured when LCD on the "Black" state

Note 5: White $Vi = V_{i50} \pm 1.5V$

Black $Vi = V_{i50} \pm 2.0V$

"±" means that the analog input signal swings in phase with VCOM signal.

"±" means that the analog input signal swings out of phase with VCOM signal.

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

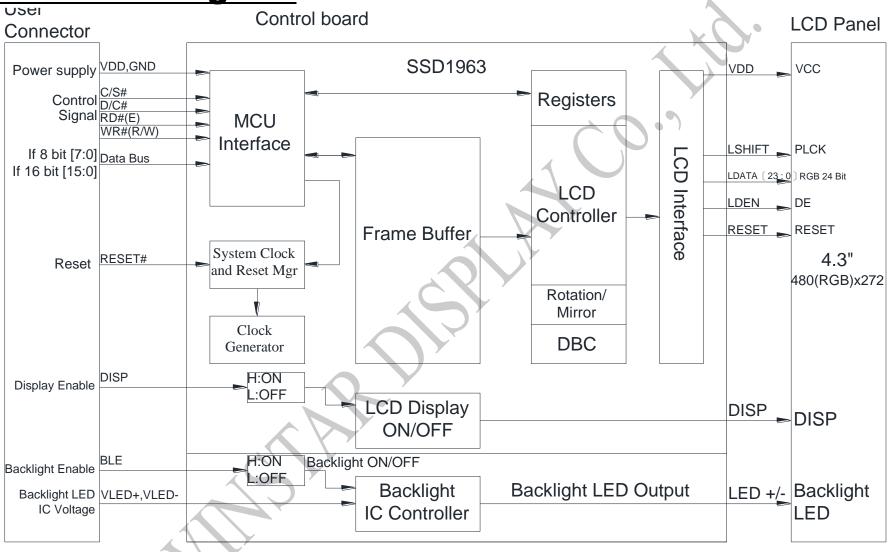
9.Interface

9.1. LCM PIN Definition (CON3)

Pin	Symbol	Function	Remark
1	GND	System round pin of the IC.	
		Connect to system ground.	
2	VDD	Power Supply: +3.3V	
3	BL_E	Backlight control signal, H: On \ L: Off	
4	D/C	Data/Command select	A.
5	WR	Write strobe signal	
6	RD	Read strobe signal	KUY
7	DB0	Data bus	
8	DB1	Data bus	
9	DB2	Data bus	7
10	DB3	Data bus	
11	DB4	Data bus	
12	DB5	Data bus	
13	DB6	Data bus	
14	DB7	Data bus	
15	DB8	Data bus (When select 8bits mode, this pin is NC)	Note1
16	DB9	Data bus (When select 8bits mode, this pin is NC)	Note1
17	DB10	Data bus (When select 8bits mode, this pin is NC)	Note1
18	DB11	Data bus (When select 8bits mode, this pin is NC)	Note1
19	DB12	Data bus (When select 8bits mode, this pin is NC)	Note1
20	DB13	Data bus (When select 8bits mode, this pin is NC)	Note1
21	DB14	Data bus (When select 8bits mode, this pin is NC)	Note1
22	DB15	Data bus (When select 8bits mode, this pin is NC)	Note1
23	NC	No connection	
24	NC	No connection	
25	CS	Chip select	
26	RESET	Hardware reset	
27	DIP ON	Display control H: On \ L:Off	
28	NC	No connection	
29	NC	No connection	
30	NC	No connection	
31	NC	No connection	
32	NC	No connection	
33	VLED-	VLED- for B/L LED inverter (GND)	
34	VLED-	VLED- for B/L LED inverter (GND)	
35	VLED+	VLED+ for B/L LED inverter (+3.3V)	
36	VLED+	VLED+ for B/L LED inverter (+3.3V)	

Note1: When select 8bit mode, DB0~DB7 be used, DB8~DB15 no connect When select 16bit mode, DB0~DB15 be used

10.Block Diagram



11.Reliability

Content of Reliability Test (Wide temperature, -20°C ~70°C)

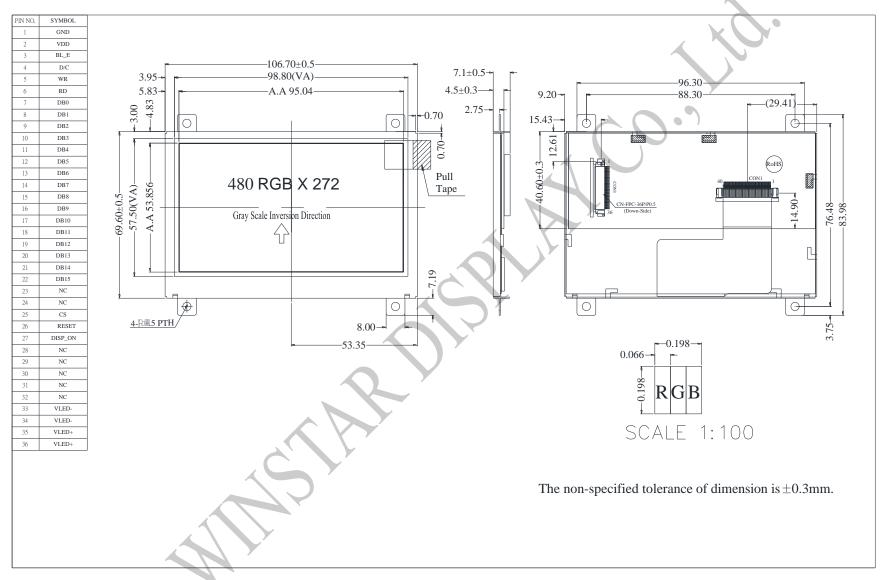
Environmental Test					
Test Item	Content of Test	Test Condition	Note		
High Temperature	Endurance test applying the high storage	80°C	2		
storage	temperature for a long time.	200hrs			
Low Temperature	Endurance test applying the low storage	-30°C ▲	1,2		
storage	temperature for a long time.	200hrs			
High Temperature	Endurance test applying the electric stress	70°C	X		
Operation	(Voltage & Current) and the thermal stress to the element for a long time.	200hrs			
Low Temperature	Endurance test applying the electric stress under	-20°C	1		
Operation	low temperature for a long time.	200hrs			
High Temperature/	The module should be allowed to stand at 60	60°C,90%RH	1,2		
Humidity Operation	°C,90% RH max	96hrs			
Thermal shock	The sample should be allowed stand the	-20°C/70°C			
resistance	following 10 cycles of operation -20°C 25°C 70°C	10 cycles			
	30min 5min 30min 1 cycle				
Vibration test	Endurance test applying the vibration during	Total fixed	3		
	transportation and using.	amplitude : 1.5mm			
		Vibration Frequency: 10~55Hz			
	O Y	One cycle 60 seconds			
		to 3 directions of			
		X,Y,Z for Each 15 minutes			
Static electricity test	Endurance test applying the electric stress to the	VS=±600V(contact),			
4	terminal.	±800v(air),			
	\vee	RS=330 Ω			
	<u> </u>	CS=150pF			
		10 times			

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

12.Contour Drawing



13.Initial Code For Reference

```
void Initial code()
         Write Command(0x01):
         Delay_ms(10);
         Write_Command(0xe0);
         Write_Parameter(0x01);
         Delay_ms(50);
         Write Command(0xe0);
         Write Parameter(0x03);
         Delay_ms(5);
         Write_Command(0xb0);
         Write Parameter(0x08):
         Write_Parameter(0x80);
         Write_Parameter(0x01);
         Write Parameter(0xdf);
         Write Parameter(0x01):
         Write Parameter(0x0f);
         Write Parameter(0x00);
         Write_Command(0xf0);
         Write Parameter(0x03); //0x03 is 16bit(565 format);0x00 is for 8-bit,pixel data format
         //Set the MN of PLL
         Write Command(0xe2);
         Write_Parameter(0x1d);
         Write Parameter(0x02);
         Write Parameter(0x54);
         Write Command(0xe6);
         Write_Parameter(0x01);
         Write Parameter(0xd2);
         Write Parameter(0xcd):
         //Set front porch and back porch
         Write Command(0xb4);
         Write Parameter(0x02):
         Write Parameter(0x0d):
         Write Parameter(0x00);
         Write Parameter(0x14):
         Write Parameter(0x05);
         Write Parameter(0x00):
         Write_Parameter(0x00);
         Write Parameter(0x00):
```

```
Write_Command(0xb6);
         Write_Parameter(0x01);
         Write_Parameter(0x24);
         Write_Parameter(0x00);
         Write_Parameter(0x0a);
         Write_Parameter(0x05);
         Write_Parameter(0x00);
         Write_Parameter(0x00);
         Write_Command(0x2a);
         Write_Parameter(0x00);
         Write_Parameter(0x00);
         Write_Parameter(0x01);
         Write_Parameter(0xdf);
         Write_Command(0x2b);
         Write_Parameter(0x00);
         Write_Parameter(0x00);
         Write_Parameter(0x01);
         Write_Parameter(0x0f);
         Write_Command(0x29);
         Write_Command(0x2c);
}
```

	winstar <u>LCM Sam</u> Module Number:		Feedback Sheet	Page: 1			
1 · Panel Specification :							
1.	Panel Type:	□ Pass	□ NG ,				
2.	View Direction:	□ Pass	□ NG ,				
3.	Numbers of Dots:	□ Pass	□ NG ,				
4.	View Area:	□ Pass	□ NG ,				
5.	Active Area:	□ Pass	□ NG ,	A			
6.	Operating Temperature :	□ Pass	□ NG ,	KO.			
7.	Storage Temperature :	□ Pass	□ NG ,				
8.	Others:						
2 \ <u>M</u>	echanical Specification :			*			
1.	PCB Size:	□ Pass	□ NG ,				
2.	Frame Size :	□ Pass	□ NG ,				
3.	Material of Frame:	□ Pass	□ NG ,				
4.	Connector Position:	□ Pass	□ NG ,				
5.	Fix Hole Position:	□ Pass	□ NG ,				
6.	Backlight Position:	□ Pass	□ NG ,				
7.	Thickness of PCB:	□ Pass	□ NG ,				
8.	Height of Frame to PCB:	□ Pass	□ NG ,				
9.	Height of Module:	□ Pass	□ NG ,				
10.	Others:	□ Pass	□ NG ,				
3 · <u>R</u>	elative Hole Size :	Y					
1.	Pitch of Connector:	□ Pass	□ NG ,				
2.	Hole size of Connector :	□ Pass	□ NG ,				
3.	Mounting Hole size:	□ Pass	□ NG ,				
4.	Mounting Hole Type:	□ Pass	□ NG ,				
5.	Others:	□ Pass	□ NG ,				
4 · Backlight Specification :							
1. I	B/L Type:	□ Pass	□ NG ,				
	B/L Color:	□ Pass	□ NG ,				
3. B/L Driving Voltage (Reference for LED Type) : □ Pass □ NG ,							
	· ·	□ Pass	□ NG ,				
	Brightness of B/L:	□ Pass	□ NG ,				
6. I	B/L Solder Method:	□ Pass	□ NG ,				
7. (Others:	□ Pass	□ NG ,				
>> Go to page 2 <<							

	Winstar Module Number	:		Page: 2		
5、	Electronic Characteristics			J		
1.	Input Voltage:	□ Pass	□ NG ,			
2.	Supply Current:	□ Pass	□ NG ,			
3.	Driving Voltage for LCD:	□ Pass	□ NG ,			
4.	Contrast for LCD:	□ Pass	□ NG ,	A		
5.	B/L Driving Method:	□ Pass	□ NG ,	× 0, °		
6.	Negative Voltage Output:	□ Pass	□ NG ,			
7.	Interface Function:	□ Pass	□ NG ,			
8.	LCD Uniformity:	□ Pass	□ NG ,			
9.	ESD test:	□ Pass	□ NG ,			
10.	Others:	□ Pass	□ NG ,			
6、	Summary :					
Sales signature :						
Customer Signature: <u>Date: / /</u>						