

## ANALOG POWER

The diagram illustrates the Analog Power section, featuring two identical voltage regulators, U1 and U2, each converting a +5V input to a 3V3 output.

**Regulator U1 (Top):**

- IC:** LP5907MFX-3.3/NOPB
- Input:** +5V (connected to pin 1, IN)
- Ground:** GND (connected to pin 2, GND)
- Enable:** EN (connected to pin 3, EN)
- Output:** OUT (connected to pin 5, OUT, providing 3V3\_1)
- Pin 4:** N/C (Not Connected, marked with a green X)
- Capacitors:** C19 (1uF input capacitor), C10 (1uF output capacitor)
- Diode:** D4 (B5819W\_C8598) is connected in parallel with the output.

**Regulator U2 (Bottom):**

- IC:** LP5907MFX-3.3/NOPB
- Input:** +5V (connected to pin 1, IN)
- Ground:** GND (connected to pin 2, GND)
- Enable:** EN (connected to pin 3, EN)
- Output:** OUT (connected to pin 5, OUT, providing 3V3\_2)
- Pin 4:** N/C (Not Connected, marked with a green X)
- Capacitors:** C15 (1uF input capacitor), C11 (1uF output capacitor)
- Diode:** D1 (B5819W\_C8598) is connected in parallel with the output.

# DIGITAL POWER

The diagram illustrates a digital power supply circuit. It starts with a +5V input connected to the VIN pin of the AMS1117-3.3 voltage regulator (U8). The regulator's GND pin is connected to the common ground. The VOUT pin of the regulator is connected to the output of the circuit, which is also connected to the LED3 (3V3) through a 10K resistor (R6). The output is decoupled by capacitors C28 (10uF) and C29 (100nF). The input is decoupled by capacitors C30 (100nF), C31 (100nF), and C32 (10uF). The circuit is labeled with various components and test points: C30, C31, C32, U8, R6, LED3, C28, C29, TP\_GVDD2, TP\_1V1, and GND.

Date: 2024-09-04 Drawn By: andriy@sonocotta.com

