

Sequential Circuits.

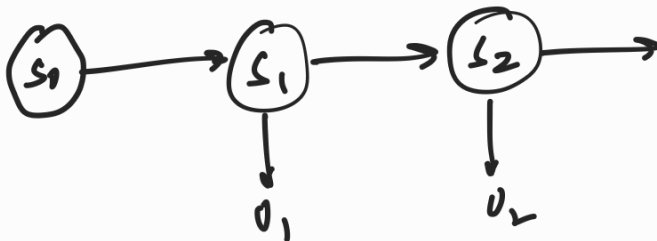
Address

Combinational circuit - "memory less"

State

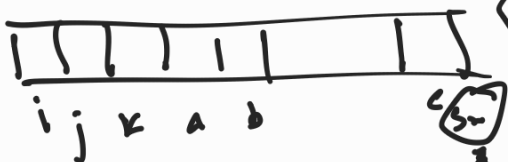
State transitions.

Memory

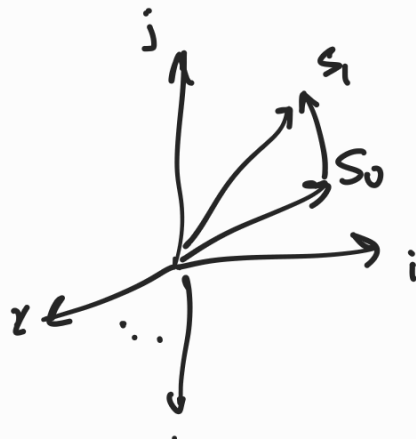
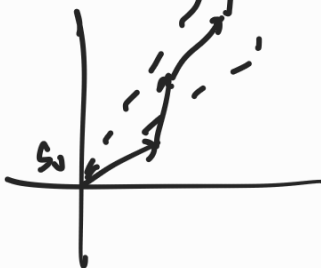


Counter

000
001
010
011
:
:
111



```
int i = 0;  
while (true)  
{  
    i = i + 1;  
}
```



Fetch - Decode - Execute

LOAD 6
opcode
RAM

