

# Sequential Circuits.

## Stored Program Concept -

Fetch - Decode - Execute

$\downarrow$   
 F   D   E  
   F   D   E  
     F   D   E

$\downarrow \downarrow$   
a = a + b ;

ADD  
JMP

PC

IR

IP FP  
Register

ADD

- Synchronization.
- Memory

Registers.

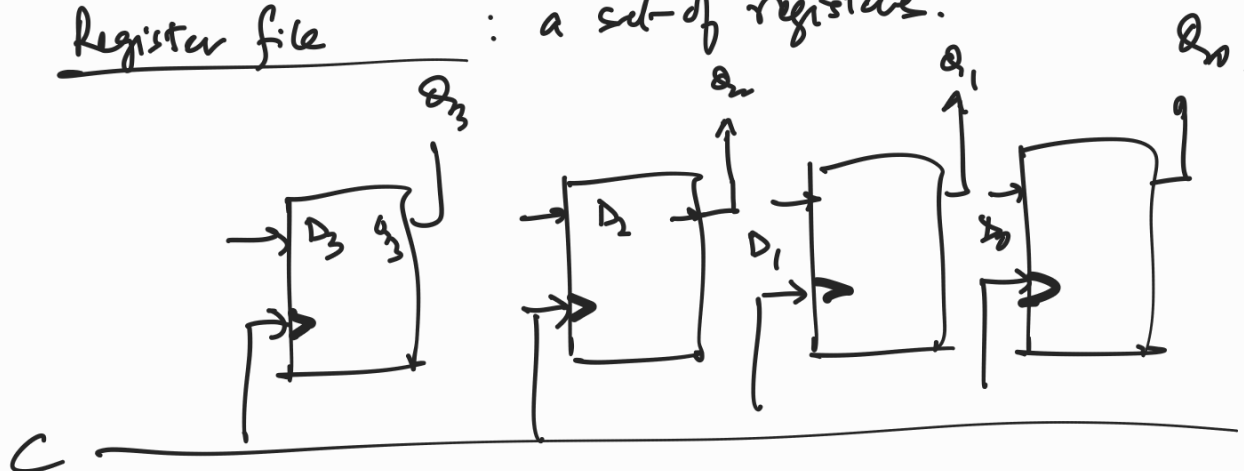
Counters.

n-bit register.

: n FFs.

Register file

: a set of registers.



Writing data. :  $\rightarrow$  0 1 1 0

$R_0, R_1, \dots, R_7$

