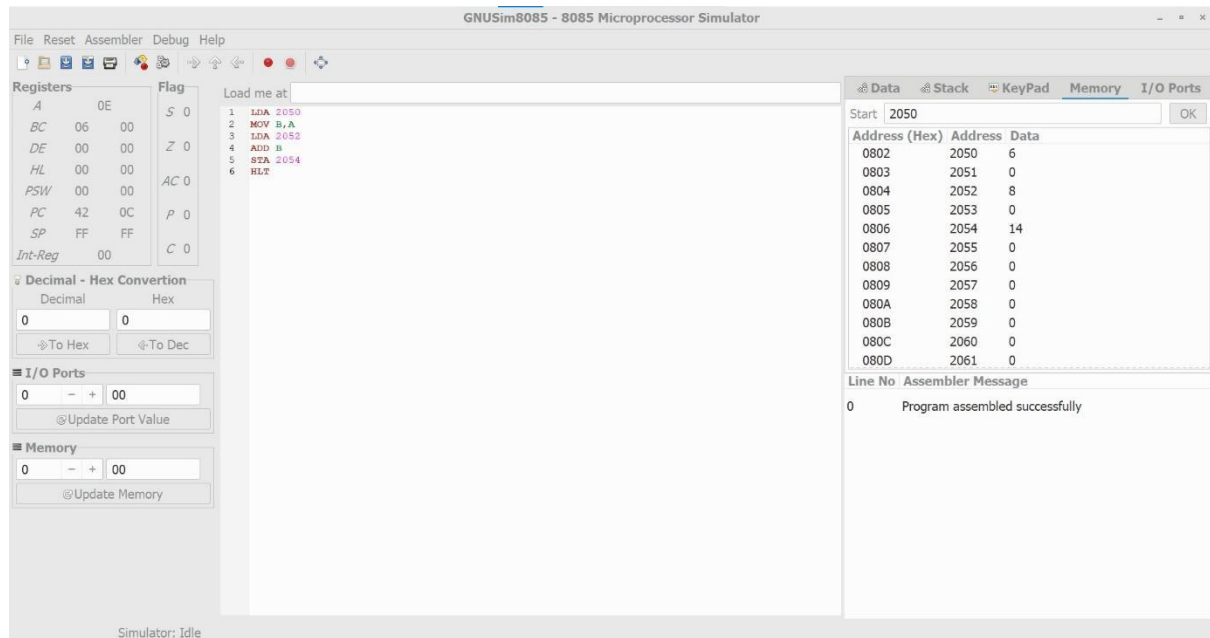
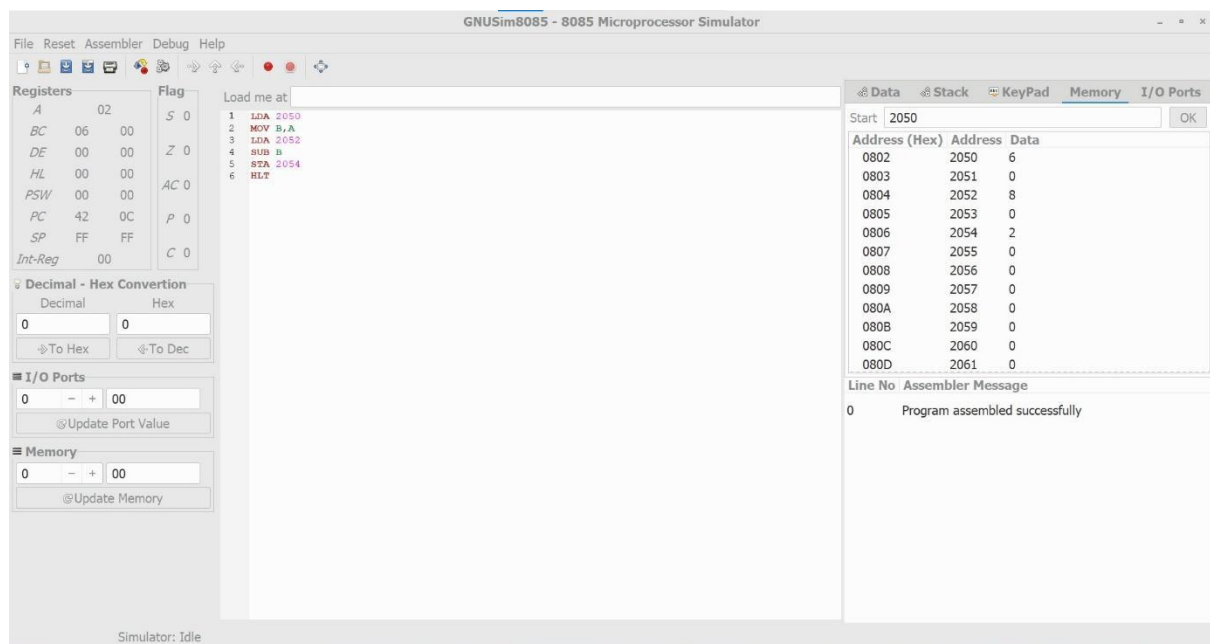


1) 8 BIT ADDITION



2) 8 BIT SUBTRACTION



3) 8 BIT MULTIPLICATION

GNUSim8085 - 8085 Microprocessor Simulator

File Reset Assembler Debug Help

Registers

Register	Value	Flag
A	48	S 0
BC	08 00	Z 1
DE	00 00	AC 0
HL	00 00	P 1
PSW	00 00	C 0
PC	42 13	
SP	FF FF	
Int-Reg	00	

Decimal - Hex Conversion

Decimal: 0 Hex: 0

I/O Ports

0 - + 00

Memory

0 - + 00

Load me at

```
1 lda 2001
2 mov b,a
3 lda 2005
4 mov c,a
5 mvi a,00h
6 loop: add b
7 dec c
8 jnz loop
9 sta 2008
10 hlt
```

Start: 2001

Address (Hex)	Address	Data
07D1	2001	8
07D2	2002	0
07D3	2003	0
07D4	2004	0
07D5	2005	9
07D6	2006	0
07D7	2007	0
07D8	2008	72
07D9	2009	0
07DA	2010	0
07DB	2011	0
07DC	2012	0

Line No Assembler Message

0 Program assembled successfully

Simulator: Idle

4) 8 BIT DIVISION

GNUSim8085 - 8085 Microprocessor Simulator

File Reset Assembler Debug Help

Registers

Register	Value	Flag
A	02	S 0
BC	02 04	Z 1
DE	00 00	AC 0
HL	00 00	P 1
PSW	00 00	C 0
PC	42 15	
SP	FF FF	
Int-Reg	00	

Decimal - Hex Conversion

Decimal: 0 Hex: 0

I/O Ports

0 - + 00

Memory

0 - + 00

Load me at

```
1 LDA 2001
2 MOV B,A
3 LDA 2005
4 MOV C,A
5 MOV A,B
6 MVI B,00H
7 LOOP: INR B
8 SUB C
9 JNE LOOP
10 MOV A,B
11 STA 2008
12 HLT
```

Start: 2001

Address (Hex)	Address	Data
07D1	2001	8
07D2	2002	0
07D3	2003	0
07D4	2004	0
07D5	2005	4
07D6	2006	0
07D7	2007	0
07D8	2008	2
07D9	2009	0
07DA	2010	0
07DB	2011	0
07DC	2012	0

Line No Assembler Message

0 Program assembled successfully

Simulator: Idle

