

CSIE 5452, Spring 2024: Homework 1

Due March 19 (Tuesday) at Noon

When you submit your homework, select the corresponding page(s) of each question. Points will be deducted if no appropriate intermediate step is provided.

1 Timing Analysis of the CAN Protocol: Part I (12pts)

Given a set of periodic messages μ_0, μ_1, μ_2 with their priorities, transmission times, and periods as follows:

Message	Priority (P_i)	Transmission Time (C_i) (msec)	Period (T_i) (msec)
μ_0	0	10	50
μ_1	1	30	200
μ_2	2	20	100

The worst-case response time R_i of μ_i can be computed as

$$R_i = Q_i + C_i, \quad (1)$$

and

$$Q_i = B_i + \sum_{\forall j, P_j < P_i} \left\lceil \frac{Q_i + \tau}{T_j} \right\rceil C_j, \quad (2)$$

where $\tau = 0.1$ in this question. You can consider using the following tables to help you.

- (4pts) What is the worst-case response time of μ_0 ?

Iteration	LHS (Q_0)	B_0	RHS	Stop?
1				

- (4pts) What is the worst-case response time of μ_1 ?

Iteration	LHS (Q_1)	B_1	j	$Q_1 + \tau$	T_j	$\left\lceil \frac{Q_1 + \tau}{T_j} \right\rceil$	C_j	RHS	Stop?
1			0						
2			0						

- (4pts) What is the worst-case response time of μ_2 ?

Iteration	LHS (Q_2)	B_2	j	$Q_2 + \tau$	T_j	$\left\lceil \frac{Q_2 + \tau}{T_j} \right\rceil$	C_j	RHS	Stop?
1			0						
			1						
2			0						
			1						
3			0						
			1						

2 Timing Analysis of the CAN Protocol: Part II (36pts)

Please download the benchmark “input.dat” from NTU COOL. In the benchmark, the first number is n , the number of messages. The second number is τ . Each of the following lines contains the priority (P_i), the transmission time (C_i), and the period (T_i) of each message. You are required to do two things in your submission:

1. You should print out n numbers (one number per line) representing the worst-case response time (R_i) of those messages. Note that you need to follow the message ordering in the benchmark, *e.g.*, the first number in the list is the worst-case response time of the first message in the benchmark.
2. You should also print out your source codes. (For your information, my implementation is less than 100 lines.) We may ask you to provide your source codes which must be the same as those on your printout. If the worst-case response times above are correct but the source codes are clearly wrong implementation, it is regarded as academic dishonesty.

It is highly recommended to write your codes well (*e.g.*, capable of dynamically allocating memory based on n) so that you can reuse them in Homework 2. Ideally, you can test your implementation with the small benchmark in Question 1 and verify its solution by your implementation. Just do not make the same mistake in Questions 1 and 2.

3 Timing Analysis of Preemptive Fixed-Priority Scheduling (16pts)

The CAN protocol is based on non-preemptive fixed-priority scheduling. For tasks on an Electronic Control Unit (ECU), they are usually scheduled by preemptive fixed-priority scheduling. The worst-case response time R_i of a task τ_i can be computed as

$$R_i = C_i + \sum_{\forall j, P_j < P_i} \left\lceil \frac{R_j}{T_j} \right\rceil C_j, \quad (3)$$

where P_i , C_i , and T_i are the priority, the computation (execution) time, and the period of τ_i , respectively. Given a set of periodic tasks τ_0, τ_1, τ_2 with their priorities, computation times, and periods as follows:

Task	Priority (P_i)	Computation Time (C_i) (msec)	Period (T_i) (msec)
τ_0	0	10	50
τ_1	1	30	200
τ_2	2	20	100

1. (4pts) What is the worst-case response time of τ_0 ?

Iteration	LHS (R_0)	C_0	RHS	Stop?
1				

2. (4pts) What is the worst-case response time of τ_1 ?

Iteration	LHS (R_1)	C_1	j	R_1	T_j	$\left\lceil \frac{R_1}{T_j} \right\rceil$	C_j	RHS	Stop?
1			0						
2			0						

3. (4pts) What is the worst-case response time of τ_2 ?

Iteration	LHS (R_2)	C_2	j	R_2	T_j	$\frac{R_2}{T_j}$	C_j	RHS	Stop?
1			0						
			1						
2			0						
			1						
3			0						
			1						

4. (4pts) Compared with non-preemptive fixed-priority scheduling, preemptive fixed-priority scheduling is expected to be disadvantageous to the lowest-priority message/task. Explain why the the worst-case response time of τ_2 is smaller than the worst-case response time of μ_2 in Question 1.

4 Timing Analysis of TDMA-Based Protocols (12pts)

Following the assumptions (each time slot has the same length, each time slot serves exactly one frame, and a frame is transmitted only if the whole time slot is available) in the lecture, please compute the worst-case response time of the “asynchronous” message with the frame arrival pattern (4, 10, 0, 3, 5, 6) and the schedule pattern (2, 5, 1, 2) by completing the following steps.

- (2pts) Please duplicate the schedule pattern (hint: (4, 10, 1, 2, ...)). No intermediate work is needed here.
- (2pts) Please duplicate the arriving times of frames in the frame arrival pattern but fix $m = 4$ and $p = 10$. No intermediate work is needed here.
- (2pts) Please duplicate the starting times of time slots in the schedule pattern but fix $n = 4$ and $q = 10$. No intermediate work is needed here.
- (4pts) Please complete the following table:

k	$\max_{1 \leq j \leq n}(s_{j+k} - s_j)$	=	$\min_{1 \leq i \leq m}(a_{i+k-1} - a_i)$	=	(Column-3) - (Column-5)
1	$\max_{1 \leq j \leq 4}(s_{j+1} - s_j)$		$\min_{1 \leq i \leq 4}(a_i - a_i)$		
2	$\max_{1 \leq j \leq 4}(s_{j+2} - s_j)$		$\min_{1 \leq i \leq 4}(a_{i+1} - a_i)$		
3	$\max_{1 \leq j \leq 4}(s_{j+3} - s_j)$		$\min_{1 \leq i \leq 4}(a_{i+2} - a_i)$		
4	$\max_{1 \leq j \leq 4}(s_{j+4} - s_j)$		$\min_{1 \leq i \leq 4}(a_{i+3} - a_i)$		

5. (2pts) Please compute the worst-case response time (which is waiting time plus transmission time) of the message.

5 MILP Linearization (12pts)

We will prove or make the following propositions are equivalent so that we can transform constraints to linear forms and thus apply the Mixed Integer Linear Programming (MILP). Note that “ \iff ” denotes “equivalence” and “ \wedge ” denotes “logical conjunction” (AND).

1. (4pts) Given α, β, γ which are binary variables, prove

$$\alpha + \beta + \gamma \neq 2 \iff \alpha + \beta - \gamma \leq 1 \wedge \alpha - \beta + \gamma \leq 1 \wedge -\alpha + \beta + \gamma \leq 1$$

by filling “T” (True) or “F” (False) in the following table (if LHS=RHS in all cases, then LHS and RHS are equivalent):

α	β	γ	LHS	$\alpha + \beta - \gamma \leq 1$	$\alpha - \beta + \gamma \leq 1$	$-\alpha + \beta + \gamma \leq 1$	RHS	LHS=RHS?
0	0	0						
0	0	1						
0	1	0						
0	1	1						
1	0	0						
1	0	1						
1	1	0						
1	1	1						

2. (4pts) Given α, β, γ which are binary variables, prove

$$\alpha\beta = \gamma \iff \alpha + \beta - 1 \leq \gamma \wedge \gamma \leq \alpha \wedge \gamma \leq \beta$$

by filling “T” (True) or “F” (False) in the following table (if LHS=RHS in all cases, then LHS and RHS are equivalent):

α	β	γ	LHS	$\alpha + \beta - 1 \leq \gamma$	$\gamma \leq \alpha$	$\gamma \leq \beta$	RHS	LHS=RHS?
0	0	0						
0	0	1						
0	1	0						
0	1	1						
1	0	0						
1	0	1						
1	1	0						
1	1	1						

3. (4pts) Given β which is a binary variable, x, y which are non-negative real variables, and a constraint $x \leq 2022$, select a value of M to guarantee

$$\beta x = y \iff 0 \leq y \leq x \wedge x - M(1 - \beta) \leq y \wedge y \leq M\beta,$$

where you can refer to the following table:

β	LHS	$0 \leq y \leq x$	$x - M(1 - \beta) \leq y$	$y \leq M\beta$	RHS
0	$0 = y$	$0 \leq y \leq x$	$x - M \leq y$	$y \leq 0$	$x - M \leq y = 0 \leq x$
1	$x = y$	$0 \leq y \leq x$	$x \leq y$	$y \leq M$	$0 \leq y = x \leq M$

6 Signal Packing (12pts)

Bit stuffing does not need to be considered in this problem, *i.e.*, you can assume that the length of a message is the length of its data field plus 44 plus 3. Note that the length of a data field must be 8, 16, 24, ..., or 64 bits, even if the message itself is shorter. Assume that there are 4 Electronic Control Units (ECUs), $\varepsilon_0, \varepsilon_1, \varepsilon_2, \varepsilon_3$, and 4 messages, $\mu_0, \mu_1, \mu_2, \mu_3$, as follows:

Message	Sender	Receiver(s)	Number of Bits (Data Field)	Period (msec)
μ_0	ε_0	ε_1	6	50
μ_1	ε_0	ε_1	10	50
μ_2	ε_1	$\varepsilon_2, \varepsilon_3$	10	50
μ_3	ε_0	ε_3	16	100

A system designer redesigns the messages as follows:

Message	Sender	Receiver(s)	Number of Bits (Data Field)	Period (msec)
μ'_0	ε_0	ε_1	16	50
μ_2	ε_1	$\varepsilon_2, \varepsilon_3$	10	50
μ_3	ε_0	ε_3	16	100

where the first 6 bits of μ'_0 are the bits from μ_0 and the following 10 bits of μ'_0 are the bits from μ_1 .

1. (4pts) Regarding the number of bits which need to be transmitted, do you think that the new design is better? Please explain.
2. (4pts) Can you further merge μ_2 into μ'_0 ?
3. (4pts) In most cases, it does not hurt to have more frequent messages, but it is not allowed to have less frequent messages. Following this policy, can you further improve the number of bits which need to be transmitted? Please explain.