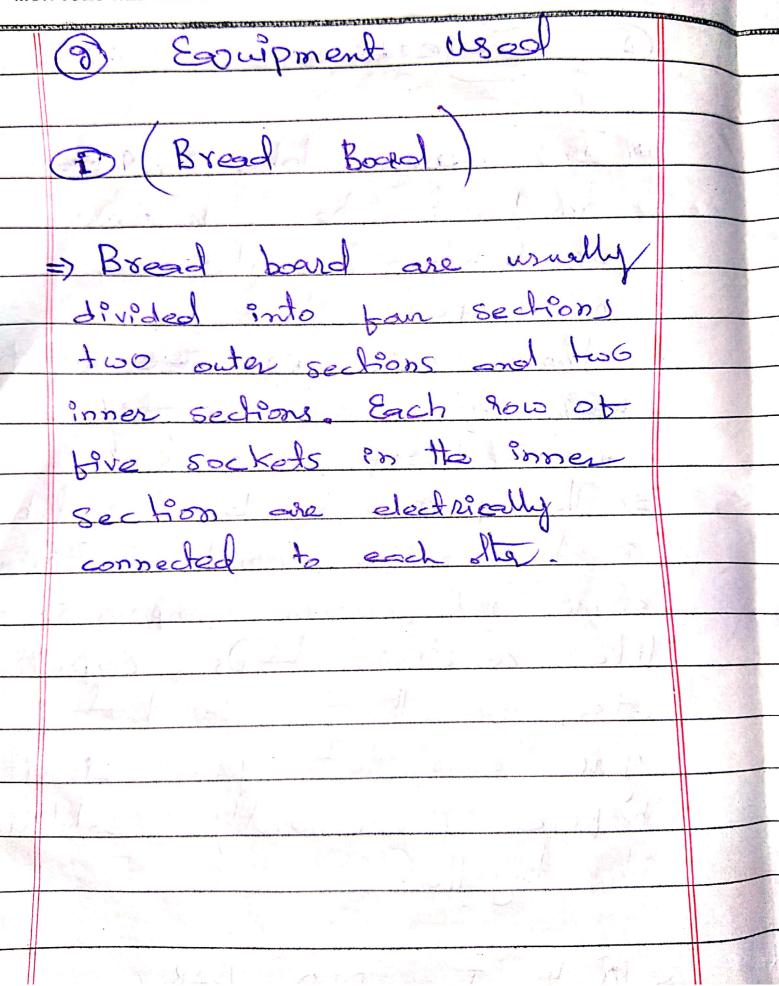


## MON TUES WED THUR FRI SAT

## Date:



	MON TUES WED THUR FRI SAT	Date:	
	(Dosign And)	Test Procedu	e)
	=> A breadboard ?	s a rectangu	lar
	plastic board	with a bunch	0
	Liny holes in i-	to These hole	25
	let you easily	Prisent alad	2 mor
	components to		
	(morning to bu		
	only Version of	) = n eledr	oni (
	elecuit extends	TO COMP	
		du du	
	Lord Lord		
ARCH ST. A. F.	The same of the sa		7.5

Results, =) In And gate 1 But 116 anuesfrom is asperny how can recognize gate y we have to hemember IC No in each gate => ok we performed a experiment on And gate Bread Board we se giving input in and getting out put and we were connect 7 with GND and one and connectery My with vec and when we were giving Low (0) End Low(0) From Both out puts it were giving Low (0) which was was green

## MON TUES WED THUR FRI SAT

Date:

		And				
l-				and the same		
	0		0-> green			
	0	1	0-> green			
page-man in the con-	1	0	0-> green 0-> green 1- des Red.			
	1	1	1 - de Red.			
province consignation for the con-						
Incoming the same						