| **Table 1. Design Objects** | |
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| **Design Objects** | **Description** |
| Block | Represents a segment of the design. You establish hierarchy by adding an instance of a block to another block. A block can be assigned as the root block to specify which block the design entry tool treats as the origin of the design. See “[Organize a Design with Hierarchy](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\DISWChap_OrganizeADesignWithHierarchy_id9756b452.html#id9756b452-328d-4243-9fb2-0f5fa63abcc2__DISWChap_OrganizeADesignWithHierarchy_id9756b452.xml#id9756b452-328d-4243-9fb2-0f5fa63abcc2)”.  A block enables you to break a design into more manageable sections. All designs are comprised of at least one block. You can implement a block either as a schematic (comprised of one or more schematic sheets) or an Interconnectivity Table (ICT). If your design contains multiple blocks, you can set one as root, which the design entry tool treats as the origin of the design.  The Navigator window displays schematic blocks with the schematic block icon () and ICT blocks with the ICT block icon (). Blocks that are not yet implemented as a schematic or an ICT, appear with the generic block icon (). |
| Board | Contains board-level design data. See “[Editing Multiple Board Projects](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\TaskTop_EditingMultipleBoardProjects_idbf37f32a.html#idbf37f32a-a477-4c14-aae4-b36595c781b8__TaskTop_EditingMultipleBoardProjects_idbf37f32a.xml#idbf37f32a-a477-4c14-aae4-b36595c781b8)”. |
| Border | An outline appearing around a sheet that includes corporate or custom information. See “[Controlling Sheet Borders](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\TaskTop_ControllingSheetBorders_id9a7a68c4.html#id9a7a68c4-85d3-4c66-b437-df42a7cfd741__TaskTop_ControllingSheetBorders_id9a7a68c4.xml#id9a7a68c4-85d3-4c66-b437-df42a7cfd741)”. |
| Bus | Represents a collection of nets on schematic sheets. See “[Connectivity With Buses](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\Contain_ConnectivityBuses_ideca392f9.html#ideca392f9-1064-46c9-a92e-d64c8daa0246__Contain_ConnectivityBuses_ideca392f9.xml#ideca392f9-1064-46c9-a92e-d64c8daa0246)”. See also Net ripper in this table. |
| Cell | Represents the footprint or land pattern used to attach a component to a PCB. Your librarian links a cell to a symbol with an part object in the central library. See “[Capture a Schematic Design With Symbols](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\DISWChap_CaptureASchematicDesignWithSymbols_id423c0be2.html)”. |
| Component | Represents an electrical or electromechanical device that is added or assembled onto a printed circuit board (PCB) as part of the product manufacturing process. Components may represent any part that is added, such as nuts and bolts, that are included in a bill of materials but not in a layout netlist. See “[Capture a Schematic Design With Symbols](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\DISWChap_CaptureASchematicDesignWithSymbols_id423c0be2.html)”. See also Special component and Part. |
| Connector | A specific type of part that carries signals from and to boards. |
| Graphical objects | Drawing elements that include arcs, rectangles, circles, and lines that you can place on a symbol or a schematic sheet. See “[Creating and Editing Graphical Objects on a Sheet](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\TaskTop_CreatingEditingGraphicalObjectsOnSheet_idce52de72.html#idce52de72-df85-4750-91b8-fda7a3562b95__TaskTop_CreatingEditingGraphicalObjectsOnSheet_idce52de72.xml#idce52de72-df85-4750-91b8-fda7a3562b95)”. |
| Group | Specifies a collection of objects. For example, you can assign properties to all symbols in a group at once. |
| HDL Design | Specifies properties and files related to HDL simulation. See “[Digital Block Simulation Process Overview](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\Contain_DigitalBlockSimulationProcessOverview_id067e41b1.html#id067e41b1-6b30-4926-bc9f-be7a72a02d87__Contain_DigitalBlockSimulationProcessOverview_id067e41b1.xml#id067e41b1-6b30-4926-bc9f-be7a72a02d87)”. |
| Interconnectivity Table (ICT) | A type of design block that defines pin/net connectivity in a tabular form. You create and edit ICTs with the Xpedition Designer interconnect editor. See “[Create a Table-Based Design](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\DISWChap_CreateATableBasedDesign_id7e8cee2c.html#id7e8cee2c-08c8-452c-aaaa-1af0f3c2cb71__DISWChap_CreateATableBasedDesign_id7e8cee2c.xml#id7e8cee2c-08c8-452c-aaaa-1af0f3c2cb71)”. |
| Library partition | Represent a collection of objects in a library. See “[Search for Electronic Component Information](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\DISWChap_SearchForElectronicComponentInformation_id8ac7606d.html)”. |
| Net | Represents connectivity between components. A logical representation of physical traces on a PCB. Nets are represented in a schematic as lines between component pins. See “[Connectivity With Nets](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\Contain_ConnectivityWithNets_ide0478475.html#ide0478475-0566-4fcc-90ee-cebd636b66df__Contain_ConnectivityWithNets_ide0478475.xml#ide0478475-0566-4fcc-90ee-cebd636b66df)”. See also Signal in this table. |
| Net ripper | Represents connectivity between a net and its associated signal in a bus on schematic sheets. See “[Connectivity With Buses](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\Contain_ConnectivityBuses_ideca392f9.html#ideca392f9-1064-46c9-a92e-d64c8daa0246__Contain_ConnectivityBuses_ideca392f9.xml#ideca392f9-1064-46c9-a92e-d64c8daa0246)”. See also Bus in this table. |
| Part | Links a cell and symbol. A part is synonymous with a component, however, components are understood to include additional parametric data necessary for procurement and PCB manufacturing such as an ordering part number. See “[Search for Electronic Component Information](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\DISWChap_SearchForElectronicComponentInformation_id8ac7606d.html)”. |
| Pin | Represents an interface to a component or a block. |
| Port | A port creates connectivity from a child block to an instance of the block in a parent block. See also Block. |
| Project | Contains all design data. You can have only one project open in the Xpedition Designer tool at a time. |
| Property | Represents information about a design object. See “[Adding a Property to Design Objects](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\TaskTop_AddingAPropertyToDesignObjects_id5fa4e483.html#id5fa4e483-fb37-4227-9e0c-03dce1579370__TaskTop_AddingAPropertyToDesignObjects_id5fa4e483.xml#id5fa4e483-fb37-4227-9e0c-03dce1579370)” and “[Property Characteristics](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_ref\topics\DISWChap_PropertyCharacteristics_idf9cd6087.html)” in the *Xpedition Designer and Xpedition System Designer Reference*. |
| Schematic | A type of design block comprised of one or more sheets that defines pin/net connectivity in a diagram form. See also Sheet and “[Capture a Schematic Design With Symbols](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\DISWChap_CaptureASchematicDesignWithSymbols_id423c0be2.html)”. |
| Sheet | A single page of a schematic that you view and edit graphically. A sheet can contain net, buses, symbols, blocks, properties, and graphical objects. See also “[Manipulating Sheets](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\TaskTop_ManipulatingSheets_id4738a720.html)”. |
| Signal | Represents the information conveyed by connected elements. A signal can traverse multiple nets and components. |
| Special component | Represents components that serve a special purpose such as power and ground, or off-page connectors, or ports. See “[Special Component Symbols](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\Concept_SymbolEditorOverview_id4498e415.html#id4498e415-5b24-421c-9459-4d6034966427__id486e4e21-e64a-4181-b5d6-3df76ab476d9)”. |
| Symbol | A graphical representation of a component or a block. A symbol can represent one or more components. For example, one resistor symbol can represent many different resistor components. High pin-count components, like connectors or FPGAs, may be segmented into multiple symbols. See “[Symbol Types](file:///D:\XpetitionV2.13\DOC\EEVX213docs\EEVX.2.13\docs\htmldocs\designer_gd\topics\Concept_SymbolEditorOverview_id4498e415.html#id4498e415-5b24-421c-9459-4d6034966427__id74688b56-fe89-437f-a3b9-f18b8598e957)”. See also Block, Component, and Special components. |