

P9					P8			
DGND	1	2	DGND		DGND	1	2	DGND
VDD_3_3	3	4	VDD_3V3		MMC1_DAT6	3	4	MMC1_DAT7
VDD_5V	5	6	VDD_5V		MMC1_DAT2	5	6	MMC1_DAT3
SYS_5V	7	8	SYS_5V		ET_ORQLK	7	8	ET_OGRLK
PWR_BUT	9	10	SYS_RESETN		ET_IRQLK	9	10	ET_IGRLK
DSP_PWRON	11	12	NW_IGRLK		SE_ORQLK	11	12	SW_TXDAT
EXT_PWR_EN	13	14	NE_ORQLK		SE_OGRLK	13	14	SE_IRQLK
DSP_DC	15	16	NE_OGRLK		SW_RXRDY	15	16	SE_RXDAT
SPI0_CS0	17	18	SPI0_D1		SE_IGRLK	17	18	SW_ORQLK
TP_IRQ	19	20	TP_CS		SW_OGRLK	19	20	MMC1_CMD
SPI0_D0	21	22	SPI0_SCLK		MMC1_CLK	21	22	MMC1_DAT5
NE_IRQLK	23	24	SW_RXDAT		MMC1_DAT4	23	24	MMC1_DAT1
SW_TXRDY	25	26	NE_IGRLK		MMC1_DAT0	25	26	SW_IRQLK
SE_TXRDY	27	28	ET_TXRDY		NW_TXRDY	27	28	NE_TXRDY
ET_RXDAT	29	30	SE_RXRDY		NW_TXDAT	29	30	NE_TXDAT
ET_RXRDY	31	32	VDD_ADC		SW_IGRLK	31	32	WT_ORQLK
AIN4	33	34	GNDA_ADC		WT_OGRLK	33	34	WT_IRQLK
LIGHT_A	35	36	USR_ACT_A		WT_IGRLK	35	36	NW_ORQLK
EDGTMP_A	37	38	AIN3		NW_OGRLK	37	38	NW_IRQLK
GRDVLTA	39	40	CTRTMP_A		WT_RXRDY	39	40	WT_RXDAT
SE_TXDAT	41	42	ET_TXDAT		NE_RXRDY	41	42	NE_RXDAT
DGND	43	44	DGND		NW_RXRDY	43	44	NW_RXDAT
DGND	45	46	DGND		WT_TXRDY	45	46	WT_TXDAT

## LEGEND

POWER/GROUND/RESET
ARM HW CONTROLLED
PRU SW CONTROLLED
LINUX SW CONTROLLED
UNUSED/RESERVED