T2 Tile Packet Header Formats

Version 17 Date 2020-05-25

VERSION 17

DATE 2020-05-25

Packet length is 1..255 bytes. Transport layer must handle byte and packet framing.

BYTE 0							BYTE 1					BYTE 2					BYTE 3						BYTE 4																		
-	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	

Packet header ¬

T2 packet

Non-standard 0 ASCII7 ?→

Standard 1 $\stackrel{\square}{\circ}$

Local standard 1 1 1 RS YO P4 3

Local standard packets are defined per source/destination

PRU→PRU Not used

PRU→LKM Page 3 ¬

LKM→PRU Page 3 ¬

LKM→MFM Page 3 ¬

MFM → *LKM* Page 3 ¬

Standard	1	LOCL	¬														
Routed standard	1	0	URG	OVR	ERR	SD2	SD1	SD0	? →								
Bulk	1	0	0	OVR	ERR	SD2	SD1	SD0	? →								
Urgent	1	0	1	OVR	ERR	SD2	SD1	SD0	MFM	マ							
Flash	1	0	1	OVR	ERR	SD2	SD1	SD0	0		Α	SC	117:0	Coc	le		?→
MFM	1	0	1	OVR	ERR	SD2	SD1	SD0	1	XITC2	XITC1	XITC0	⊸				
KITC	1	0	1	OVR	ERR ERR	SD2	SD1	SD0	1	0	0	0	SN3	SN2	SN1	ONS	?→
ITC	1	0	1	OVR	ERR	SD2	SD1	SD0	1	0	0	1	SN3	SN2 SN2	SN1 SN1	SNO	?→
Circuit signals	1	0	1	OVR	ERR	SD2	SD1	SD0	1	01	.0-1	11	⊸				
Ring (lock request)	1	0	1	OVR	ERR ERR	SD2	SD1	SD0	1	0	1	0	CN3	CN2	CN1	CNO	?→
Answer (lock ACK)	1	0	1	OVR	ERR	SD2	SD1	SD0	1	0	1	1	CN3	CN2 CN2	CN1 CN1	cno cno	?→
Busy (lock NAK)	1	0	1	OVR	ERR	SD2	SD1	SD0	1	1	0	0	CN3	CN2	CN1	CNO	?→
Flash (lock discard)	1	0	1	OVR	ERR	SD2	SD1	SD0	1	1	0	1	CN3	CN2	CN1	CNO	?→
Talk (cache updates)	1	0	1	OVR	ERR ERR	SD2	SD1	SD0	1	1	1	0	CN3	CN2	CN1	CNO	?→
Hang up (evt done)	1	0	1	OVR	ERR	SD2	SD1	SDO	1	1	1	1	CN3	CN2	CN1	CNO	?→

Local standard PRU→**LKM**

illegal	1	1	*	0	0	0	0	0	? →									
debug	1	1	*	0	0	0	0	1	? →									
value	1	1	*	0	0	0	1	0	? →						_			
control	1	1	*	0	0	0	1	1	0	ASCII7:Code	0	ASCII7:PRU	0	ASCII7:DIR	ĺ			
Packet sync	1	1	*	0	0	0	1	1	0	'P'	0	'0''1'	0	'0''2'	ĺ			
Frame error	1	1	*	0	0	0	1	1	0	'F'	0	'0''1'	0	'0''2'	ĺ			
Timeout	1	1	*	0	0	0	1	1	0	'T'	0	'0''1'	0	'0''2'				
Monitor	1	1	*	0	0	0	1	1	0	'M'	0	'0''1'	0	'3'	0 0	0 0	EN2 O	EN1
reserved	1	1	*	0	0	0	1	1	0	all other values	0	'0''1'	0	'0''3'	? →			
reserved	1	1	*	0	0	0	1	1	1	?→								
reserved	1	1	*	0	0	1	Χ	Х	? →									
reserved	1	1	*	0	1	Х	Х	Х	? →									
reserved	1	1	*	1	Χ	Х	Х	Х	? →									

Local standard LKM → PRU

reserved $\boxed{1 \ 1 \ * \ x \ x \ x \ x}? \rightarrow$

Note: LKM → PRU uses non-standard packets rather than STNDLOCL. See T2-12/pru/itcio/firmware/SpecialPackets.c for details

Local standard LKM → **MFM**

									_
illegal	1	1	*	0	0	0	0	0	? →
reserved	1	1	*	0	0	0	0	1	? →
reserved	1	1	*	0	0	0	1	Х	? →
reserved	1	1	*	0	0	1	Х	Х	? →
reserved	1	1	*	0	1	Х	Х	Х	? →
reserved	1	1	*	1	Х	Х	Х	Х	? →

Local standard MFM → **LKM**

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illegal	1	1	*	0	0	0	0	0	? →
reserved	1	1	*	0	0	0	0	1	? →
reserved	1	1	*	0	0	0	1	Х	? →
reserved	1	1	*	0	0	1	Χ	Х	? →
reserved	1	1	*	0	1	Х	Х	Х	? →
reserved	1	1	*	1	Х	Х	Х	Х	? →

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Abbreviations:	Code:	Meaning
	?	Optional data not defined by this spec
	*	Reserved, should ignore on read, should be 0 on write
	\rightarrow	For rest of packet
	→	Defined below
	ACT	Active, sender is performing an event
	CTL	Circuit control packet
	CNn	Circuit number, bit <i>n</i>
	ENn	Enable status bit, prudir <i>n</i>
	ERR	Error, corrupted packet
	ITC	Intertile Connection
	KITC	Kernel Intertile Connection
	LOCL	Local packet moving PRU ↔ ARM, but not PRU ↔ PRU
	MFM	Movable Feast Machine
	n	Bit index counting from least significant bit is 0
	OVR	Overrun, packet too long
	RSVn	Reserved, bit <i>n</i>
	SDn	Packet source direction on read, destination direction on write, bit n
	SNn	State number, bit <i>n</i>
	STND	Standard packet format, defined by this spec
	TYPn	Type code, bit <i>n</i>
	URG	Urgent
	Χ	0 or 1
	XITC	Extended ITC