VERSION 14

DATE 2020-05-11

Packet length is 1..255 bytes. Transport layer must handle byte and packet framing.

ASCII7:Code

'P'

'F'

'M'

E	BYTE 0				BYTE 1						BYTE 2					BYTE 3						BY	BYTE 4																		
	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	ļ

ASCII7:PRU

'0'..'1'

'0'..'1'

'0'..'1'

'0'..'1'

'0'..'1'

0

0

0

0

0

0

ASCII7:DIR

'0'..'2'

'0'..'2'

'0'..'2'

'3'

'0'..'3'

EN2 EN2 O O

? →

0

0

0

0

0

0

Packet header

T2 packet

Non-standard ASCII7

Standard

TYP3 Local standard illegal 0 0 0 0 0 debug 0 0 0 0

value control Packet sync

* 0 0 0 1 1 0 * 0 0 0 1 Frame error * 0 0 0 1 1 0 Timeout 1 1 * 0 0 0 1 1 0 Monitor 0 0 0 1 1 0 ..all other values.. reserved * 0 0 0 1 reserved reserved * 0 0 1 x x |?→ * 0 1 x x reserved

* 0 0 0 1 1 0

reserved 1 1 * 1 x x x x |?→

T2 Tile Packet Head	Version			14 Da		te	202	20-0	05-	11							
Standard	1	LOCL	⊸														
Routed standard	1	0	URG	OVR	ERR	SD2	SD1	SDO	? →								
Bulk	1	0	0	OVROVR	ERRERR	SD2 SD2	SD1	SDO	? →	•							
Urgent	1	0	1	OVROVR	ERR ERR	SD2 SD2	SD1 SD1 SD1 SD1	SD0 SD0 SD0 SD0	MFM	→							_
Flash	1	0	1	OVR	ERR	SD2	SD1	SDO	0		Α	SC	117:	Coc	de		?→
MFM	1	0	1	OVR	ERR	SD2	SD1	SD0	1	XITC2	XITC1	XITCO	¬				
KITC	1	0	1	OVR	ERR	SD2	SD1	SD0	1	0	0	0	SN3	SN2	SN1	SNO	?→
Circuit	1	0	1	OVR	ERR	SD2	SD1	SD0	1	00	1-1	.00]¬				
Ring	1	0	1	OVR	ERR	SD2 SD2	SD1	SDO SDO	1	0	0	1	CN3	CN2	CN1	CNO	?→
Hang up	1	0	1	OVR OVR OVR OVR	ERR ERR ERR ERR	SD2 SD2	SD1 SD1 SD1 SD1	SD0	1	0	1	0	CN3	CN2 CN2 CN2 CN2	CN1 CN1 CN1 CN1	cno cno cno cno	?→
Cache updates	1	0	1	OVR	ERR	SD2	SD1	SD0	1	0	1	1	CN3	CN2	CN1	CNO	?→
Cache upd ack	1	0	1	OVR	ERR	SD2	SD1	SD0	1	1	0	0	CN3	CN2	CN1	CNO	?→
ITC	1	0	1	OVR	ERR	SD2	SD1	SD0	1	10	1-1	.11]¬				
Reserved active	1	0	1	OVR	ERR	SD2	SD1	SD0	1	1	0	1	CN3	CN2	CN1	CNO	?→
Reserved passive	1	0	1	OVR OVR OVR OVR	ERR ERR ERR	SD2 SD2	SD1 SD1 SD1 SD1	SD0	1	1	1	0	CN3 CN3	CN2 CN2 CD2	CN1 CN1 CD1	CNO CNO CDO	?→
ITC command	1	0	1	OVR	ERR	SD2	SD1	SD0	1	1	1	1	CD3	CD2	CD1	CD0	?→

Abbreviations: Code: Meaning

Optional data not defined by this spec Reserved, should ignore on read, should be 0 on write

T2 Tile Packet Header Formats Version 14 Date 2020-05-11

→ For rest of packet

→ Defined below

ACT Active, sender is performing an event

CTL Circuit control packet
CNn Circuit number, bit *n*

CDn Command number, bit *n*

ENn Enable status bit, prudir n

ERR Error, corrupted packet ITC Intertile Connection

KITC Kernel Intertile Connection

LOCL Local packet moving PRU → ARM, but not PRU → PRU

MFM Movable Feast Machine

n Bit index counting from least significant bit is 0

OVR Overrun, packet too long

RSVn Reserved, bit *n*

SDn Packet source direction on read, destination direction on write, bit *n*

SNn State number, bit *n*

STND Standard packet format, defined by this spec

TYPn Type code, bit *n*

URG Urgent x 0 or 1

XITC Extended ITC