

**SAJAL DOGRA**  
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Education: **NEW YORK UNIVERSITY** New York, NY  
**Leonard N. Stern School of Business**  
Master of Business Administration, May 2014  
Specialization in Finance, Accounting

- Won first prize in the 2012 Stern Stock Research Report Competition
- Selected to represent Stern at the 2012 UVA Darden Investing Challenge (DVIC)
- Analyst, Michael Price Student Investment Fund - Growth Fund
- Finalist, Benjamin Graham Fellowship Investment Challenge

**UNIVERSITY OF WISCONSIN-MADISON** Madison, WI  
Master of Science, Electrical Engineering, August 2005

- Received Graduate Assistant scholarship from University of Wisconsin-Madison, 2003-2005

**INDIAN INSTITUTE OF TECHNOLOGY MADRAS** Chennai, India  
Bachelor of Technology, Electrical Engineering, August 2003

- Secured All India Rank 364 out of 150,000 candidates (top 0.33%), IIT Joint Entrance Exam

Experience: **ADVANCED MICRO DEVICES (AMD)** Boxborough, MA  
2012 **Senior Design Engineer**

- Managed and led a team of four engineers that analyzed system level memory and bandwidth features for the next generation version of the world's largest selling gaming console, reducing memory issues by 70%
- Defined and wrote technical documentation on chip security specifications to mitigate hacking risk

2011–2012 **STANDARD MICROSYSTEMS (acquired by MICROCHIP)** Hauppauge, NY  
**Senior Design Engineer**

- Architected and designed innovative adapters between high speed wired technologies like the Apple Thunderbolt, USB 3, HDMI and Ethernet; doubled data speeds and reduced power consumption by 40%
- Managed and developed new product test flow, reducing production test development and time to market by three months; this solution was successfully adopted for a company-wide roll out
- Conducted feasibility studies, market research and documented technical ideas on high speed technology interfaces; recommendations resulted in a new audio codec to USB 3 chip

2007–2011 **QUALCOMM** San Diego, CA  
**Design Engineer**

- Developed industry defining 3G and 4G wireless chips, designing on-chip bus systems, camera interface, display controller and contributing to the product test engineering efforts
- Conducted due diligence on proprietary synthesis techniques and power saving methodologies, pitching competitive analysis results to top engineering executives inside Qualcomm; the recommendations resulted in Qualcomm acquiring a 50-member technology company
- Successfully led an eleven member product test engineering bring up effort for wired connectivity blocks on Qualcomm's first 4G modem, enabling Qualcomm to achieve a critical first mover advantage in 4G
- Led a four member hardware design and validation team; designed a novel non-blocking system-on-chip bus system, achieving a 300% improvement in best-case throughput

2005–2007 **CIRRUS LOGIC** Austin, TX  
**Design Engineer**

- Designed mixed-signal smart-grid power meter and audio codec chips for iPods
- Researched competitor solutions and reverse engineered the specifications for an LCD driver system
- Managed and led the design and software driver development team of the LCD controller
- Demonstrated customer board prototypes to upper management and customers, defining and incorporating specific customer and marketing feedback into the final product

Additional:

- Successfully passed all three CFA level exams in 18 months
- Project coordinator for AID San Diego; awarded volunteer organization of the year in 2008 by Qualcomm; organized and participated in various fund raising events in different capacities
- Blogged about technology trends and investment ideas; cited by EETimes, WSJ, Morningstar, Reuters
- Ran two half-marathons; 1800-level USATT table tennis player; silver medalist at IIT Madras and SMSC