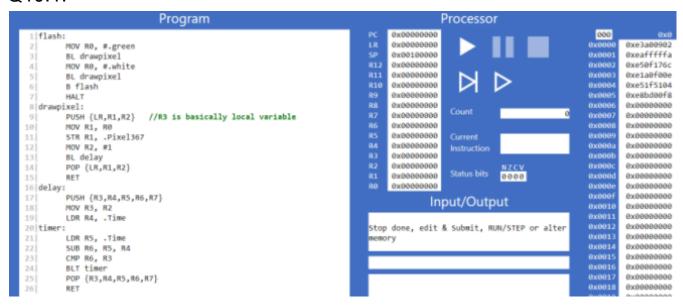
COS-10004 (Computer Systems)

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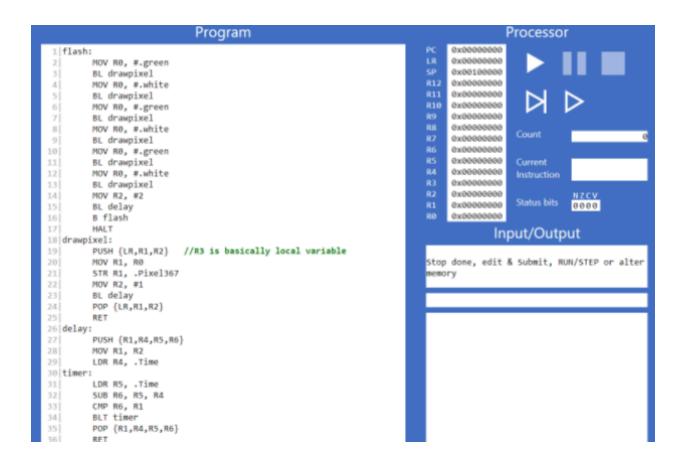
Q10.1:



Q10.1 (c):

The above code uses indirect addressing since it may store the exact value of R2 in [R4], containing a memory location and its value. In the code, 4 bytes are added to the pixel each time until it reaches 80, and data is moved to the memory location (4 bytes, 32 bits). R1 is the base address; if we add a value in R3, it will be added to R1, which holds the address and may be used to create a new pointer to the subsequent pixel.

Q10.2:



Q10.3:

