Floating Point Numbers

CSC03B3



Outline



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IEEE Representation



IEEE Single Precision Floating Point Representation

Floating point numbers have a particular representation in the 80x86 architecture.

- Single precision floating point numbers relate to a float in C++ (32-bits)
- Double precision floating point numbers relate to a **double** in **C++** (64-bits)

Single precision floating point numbers have the following format:

S	Е	E	E	E	E	E	E	E	F		F
31	30	29	28	27	26	25	24	23	22		0
S 01 bit Sign bit											
E	08 bit	s Exp	oonent								
F	23 bit	s Ma	ntissa								

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IEEE Single Precision Floating Point Conversion

Conversion from base 10 to IEEE Single Precision Representation

Convert 78.375 to binary:

2 ⁶	2 ⁵	2 ⁴	2 ³	2 ²	2 ¹	2 ⁰	2 ⁻¹	2 ⁻²	2 ⁻³
1	0	0	1	1	1	0	0	1	1

The number must then be represented in Scientific Notation:

 $1001110.011_2 = 1.001110011_2 \times 2^6$

Why 6? because we moved the decimal 6 positions!

Now you piece it together based on a few simple rules:

S

O Positive number 0, negative number 1

10000101 Exponent of 2 (6) + 127 (bias value) in binary

F 001110011000000000000000 Digits after decimal point padded with zeros SEEE EEEE EFFF FFFF FFFF FFFF FFFF

4 2 9 C C 0 0 0₁₆

 $0x429CC000_{16}$ - 0x is part of the convention used.

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Floating Point Unit



Floating Point Unit

- The Floating Point Unit (FPU) on the CPU that allows for floating point calculations. It is independent from the rest of the CPU.
- Floating point number in IEEE representation or integers can be transferred to the FPU.
- If an integer is sent to the FPU it is translated into a floating point number.
- Operations can then be performed on these floating point numbers.

FPU consists of:

- Eight (8) registers
- 80 bits long (32-bit architecture)
- ST0, ST1, ST2, ST3, ST4, ST5, ST6, ST7

The FPU registers are arranged in a STACK, and they are accessed in the same way that a stack is accessed. When you put something into the FPU, you place it onto a the register stack When you remove something from the FPU, you remove it from the register stack You don't need to reference the actual FPU registers by name. ST0 is the top of the stack and ST7 is the bottom of the stack.

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FPU Instructions

FL* load instructions:

- **FLD** *memory(real)*
- FILD memory(int)
- FBLD memory(BCD)
- FLD ST(num)
- FLD1
- FLDZ
- FLDPI

FS* store instructions:

- **FSTP** *memory(real)*
- **FST** *memory(real)*
- FST ST(num)
- **FIST** *memory(int)*

Miscellaneous

- FINIT
- FADD
- FSUB
- FMUL
- FDIV
- FSIN
- FCOS
- FTAN

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Loading items into the FPU

FL* instructions:

FLD *memory(real)* Push a real value from memory onto the FPU stack.

FILD *memory(int)* Push an integer value from memory onto the FPU stack.

FBLD *memory(BCD)* Push a Binary Coded Decimal (BCD) value from memory onto the FPU stack.

FLD *ST(num)* Push a value from **St(num)** register onto the stack.

FLD1 Push 1 onto the FPU stack

FLDZ Push 0 onto the FPU stack

FLDPI Push π onto the FPU stack

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Getting items from the FPU

FS* instructions:

FSTP *memory(real)* Pop the value from the top of the FPU stack.

FST *memory(real)* Copy the value off the top of the FPU stack.

FST *ST(num)* Copy the value from **ST0** and place it in **ST(num)**.

FIST *memory(int)* Copy the value from the top of the stack and convert it into an integer in memory.

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FPU Operations

Any trigonometric functions work with radians!

FINIT Initialize the FPU. Only need to call this once per program

FADD Pop ST0 and ST1, add them together and push the result to the stack

FSUB Pop **ST0** and **ST1**, subtract them and push the result to the stack

FMUL Pop ST0 and ST1, multiply them and push the result to the stack

FDIV Pop **ST0** and **ST1**, divide them and push the result to the stack

FSIN Pop **ST0** and push the sine of the value popped

FCOS Pop STO and push the cosine of the value popped

FTAN Pop STO and push the tangent of the value popped

Many more operations not listed (see textbook, chapter 7 for more)

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Simple Example

```
.DATA
1
   ; REAL4 represents a C-type FLOAT
       value1 REAL4 3.1415
3
      value2 REAL4 1.0
4
      result RFAL4 0.0
5
   . CODE
   start:
    : FINIT initialises the FPU
    finit
9
     : Push VALUE1 onto the FPU
10
    fld value1
11
     : Push VALUE2 onto the FPU
12
    fld value2
13
    fadd
14
     ; Fetch the result and store in RESULT
15
    fst result
16
     ; Display the result on the screen
17
     push result
18
     call
           OutputFloat
19
```

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Full Example I

```
. 386
   .MODEL FLAT
  INCLUDE in inc
4 ExitProcess PROTO NEAR32 stdcall, dwExitCode:DWORD
   .STACK 4096
   . DATA
              BYTE 10, 0 : newline for formatting
      fTemp REAL4 ? ; floating point variable
   .CODE
   ; formula (9/5 * C) + 32
   convert PROC NEAR32
    ; code on next slide
   _convert ENDP
   start:
    : Create the stack frame
    PUSH ehn
    MOV ebp. esp
    ; call convert(27)
    PUSH 27
    CALL convert
    : Display eax, eax has integer answer
    PUSH eax
    CALL OutputInt
    : NewLine to separate values
    LEA ebx, nl
    PUSH ebx
    CALL OutputStr
    ; Display ftemp, fTemp has floating point answer
    PUSH fTemp
    CALL OutputFloat
    : Newline to separate values
    LEA
          ebx, nl
    PUSH ebx
    CALL OutputStr
    ; Destroy the stack frame
    MOV esp, ebp
    POP
           ebp
    : Exit
    push
    call
           ExitProcess
  PUBLIC _start
43 END
```

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Full Example II

```
11: int convert(celcius) - formula (9/5 * C) + 32
2 | convert PROC NEAR32
   : Entry code
    PUSH ehn
    MOV
           ehn, esn
          esp, 16
                     ; 4 Local DWORDS
    PUSH ebx
    PUSH ecx
    PUSH edx
    PUSHED
11
    ; Parameters
    : [ebp+ 8] - celcius - 4 bytes
   ; Local variables
    : No names needed as we just using them for the conversion
           [ebp- 4], DWORD PTR 9
           [ebp- 8], DWORD PTR 5
    MOV
           [ebp-12], DWORD PTR 32
    MOV
           Lebn-161, DWORD PTR 0
                                   ; to save integer answer
10
28
    ; Initialise floating point unit
21
    ETNTT
22
23
    : Load values onto FPU in the correct order
    ; Order is important as the FPU oprates with a stack
    FILD DWORD PTR [ebp-12] : 32
    FILD DWORD PTR [ebp+ 8] : C
    FILD DWORD PTR [ebp- 41 : 9
    FILD DWORD PTR [ebp- 8] : 5
    : Calculation - keep track of values in comments to make easier
    FDIV : 9/5
    FMUL : (9/5) * C
    FADD : (9/5) * C + 32
    : Save answers
    FIST DWORD PTR [ebp-16] ; get integer answer back
    FSTP fTemp
                              : save floating point answer in global
                               : eax has integer answer
          eax, [ebp-16]
38
    ; Exit code
    POPED
    POP
           edx
    POP
           ecx
    POP
           ehx
    MOV
           esn. ehn
    POP
           ehn
    RET
           4
                      : narams are 4 hytes
47 | convert ENDP
```

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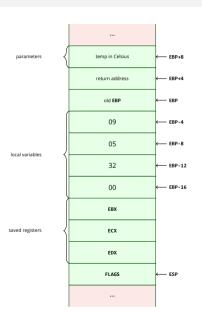
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Full Example III



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