Tabela de códigos de função (funct) e códigos de operação (OpCode) das principais instruções do MIPS

Arithm / Logical Instructions			Comparison Instructions	
Instruction	**(funct)**		**Instruction**	**(OpCode)**
add	100000 (0x20)		slti	001010 (0x0A)
addu	100001 (0x21)		sltiu	001001 (0x09)
and	100100 (0x24)			
div	011010 (0x1A)			
divu	011011 (0x1B)			
mult	011000 (0x18)		**Branch Instructions**	
multu	011001 (0x19)		beq	000100 (0x04)
nor	100111 (0x27)		bne	000101 (0x05)
or	100101 (0x25)		bgtz	000111 (0x07)
sll	000000 (0x00)		bgez	000001 (0x01)[1]
sra	000011 (0x03)		bltz	000001 (0x01)
srl	000010 (0x02)		blez	000110 (0x06)
sub	100010 (0x22)			
subu	100011 (0x23)		**Jump Instructions**	
xor	100110 (0x26)		j	000010 (0x02)
slt	101010 (0x2A)		jal	000011 (0x03)
sltu	101001 (0x29)		jalr	001001 (0x09)
			jr	001000 (0x08)
Arithm / Logical Imm				
Instruction	**(OpCode)**		**Load/Store Instructions**	
addi	001000 (0x08)		lb	100000 (0x20)
addiu	001001 (0x09)		lbu	100100 (0x24)
andi	001100 (0x0C)		lw	100011 (0x23)
ori	001101 (0x0D)		sb	101000 (0x28)
xori	001110 (0x0E)		sw	101011 (0x2B)
			Data Movement Instructions	
			mfhi	010000 (0x10)
			mflo	010010 (0x12)
			mthi	010001 (0x11)
			mtlo	010011 (0x13)

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[1] O OpCode é igual ao da instrução **bltz** mas o valor de **rt** é igual a 00001_b