

Book A, Hamacher		
Lectures	L0 - Introduction	Chapter 1
	L1 - Digital Logic	Appendix A (w/o A.5, A.11, A.12)
	L2 - Digital Data	Chapter 9 (w/o 9.2, 9.3, 9.6) + Section 1.4.1
	L3 - ISAs	Chapter 2
	L4 - Assembler	Appendix E
	L5 - IA64	Appendix E
	L6 - Basic Processing Unit	Chapter 5
	L7.1. - I/O + OS	Chapter 7 (only 7.1, 7.2, 7.3)
	L7.2. - Memory & Performance	Chapter 8 (only 8.1, 8.2, 8.4 – 8.7)
	L8 - Pipelining & Performance	Chapter 6 (w/o 6.10)
	L9 - Large-scale Systems	Chapter 12

Book B, Patterson		
Lectures	L0 - Introduction	Appendix B (w/o B.4, B.6, B.12)
	L1 - Digital Logic	Chapter 3.1 – 3.5
	L2 - Digital Data	N/A (different ISA)
	L3 - ISAs	N/A (different ISA)
	L4 - Assembler	Chapter 2.17 (very limited!)
	L5 - IA64	Chapter 2.17 (very limited!)
	L6 - Basic Processing Unit	Chapter 4.1 – 4.4
	L7.1. - I/O + OS	N/A
	L7.2. - Memory & Performance	Chapter 5.1 – 5.5, 5.8, 5.9
	L8 - Pipelining & Performance	Chapter 4.5 – 4.9
	L9 - Large-scale Systems	Chapter 6