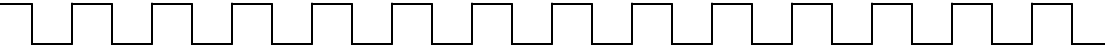
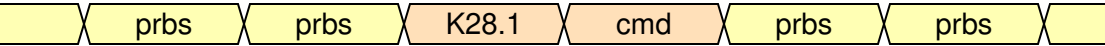


Clock (62.5MHz)



Master -> Endpoint (8b/10b symbols, 312.5MBit/s)



Endpoint -> Master

