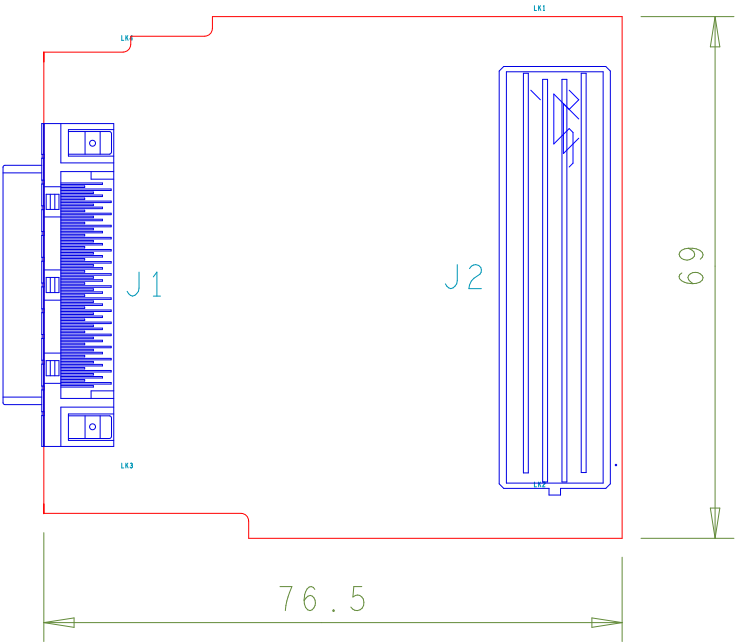


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
2	Alter padstacks - FMC,VHDCI	12/6/14	



UNLESS OTHERWISE SPECIFIED	SIGNATURES	DATE	University of Bristol HEP			
DIMENSIONS ARE IN MILMETRES TOLERANCES ON: 2 PL DECIMALS + 3 PL DECIMALS + ANGLES + FRACTIONS +	D.Cussans	Feb 17	pc054a_fmc Interface SoLiD clock board			
			SIZE	FSCM NO	DWG NO	Assembly
						Top
			SCALE			SHEET