

1

2

3

4

5

6

7

A

B

C

ADC

8 X LTM9007

PAGE 2,3

LEDS

IP SELECT SWITCHES

PAGE 4

POWER REGULATION

PAGE 5

CONNECTION TO AMPLIFIER

4 X DIN 41612

PAGE 6-9

SFP CAGES

ESATA CONNECTORS

PAGE 10

CLOCK GENERATION

PAGE 11

JTAG

PAGE 12

CONNECTIONS TO FPGA

PAGE 13

CPLD

(MULTIPLEXES I2C,SPI)

ALSO, TEMPERATURE MONITOR,
POWER-ON-RESET & UNIQUE ID

PAGE 14

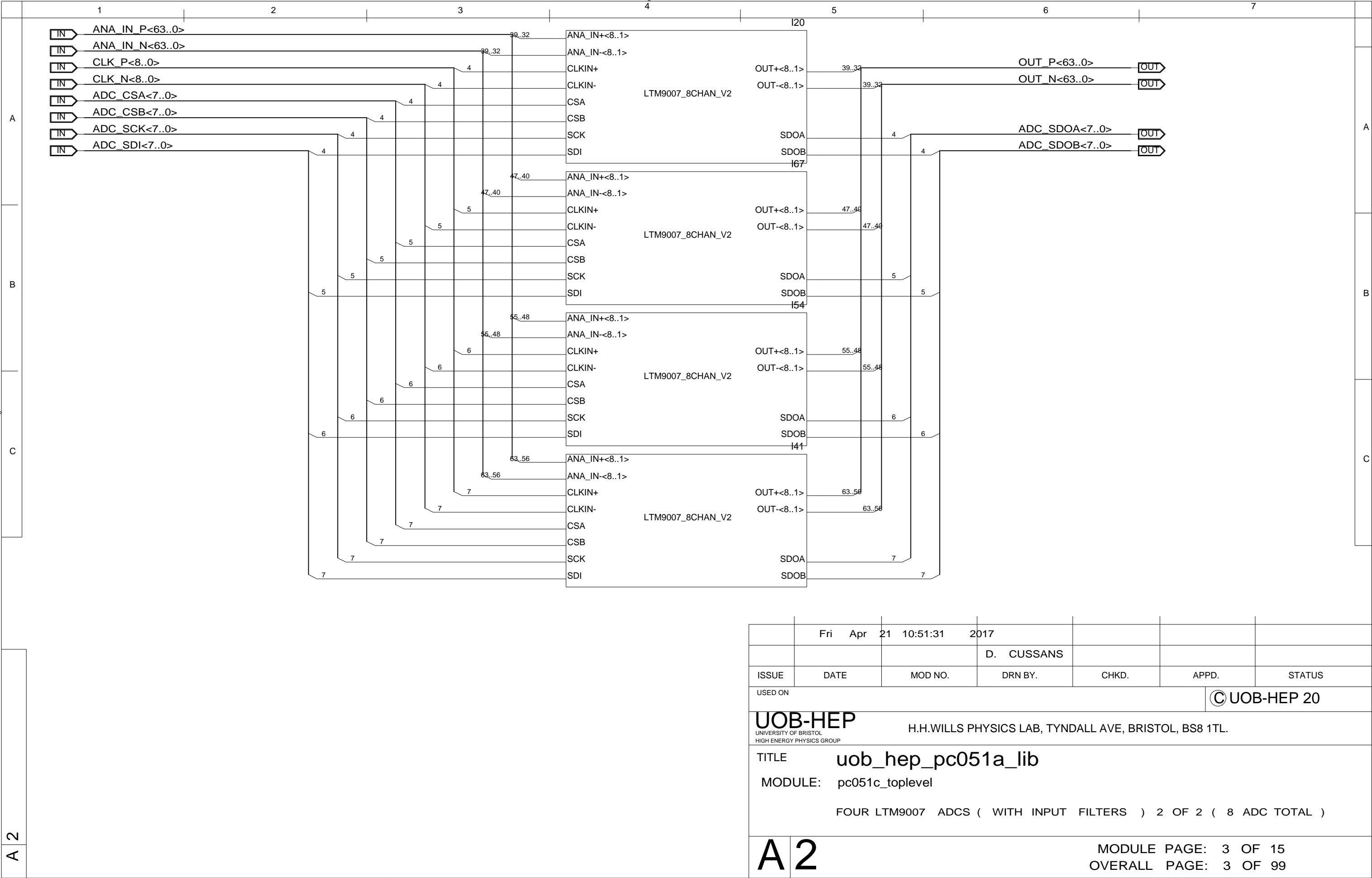
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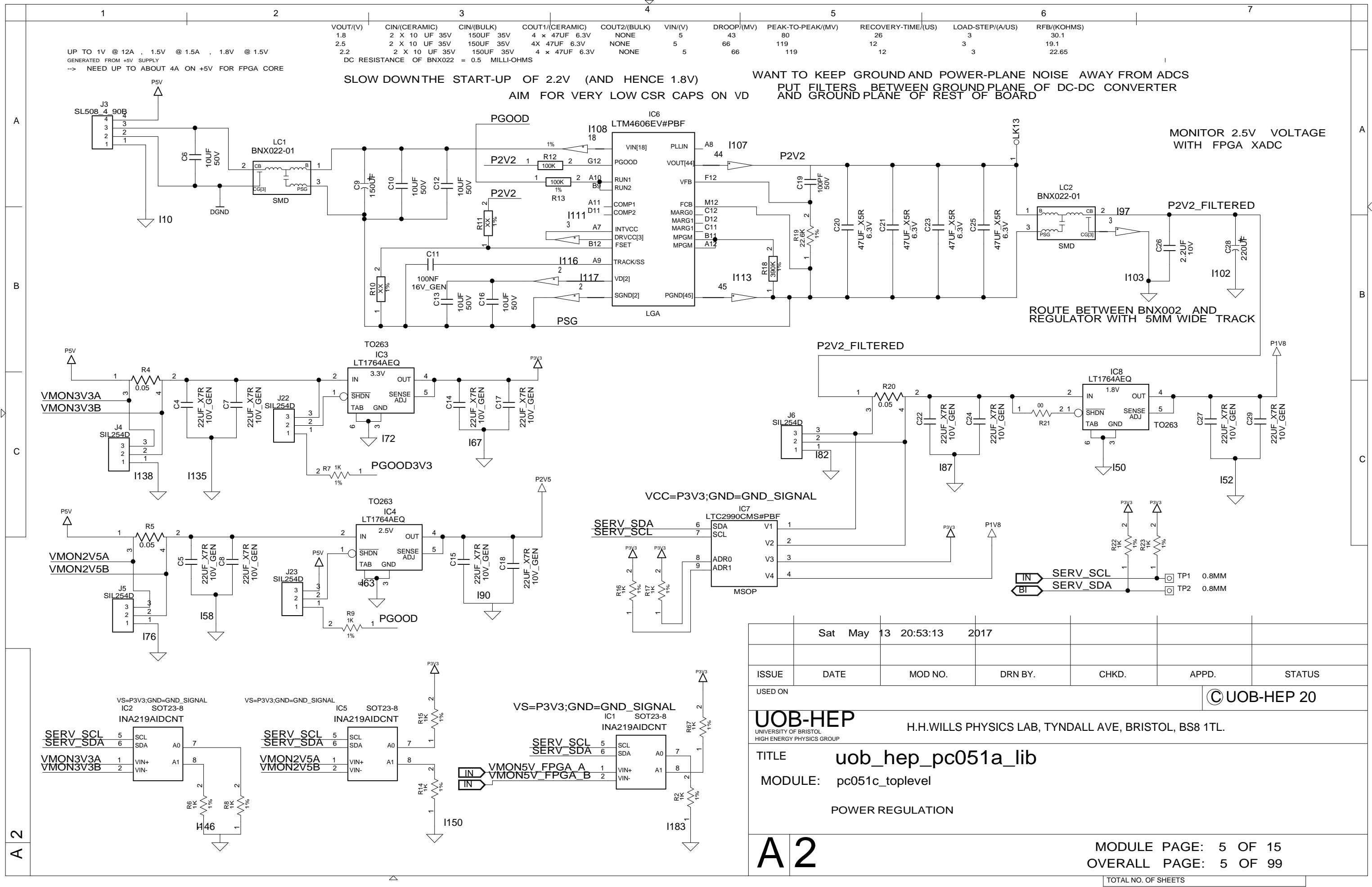
(TRENZ TE0712)

PAGE 15

	Mon Jul 11 19:23:10 2016					
			DAVID CUSSANS			
ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
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TITLE		uob_hep_pc051a_lib				
MODULE:		pc051c_toplevel				
		OVERALL LAYOUT				
A 2		MODULE PAGE: 1 OF 15 OVERALL PAGE: 1 OF 99				

TOTAL NO. OF SHEETS





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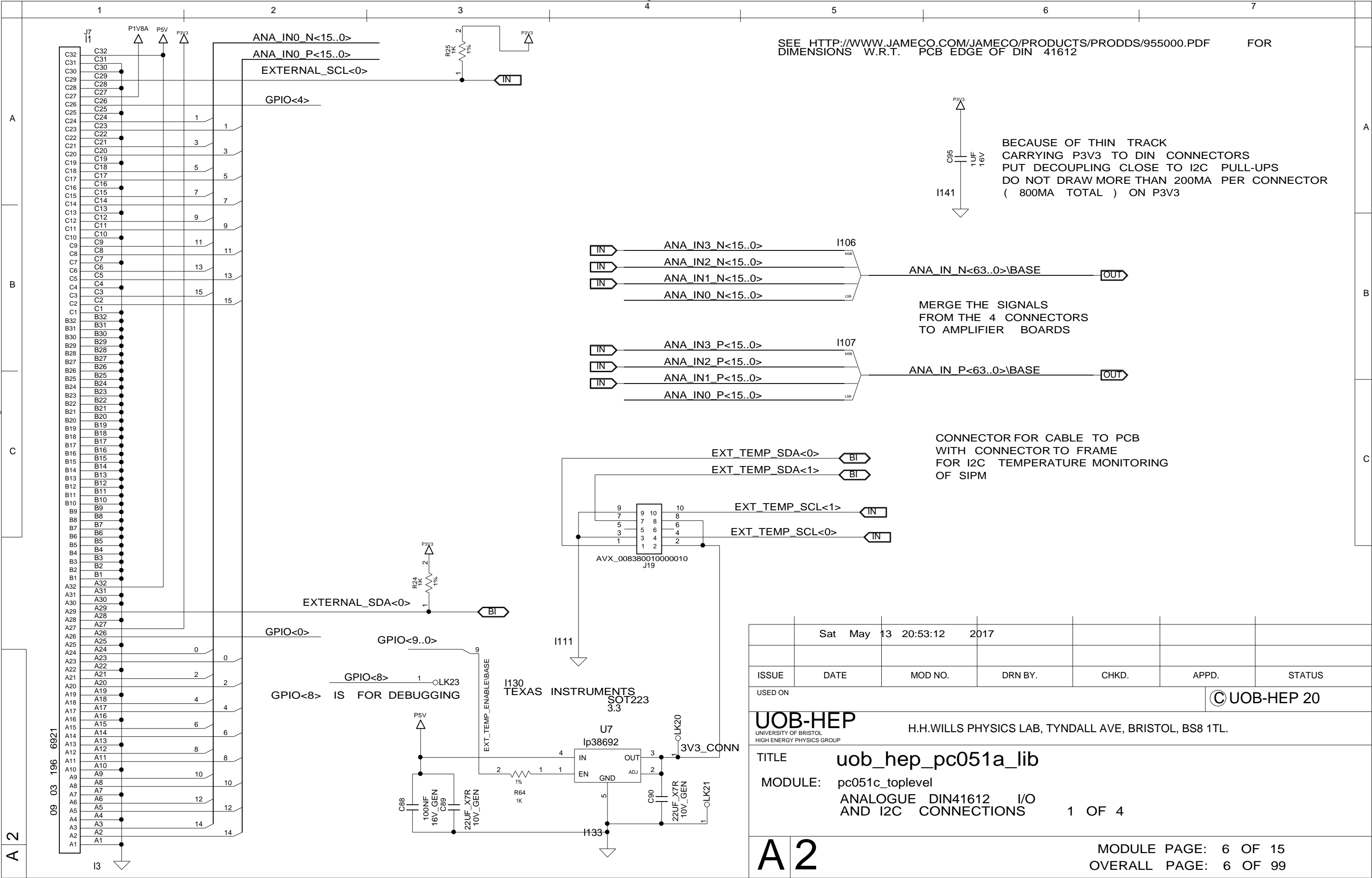
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MODULE:

pc051c_toplevel

POWER REGULATION



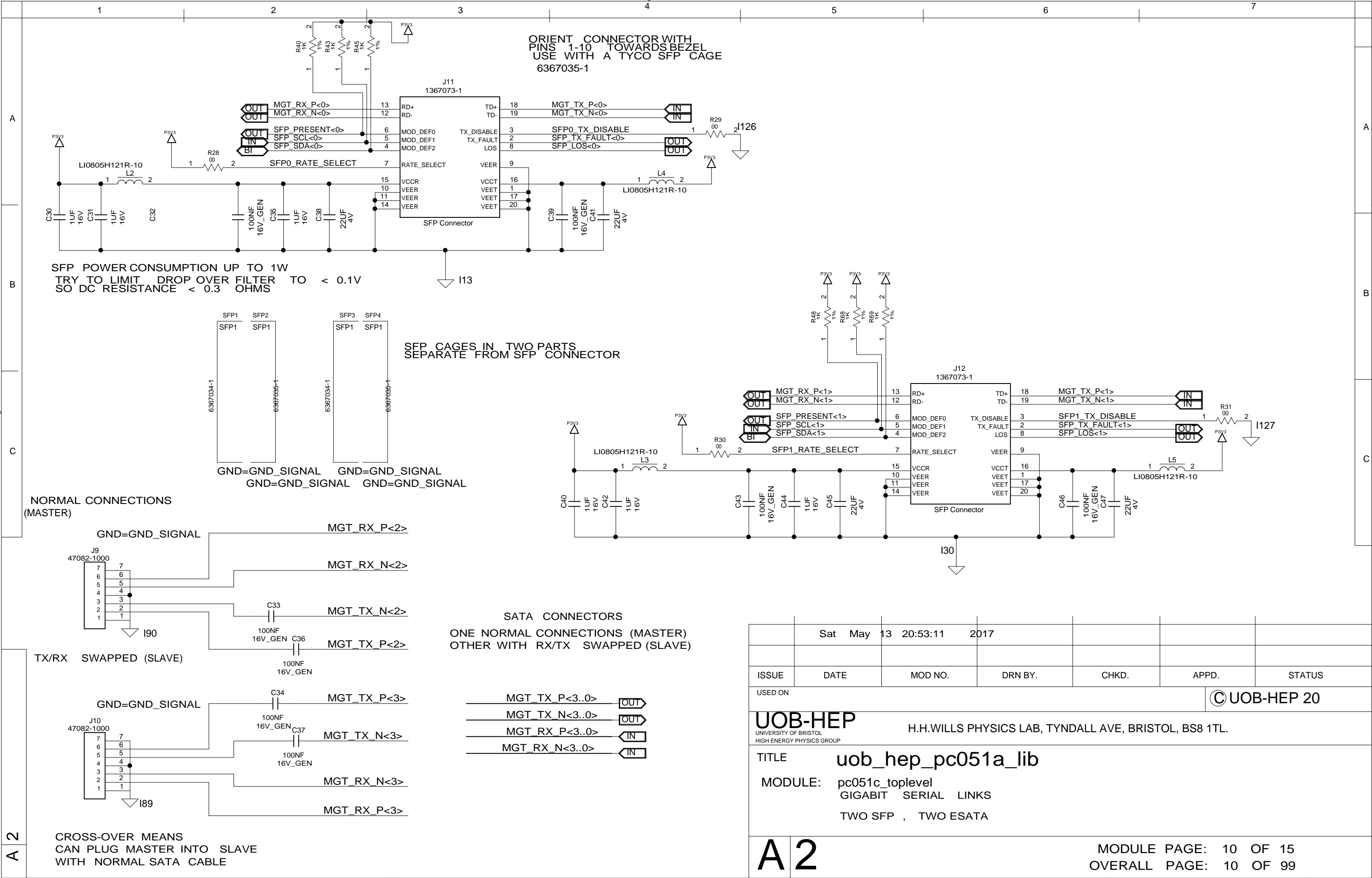
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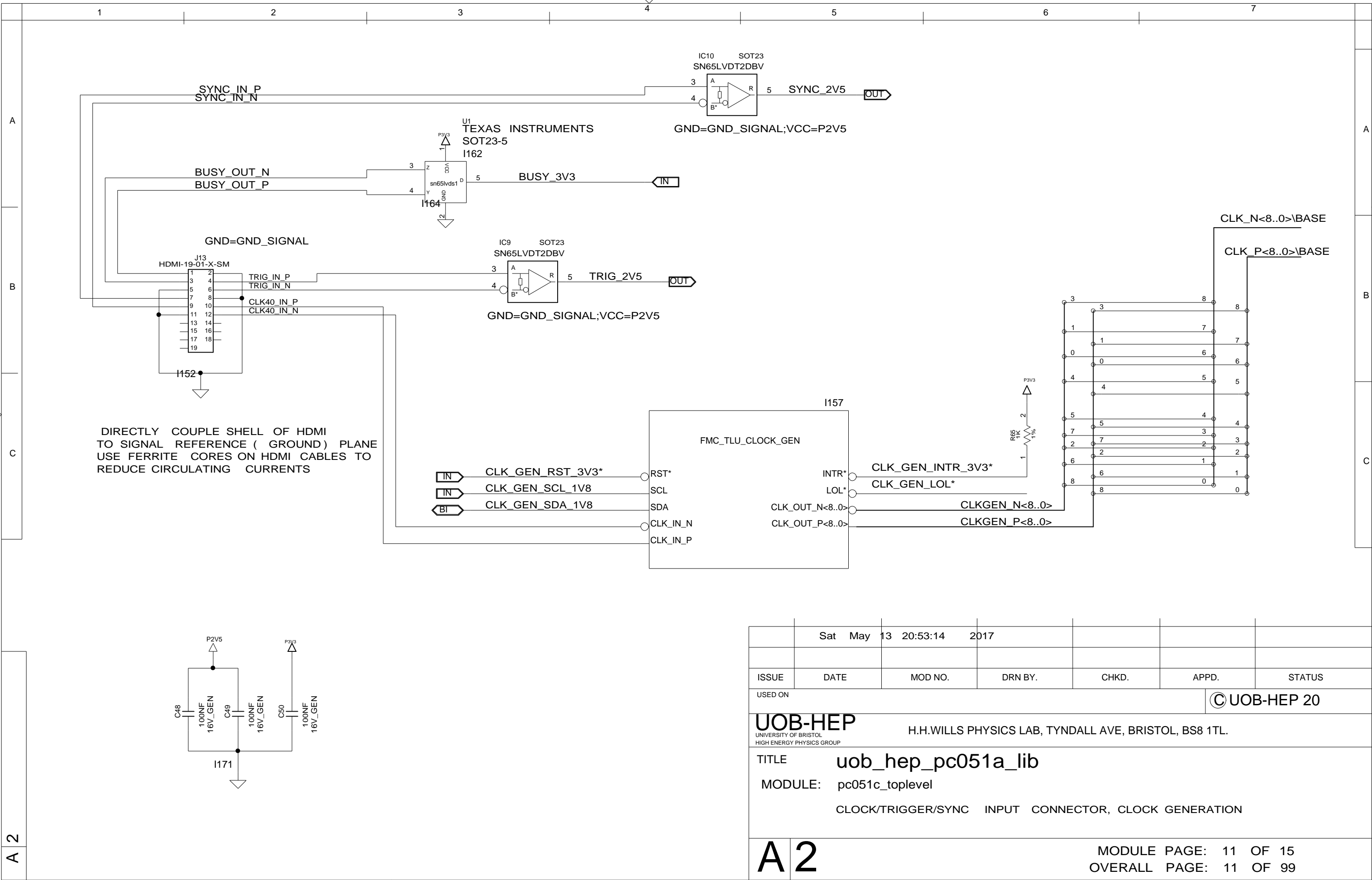
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HIGH ENERGY PHYSICS GROUP								

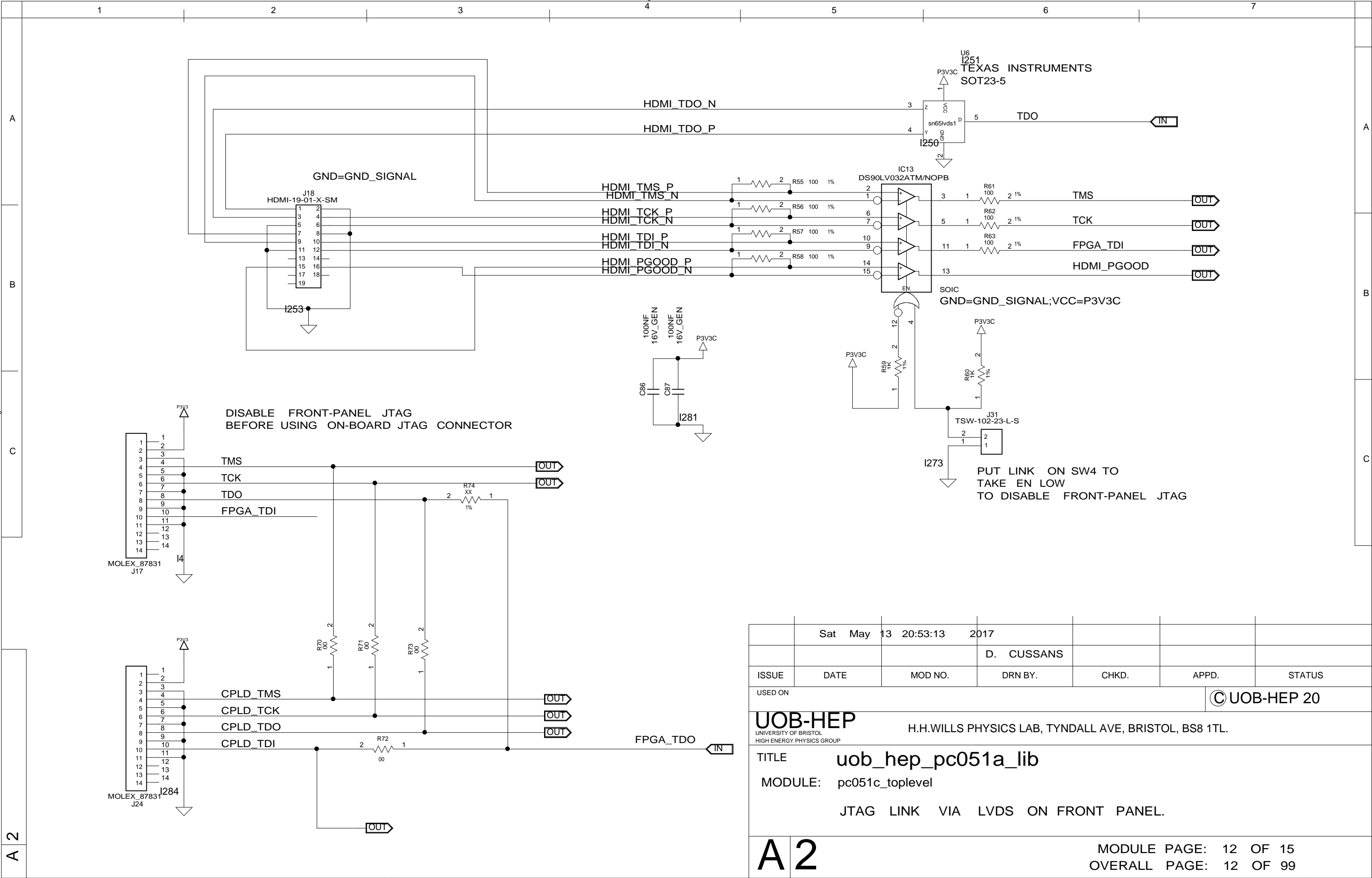
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	ANALOGUE DIN41612 I/O							
	AND I2C CONNECTIONS							
	1 OF 4							

A2	MODULE PAGE: 6 OF 15
	OVERALL PAGE: 6 OF 99





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TITLE							uob_hep_pc051a_lib	
MODULE:							pc051c_toplevel	
							CLOCK/TRIGGER/SYNC INPUT CONNECTOR, CLOCK GENERATION	
A 2							MODULE PAGE: 11 OF 15	
							OVERALL PAGE: 11 OF 99	
							TOTAL NO. OF SHEETS	



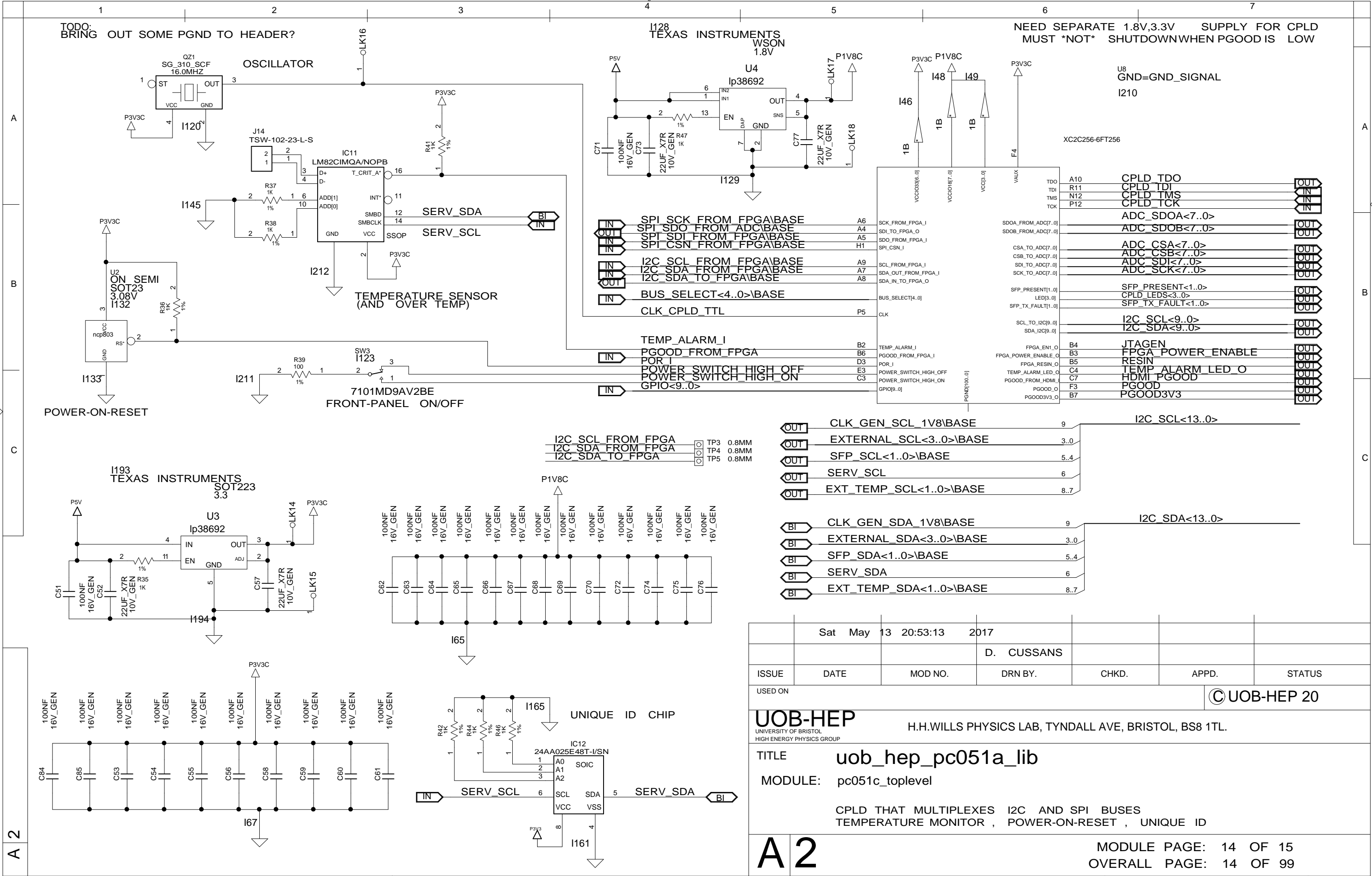
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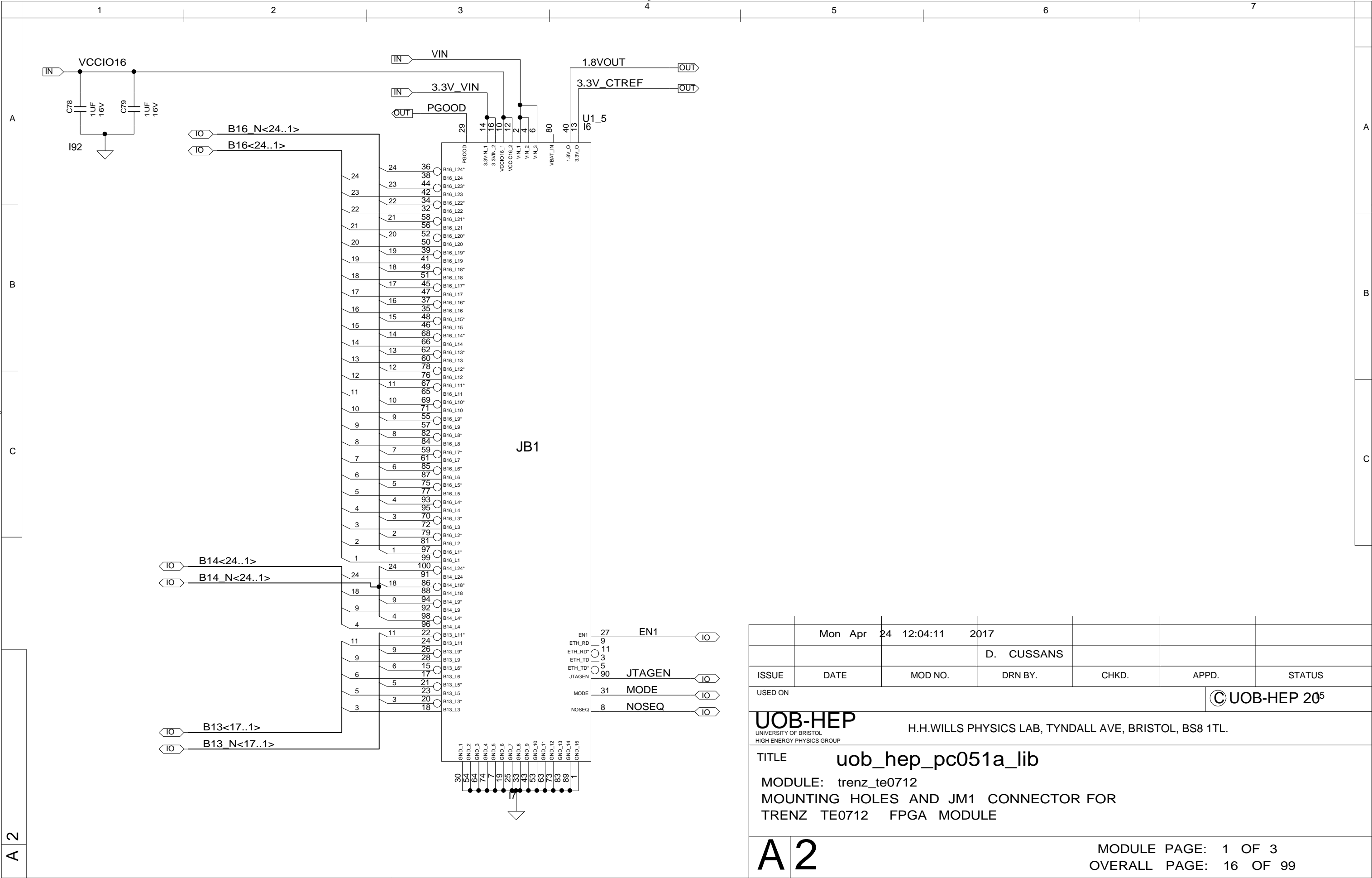
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TITLE **uob_hep_pc051a_lib**
MODULE: pc051c_toplevel
JTAG LINK VIA LVDS ON FRONT PANEL.





	Mon Apr 24 12:04:11 2017					
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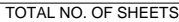
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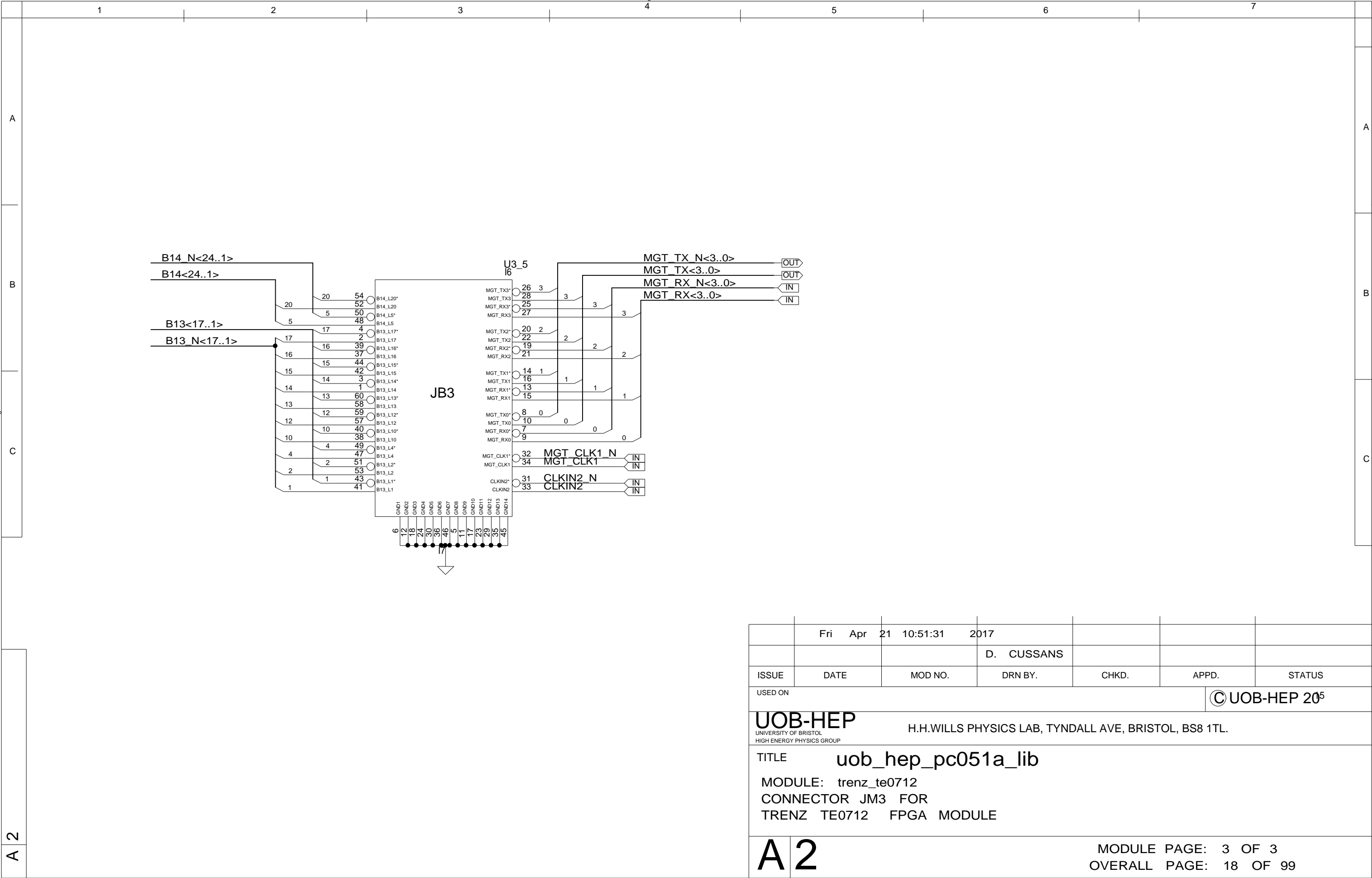
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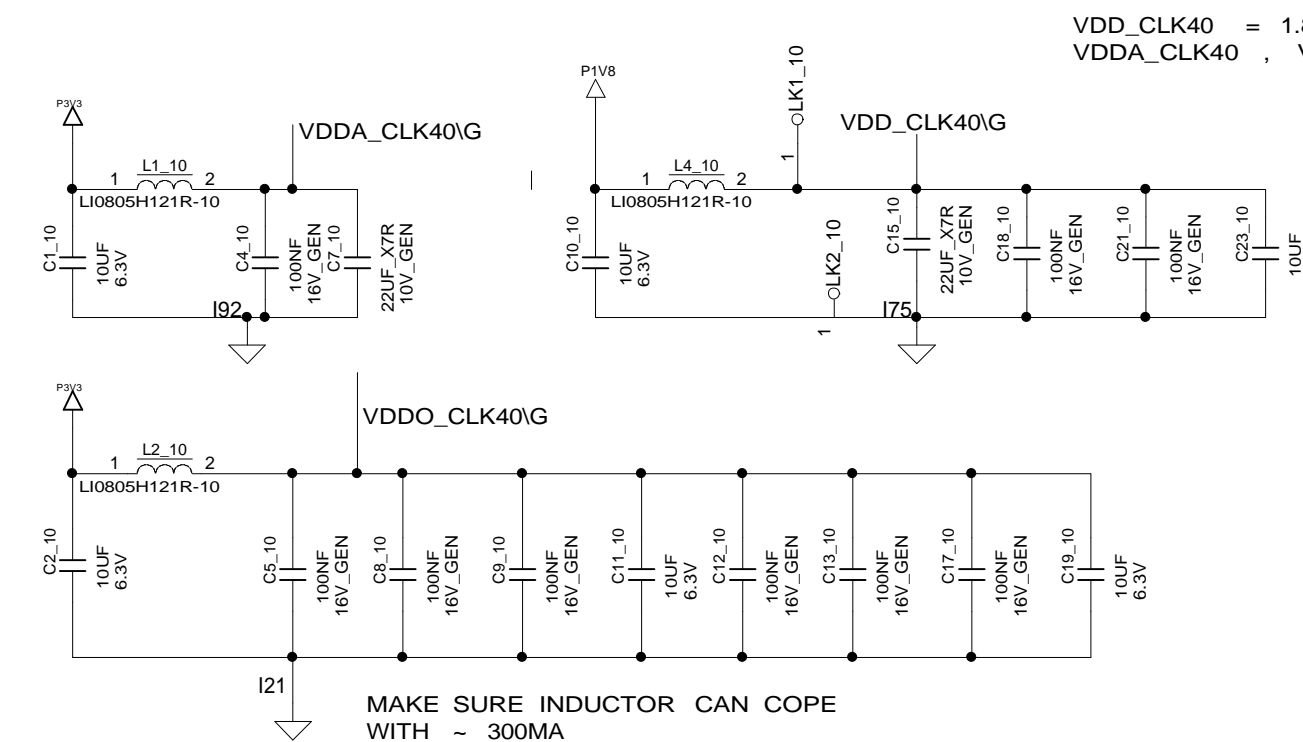
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MODULE: trenz_te0712

MOUNTING HOLES AND JM1 CONNECTOR FOR
TRENZ TE0712 FPGA MODULE







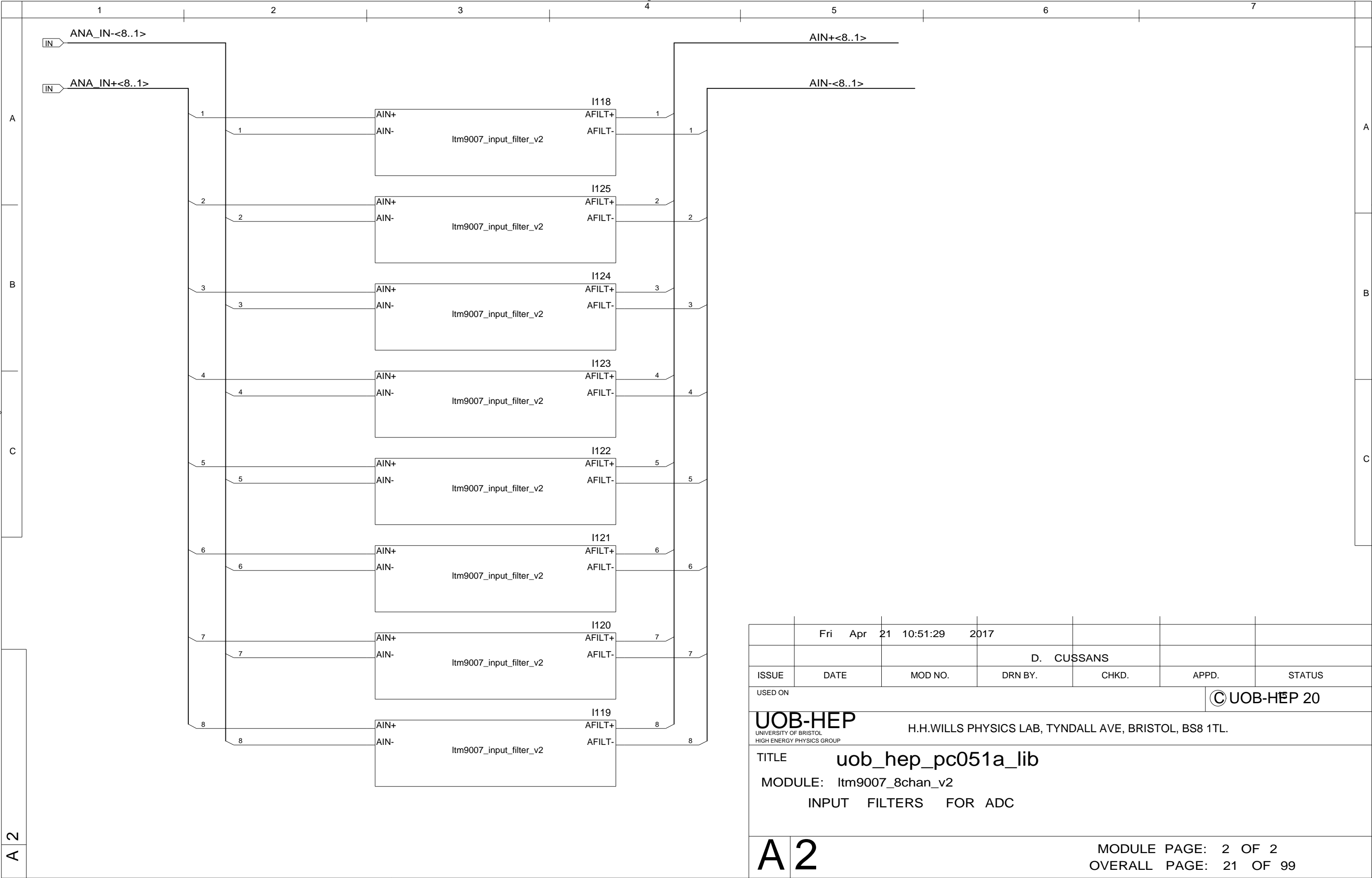
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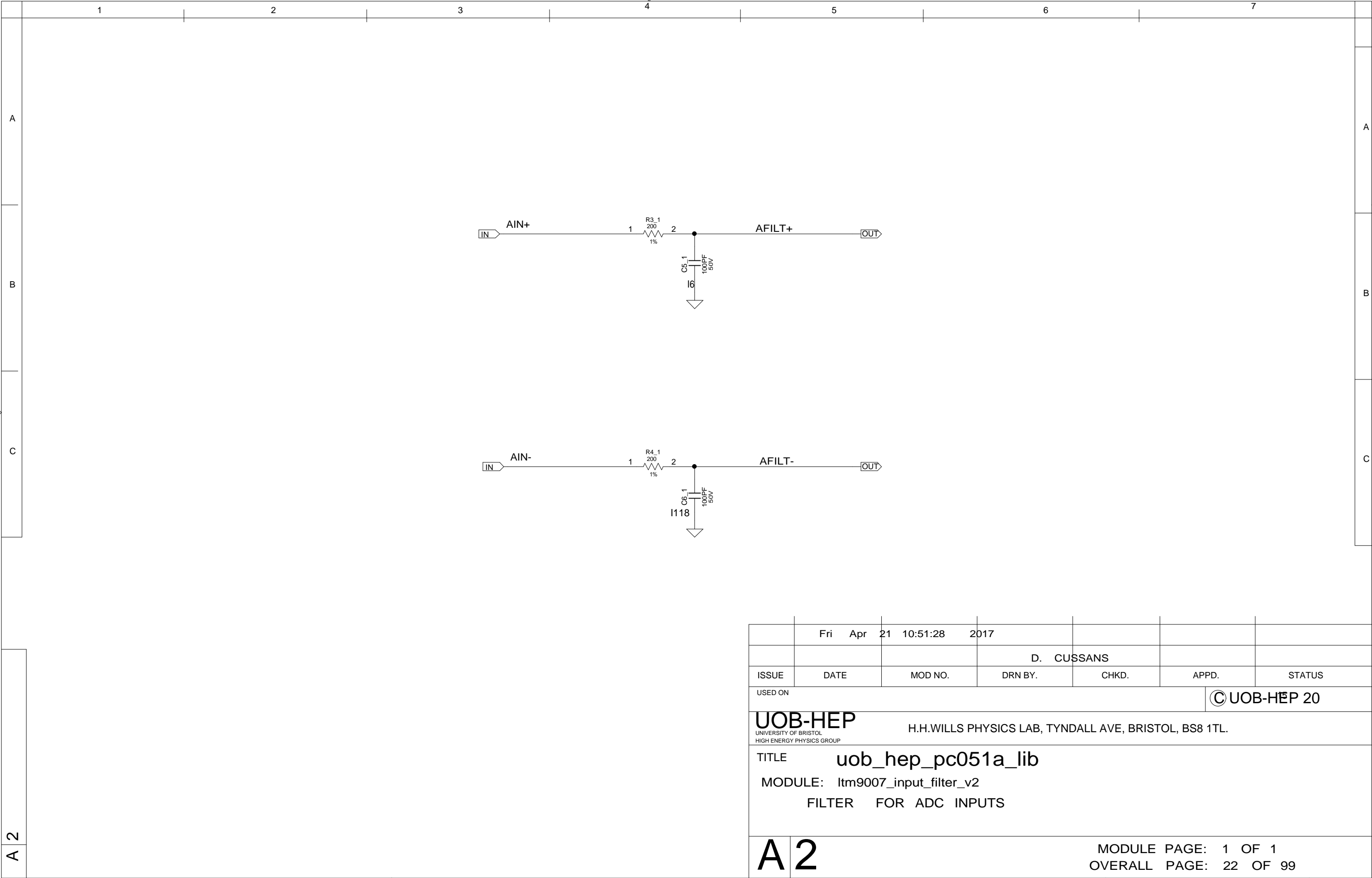
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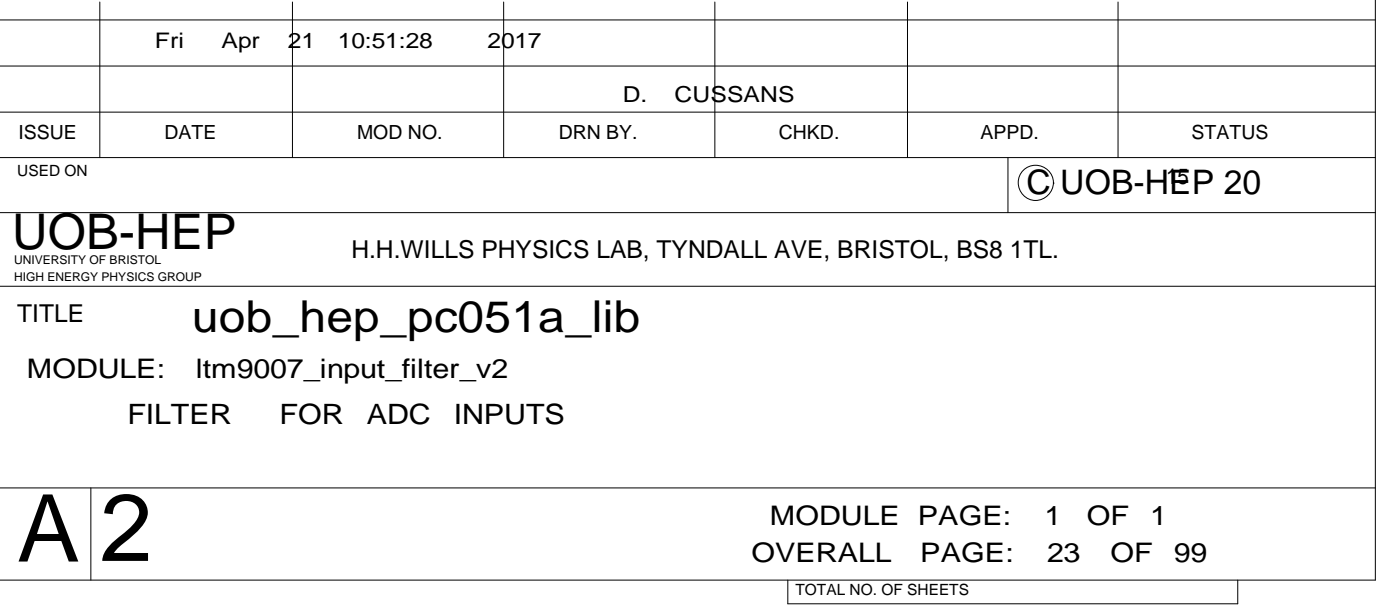
CLOCK GENERATION -
SI5345 CLOCK CHIP

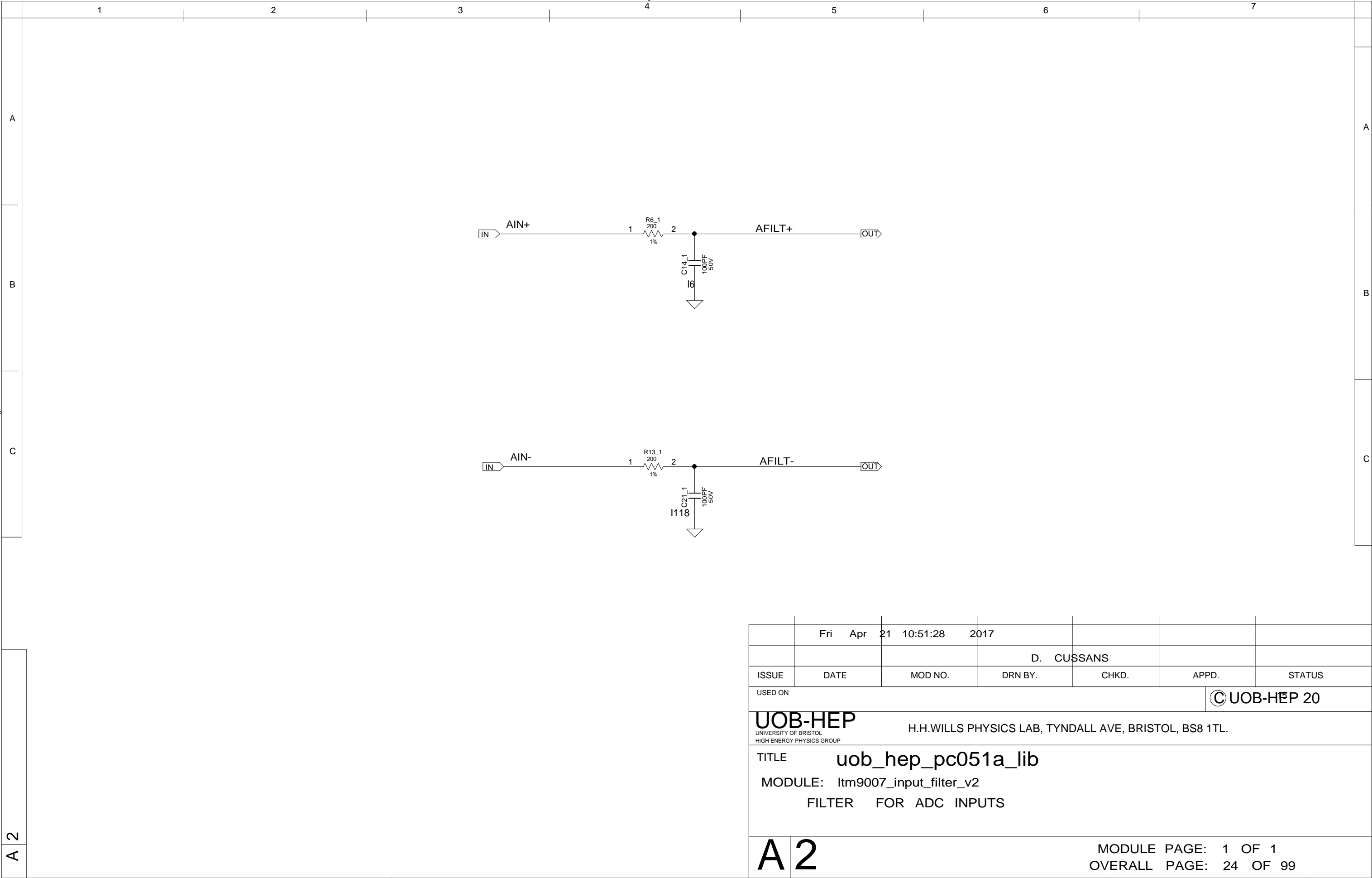
LICENSED UNDER THE TAPR OPEN HARDWARE LICENSE (WWW.TAPR.ORG/OHL)

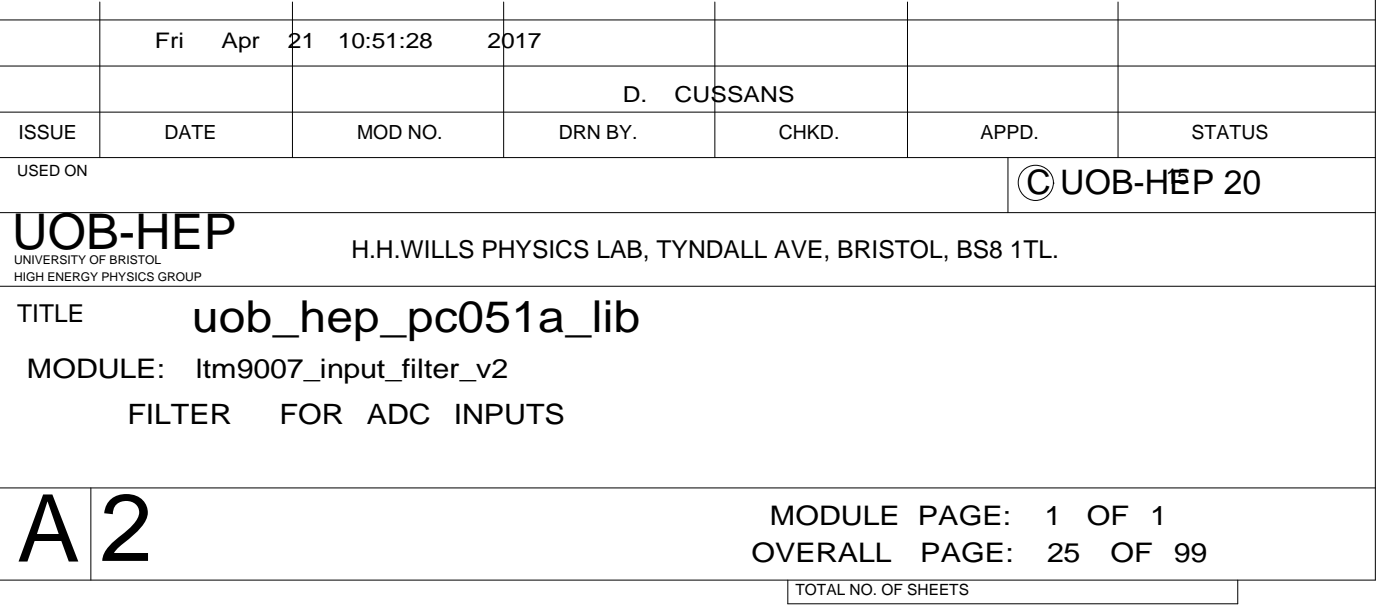


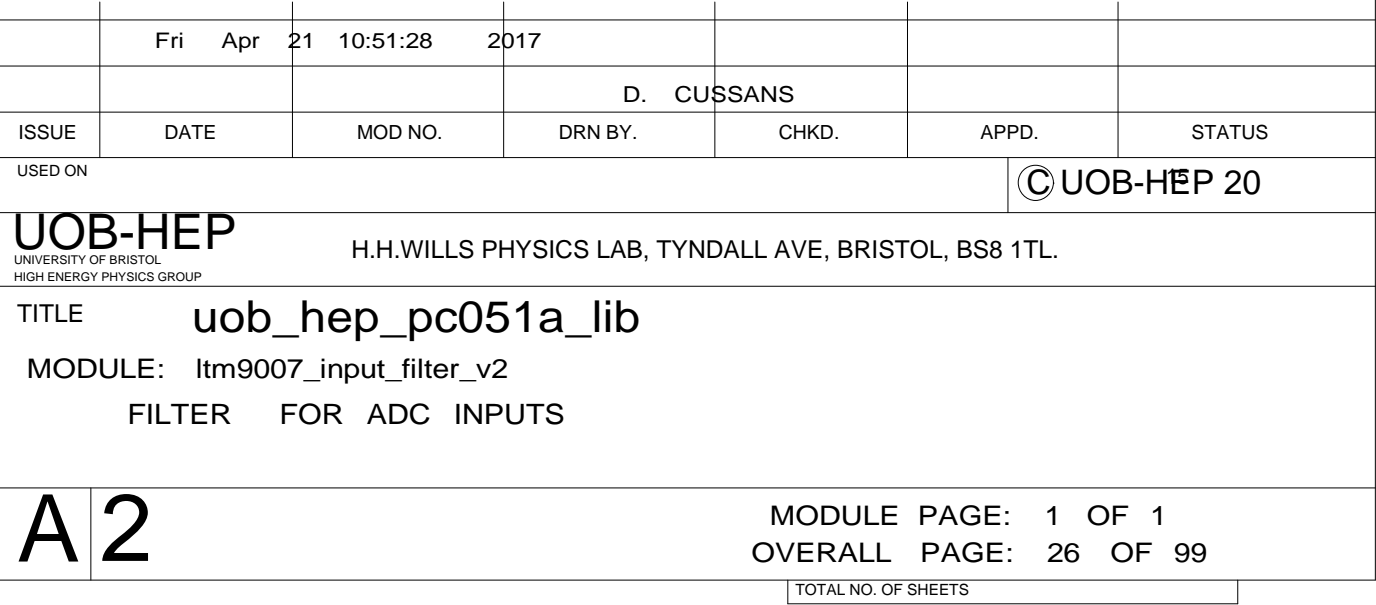


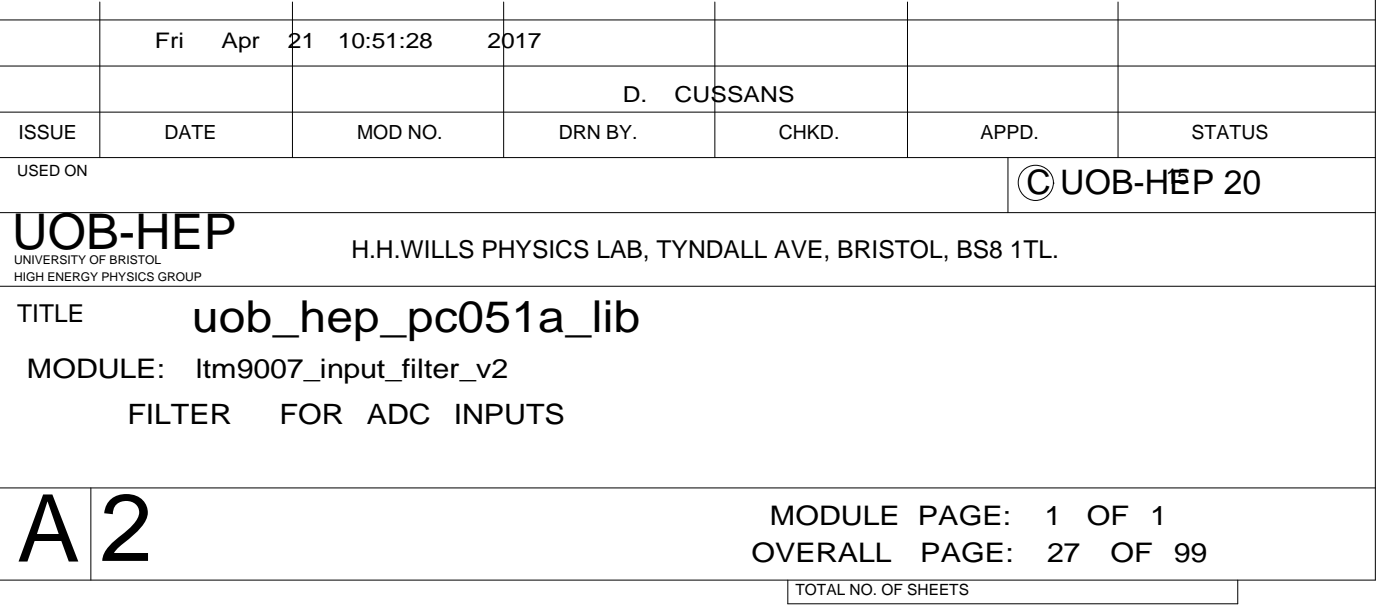


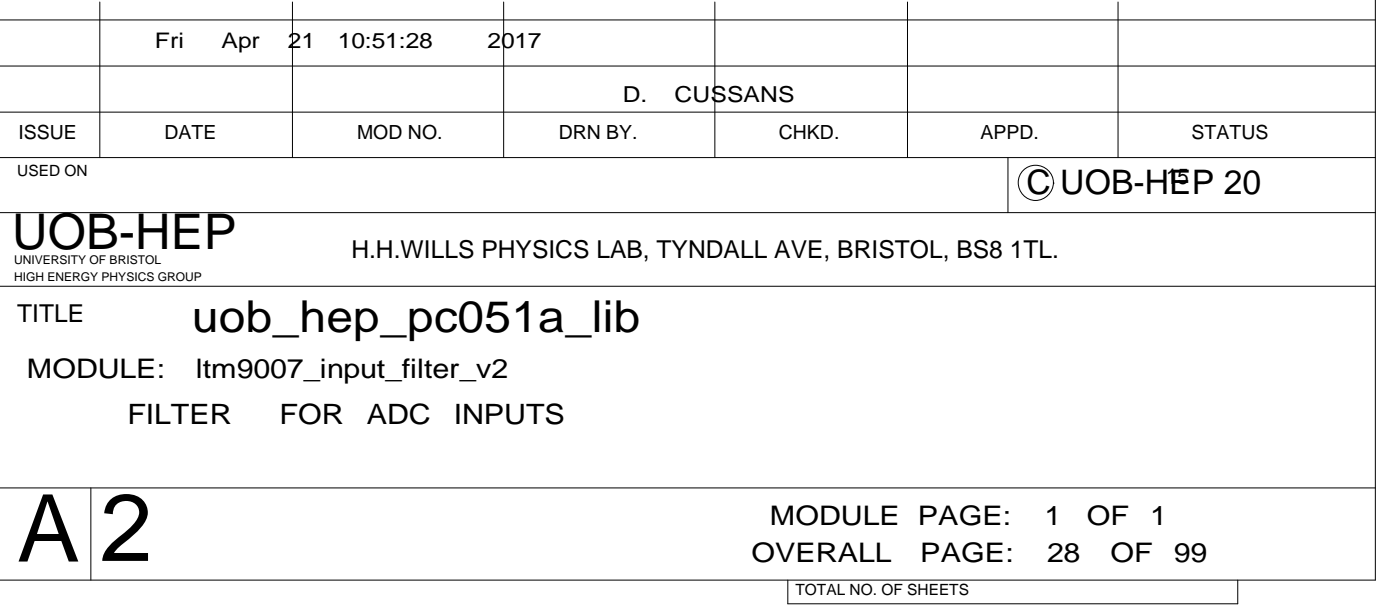


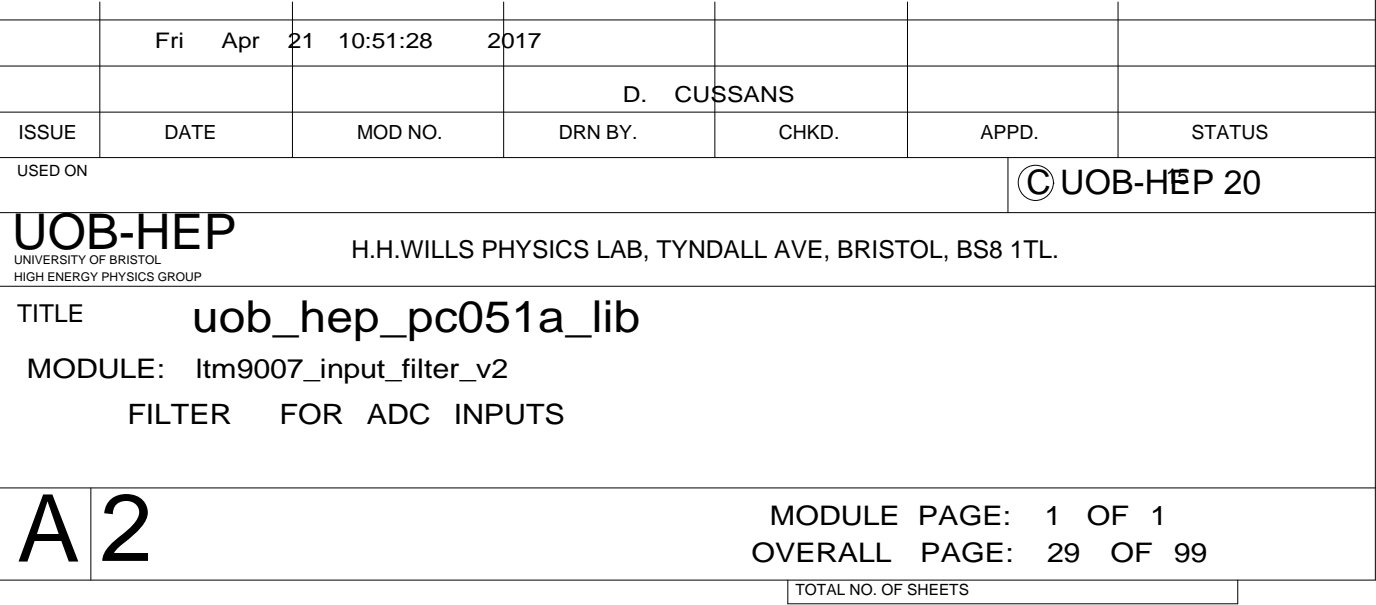


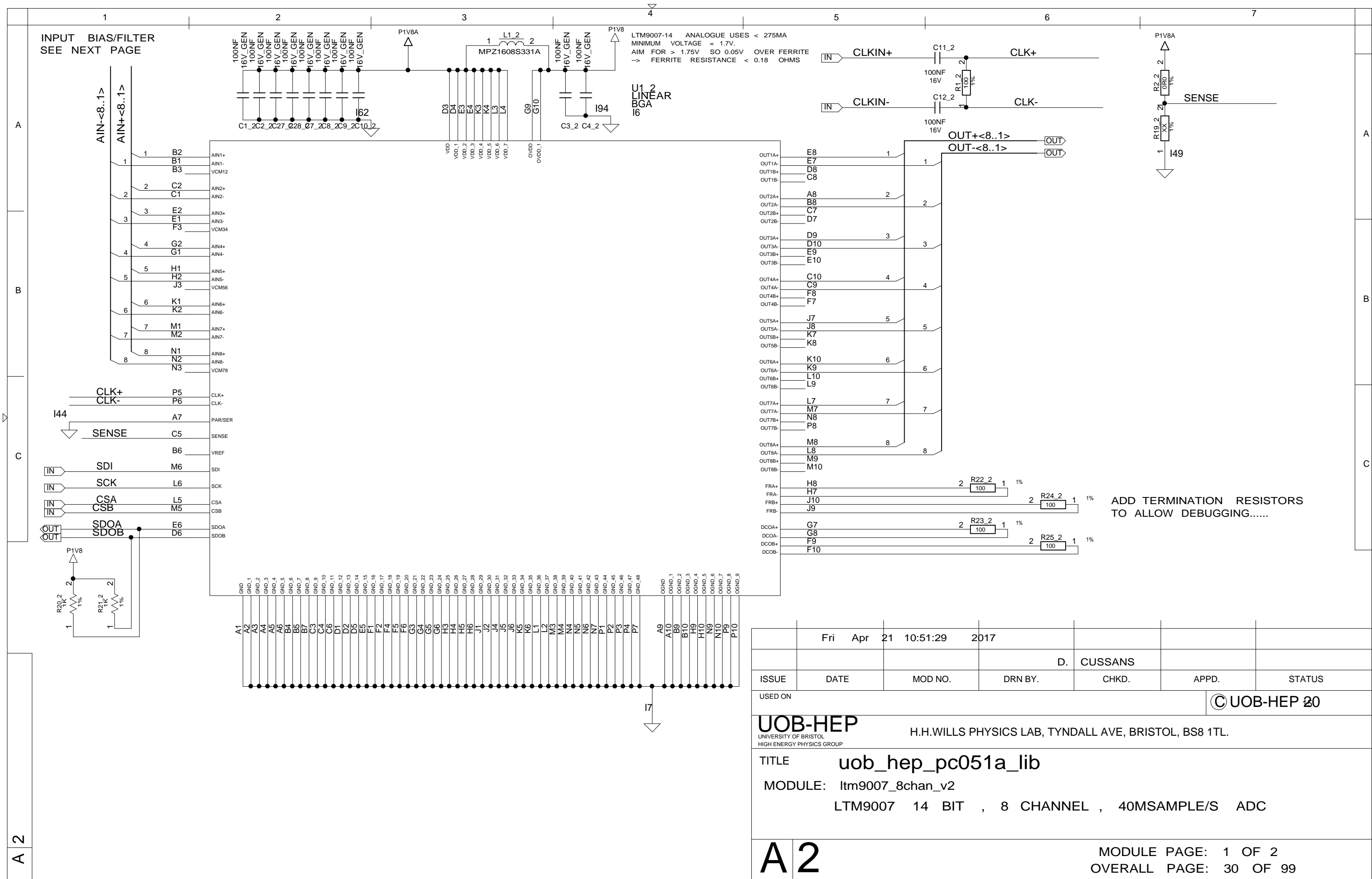


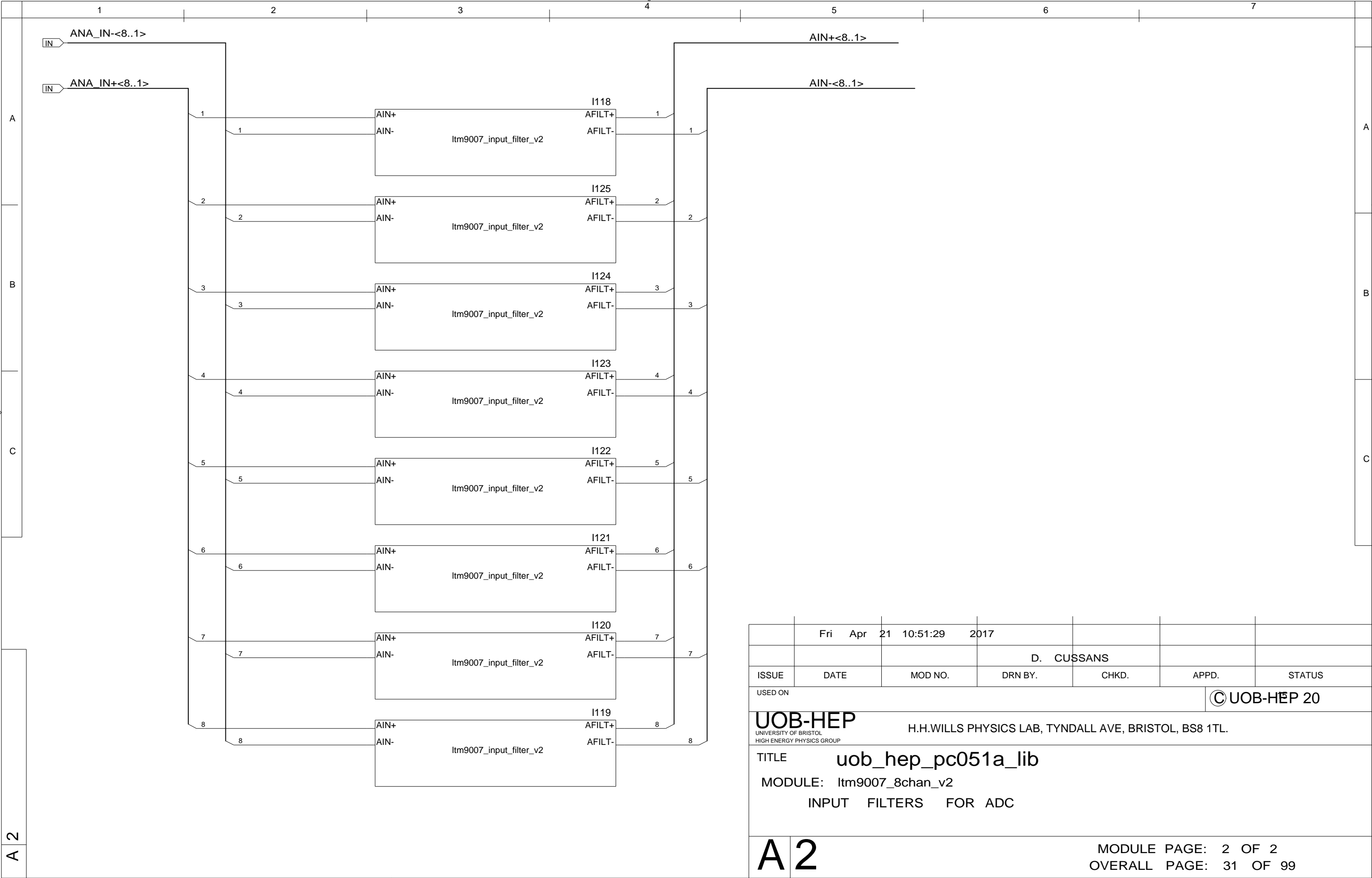


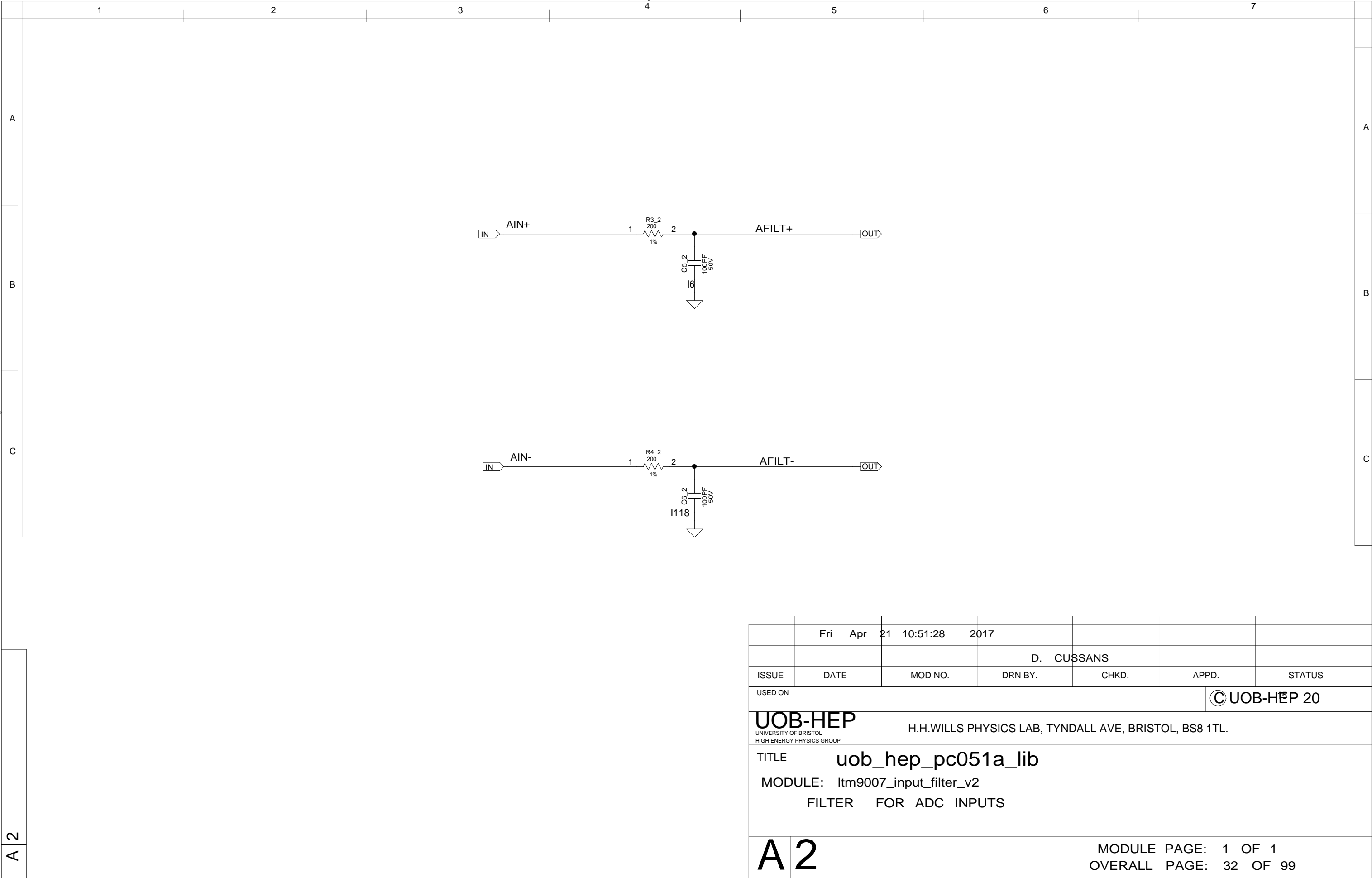


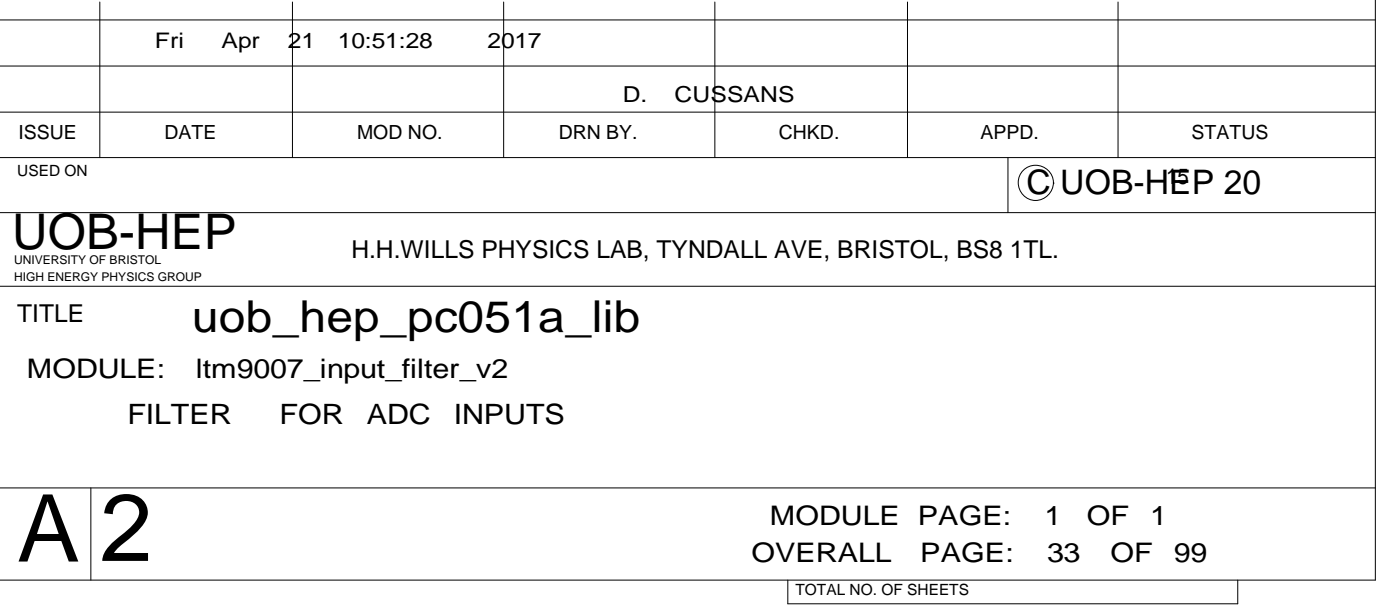


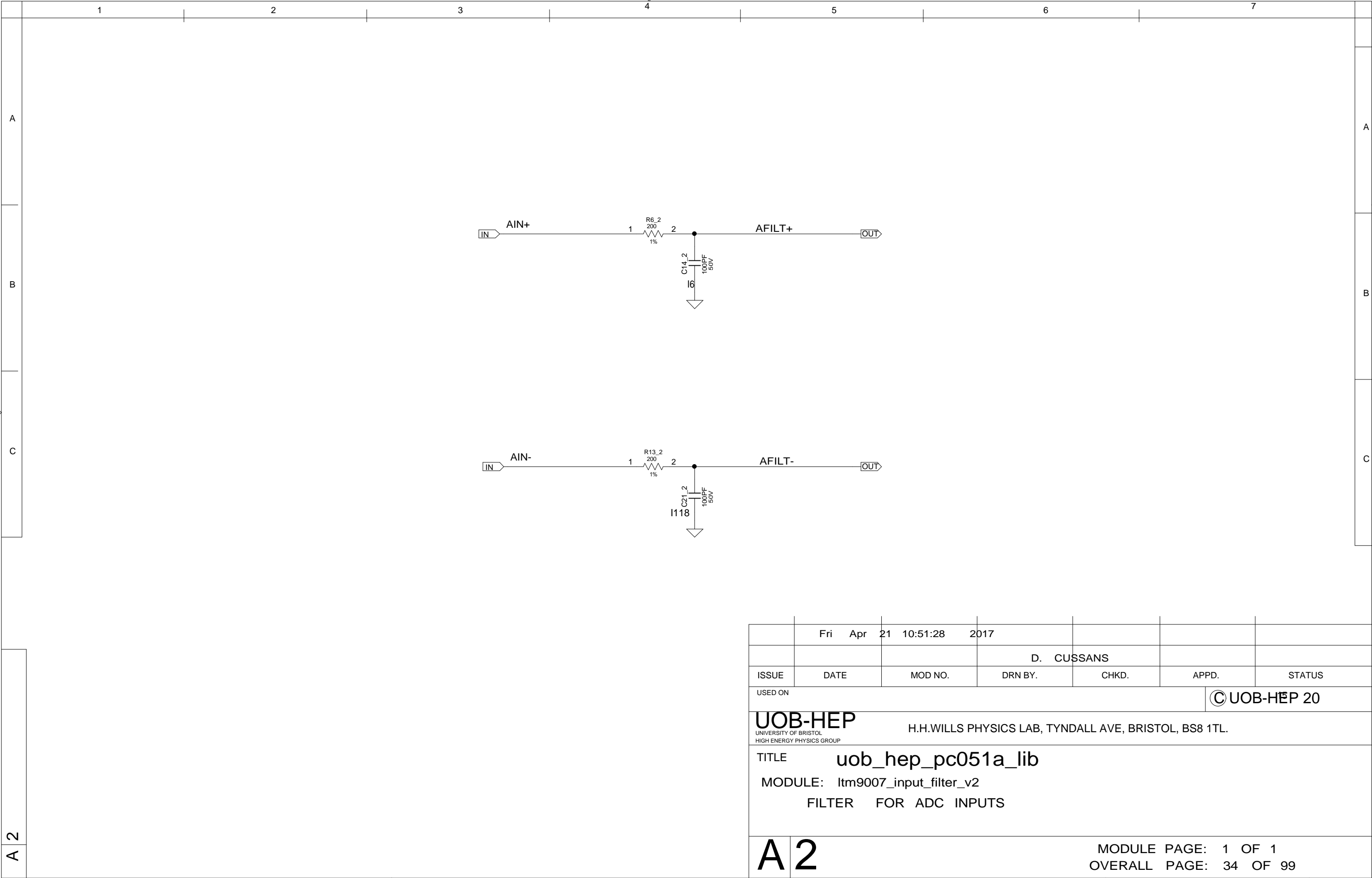


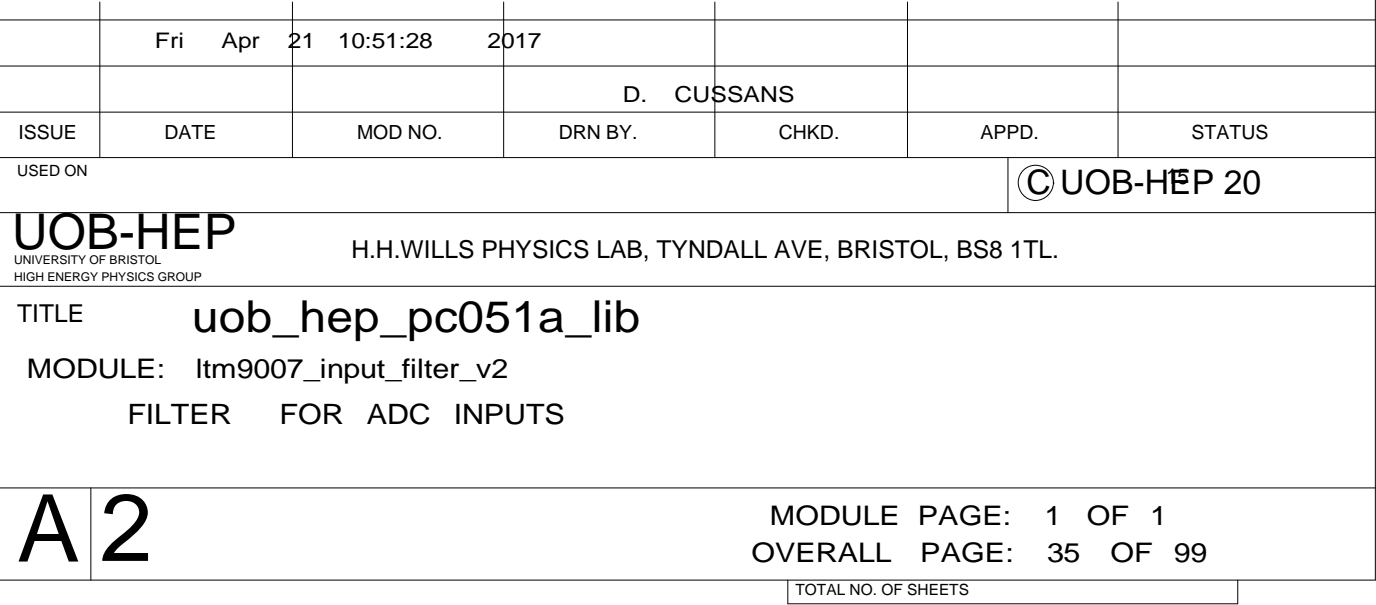


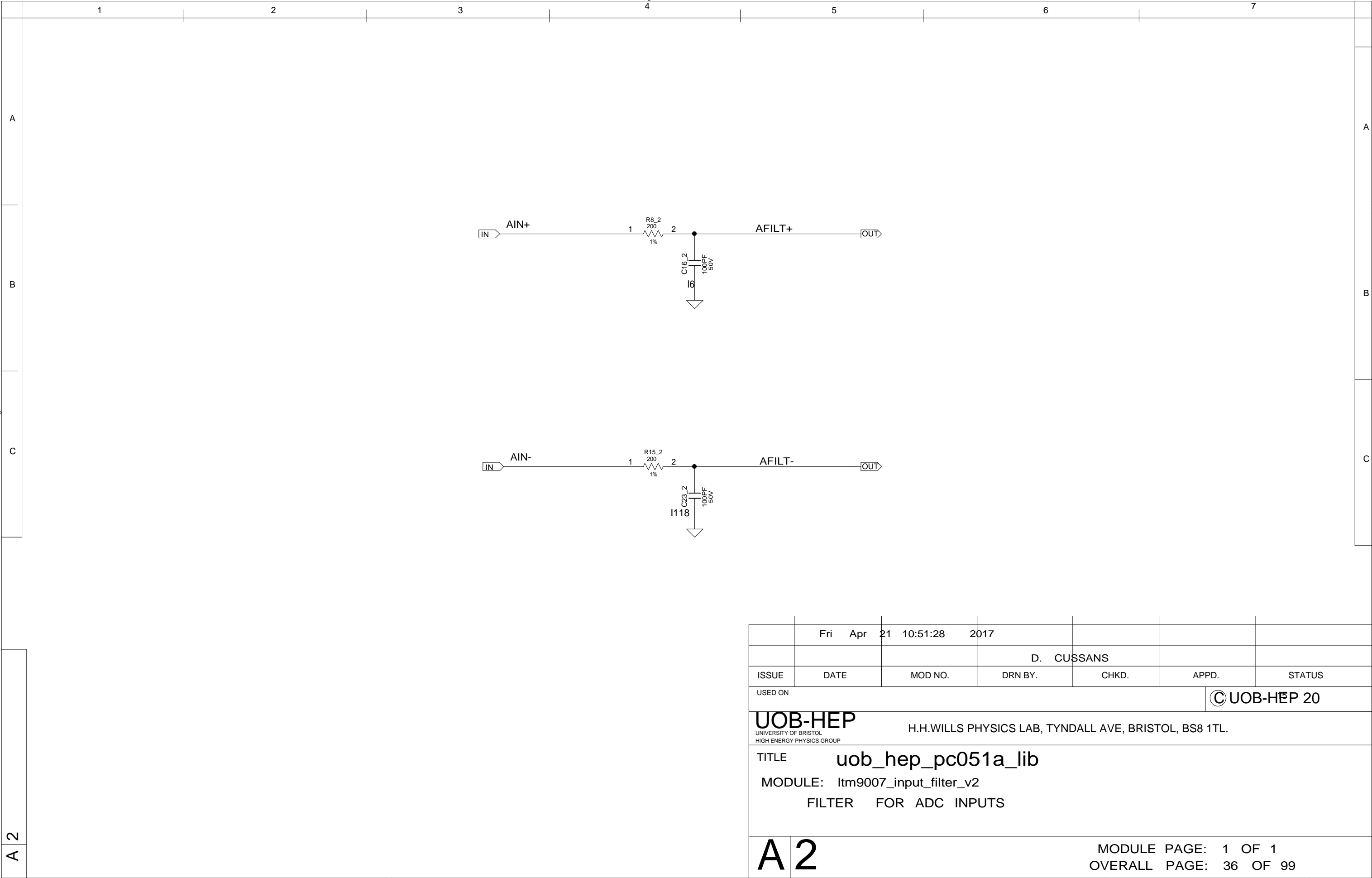


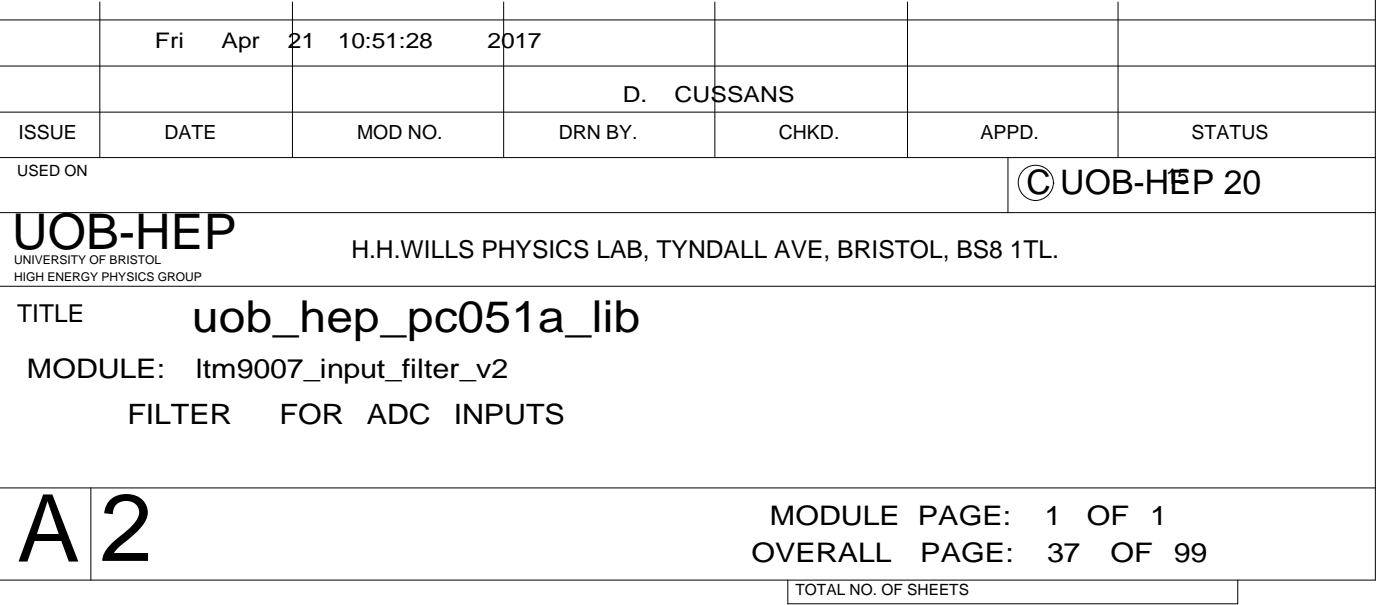


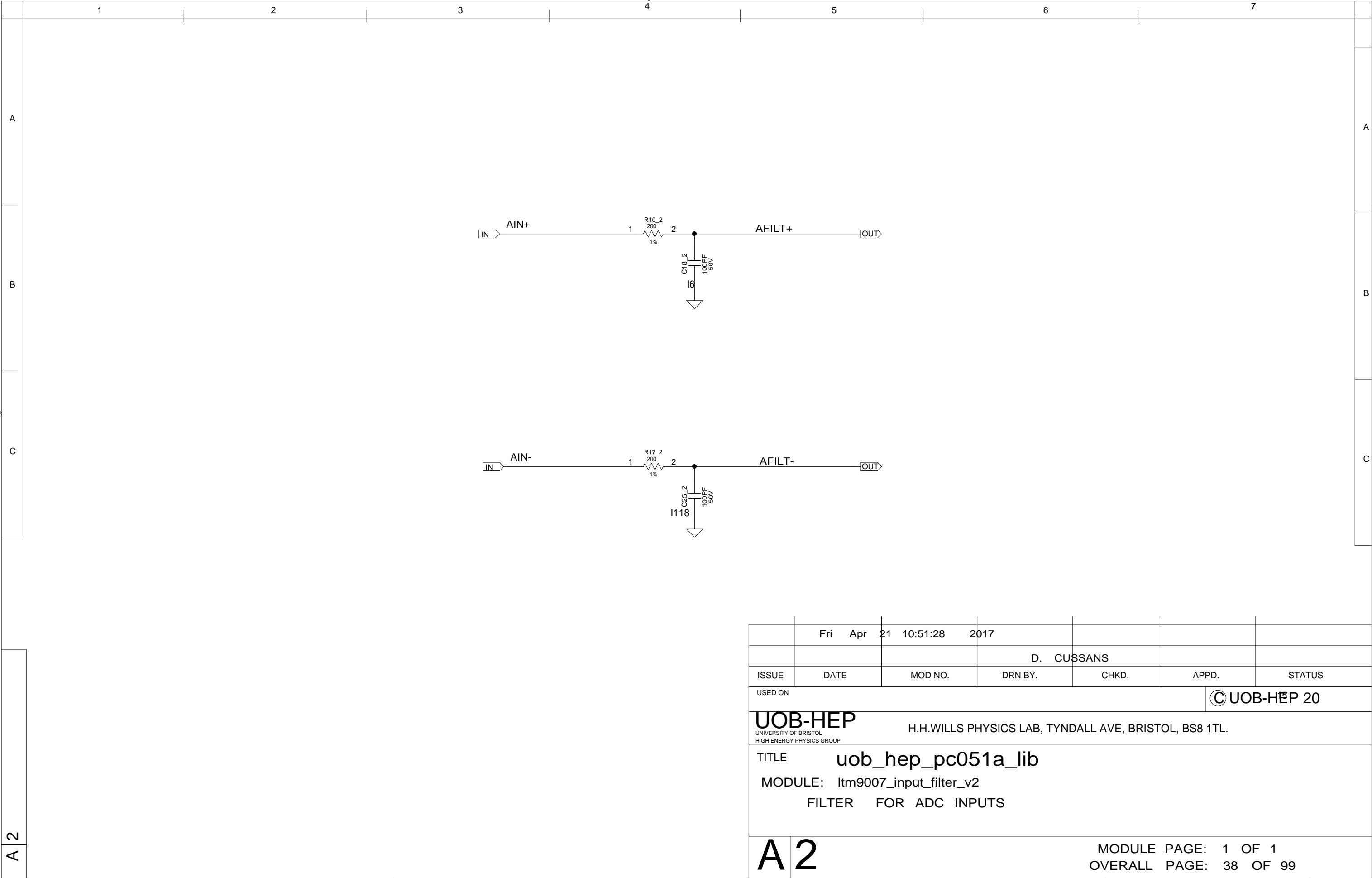












1

2

3

4

5

6

7

A

B

C

AIN+

IN

1

2

R11_2

200

1%

AFILT+

OUT

C19_2

100PF

50V

I6

AIN-

IN

1

2

R18_2

200

1%

AFILT-

OUT

C26_2

100PF

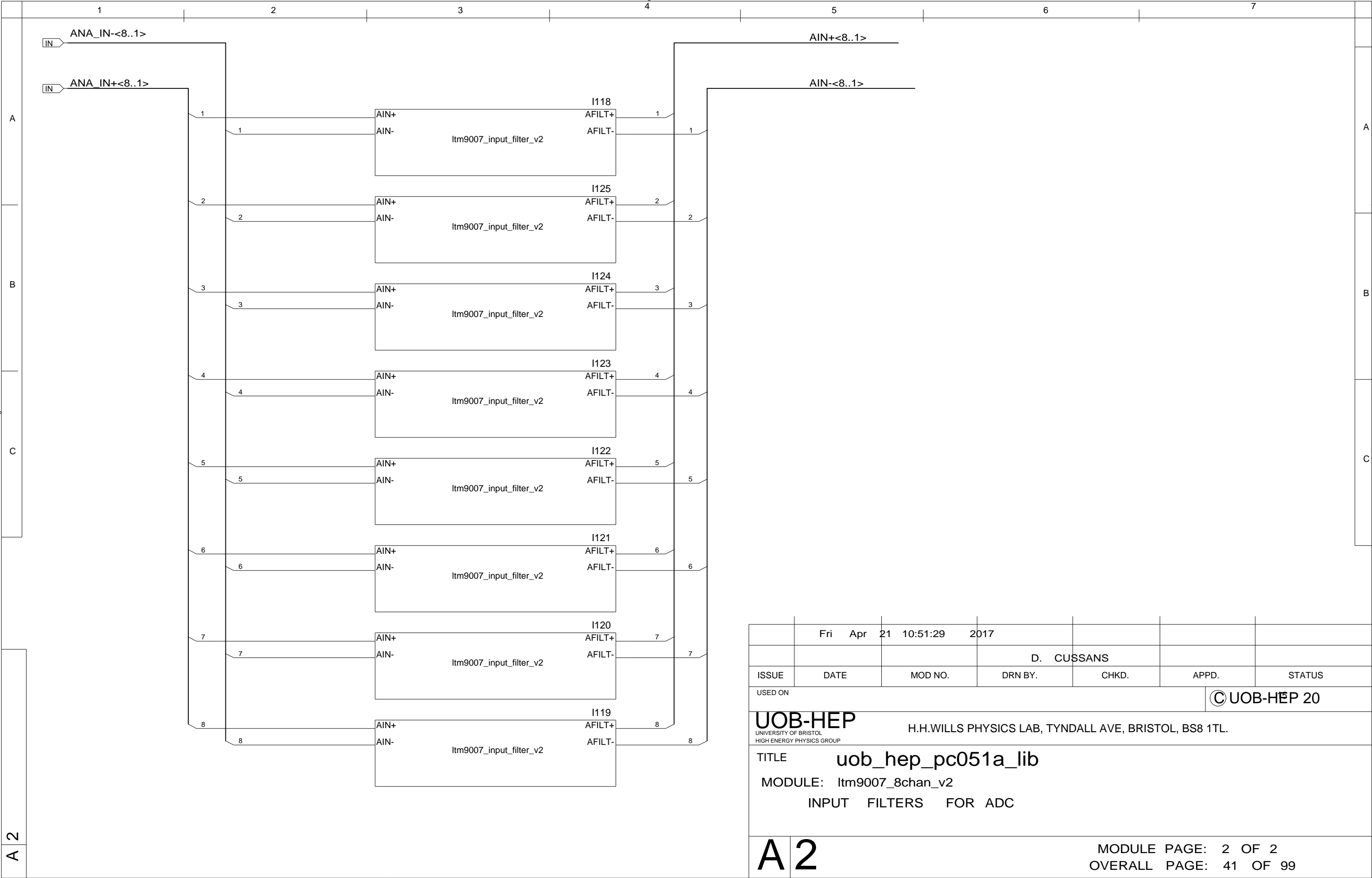
50V

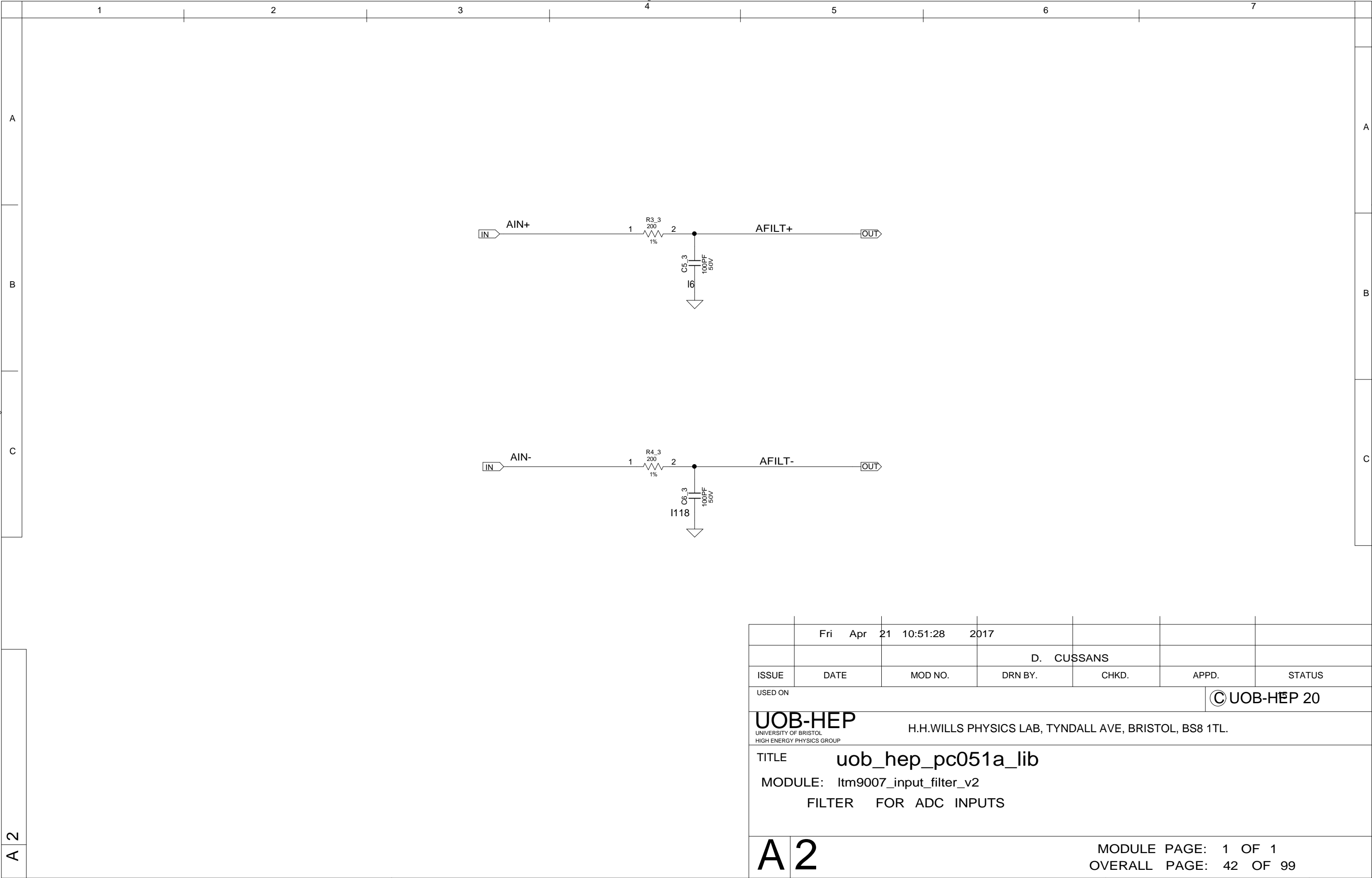
I118

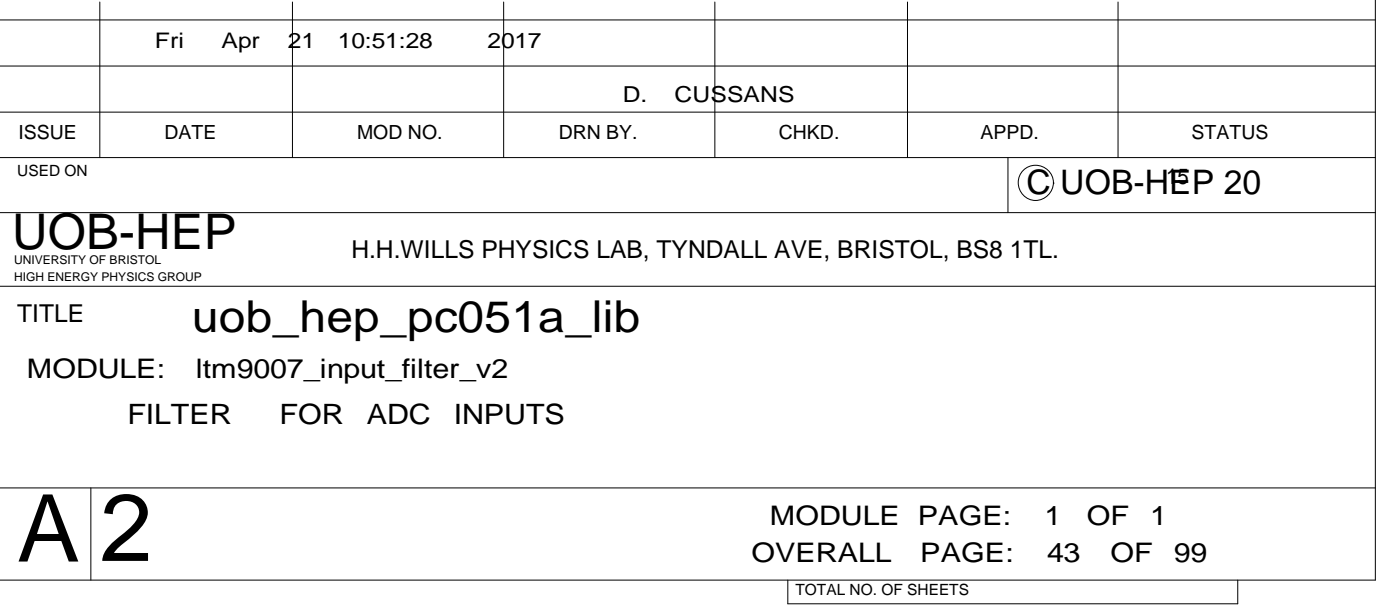
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TITLE		uob_hep_pc051a_lib						
MODULE:		Itm9007_input_filter_v2						
		FILTER FOR ADC INPUTS						
A 2		MODULE PAGE: 1 OF 1 OVERALL PAGE: 39 OF 99						

TOTAL NO. OF SHEETS









1234567

A

B

C

A 2

AIN+

IN

1

R6_3
200
1%

2

C14_3
100PF
50V
I6

AFILT+

OUT

AIN-

IN

1

R13_3
200
1%

2

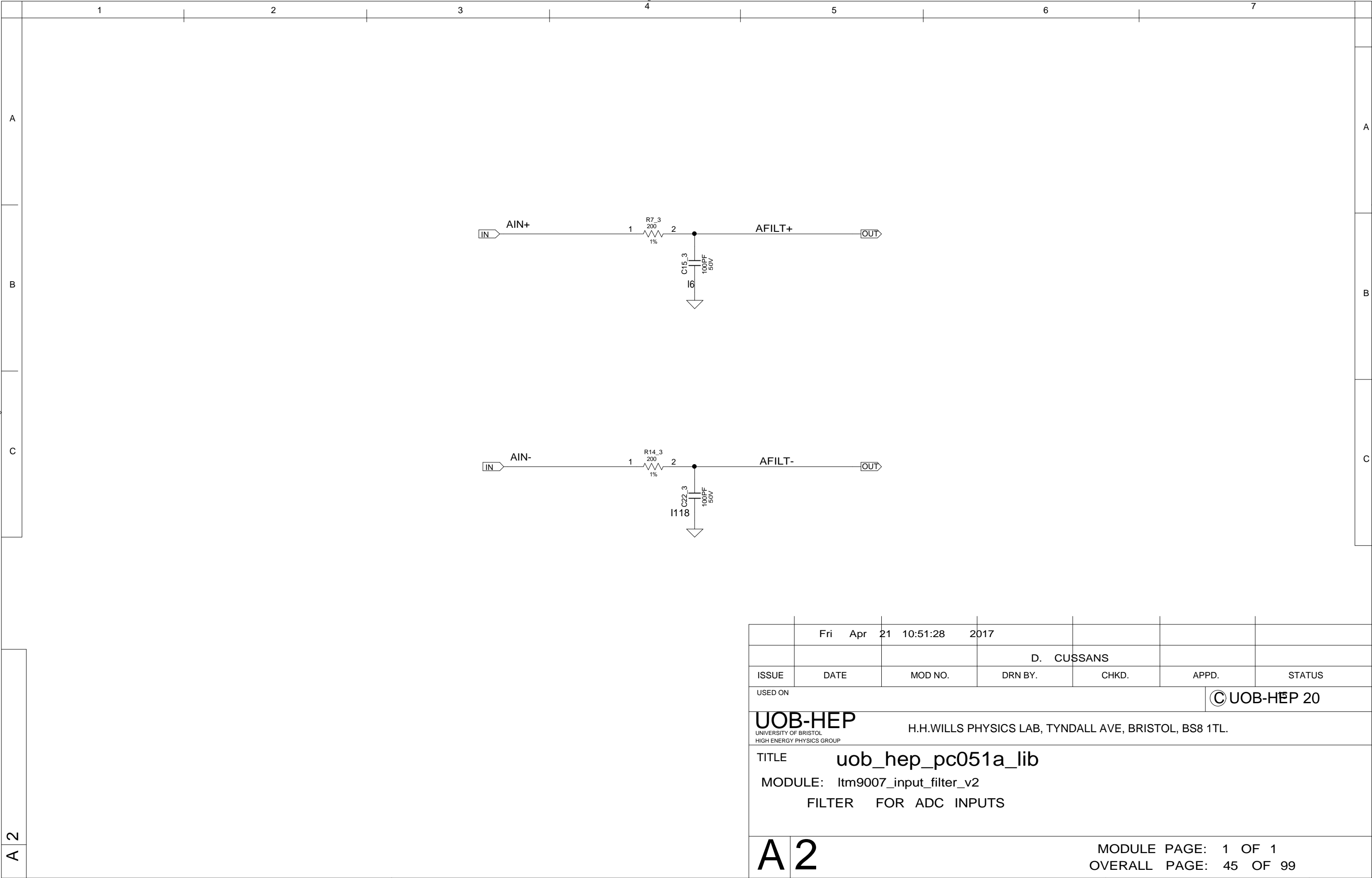
C21_3
100PF
50V
I118

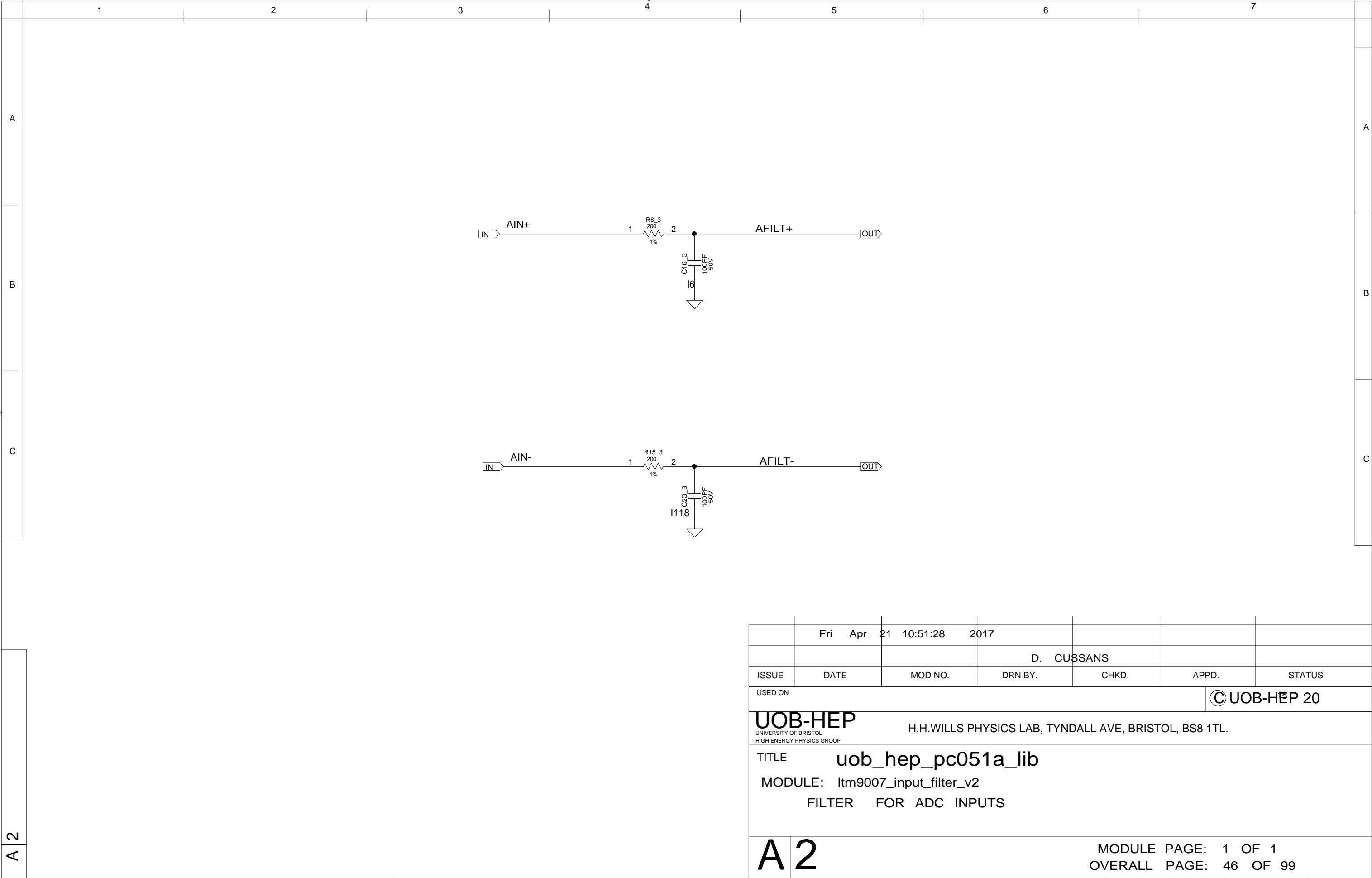
AFILT-

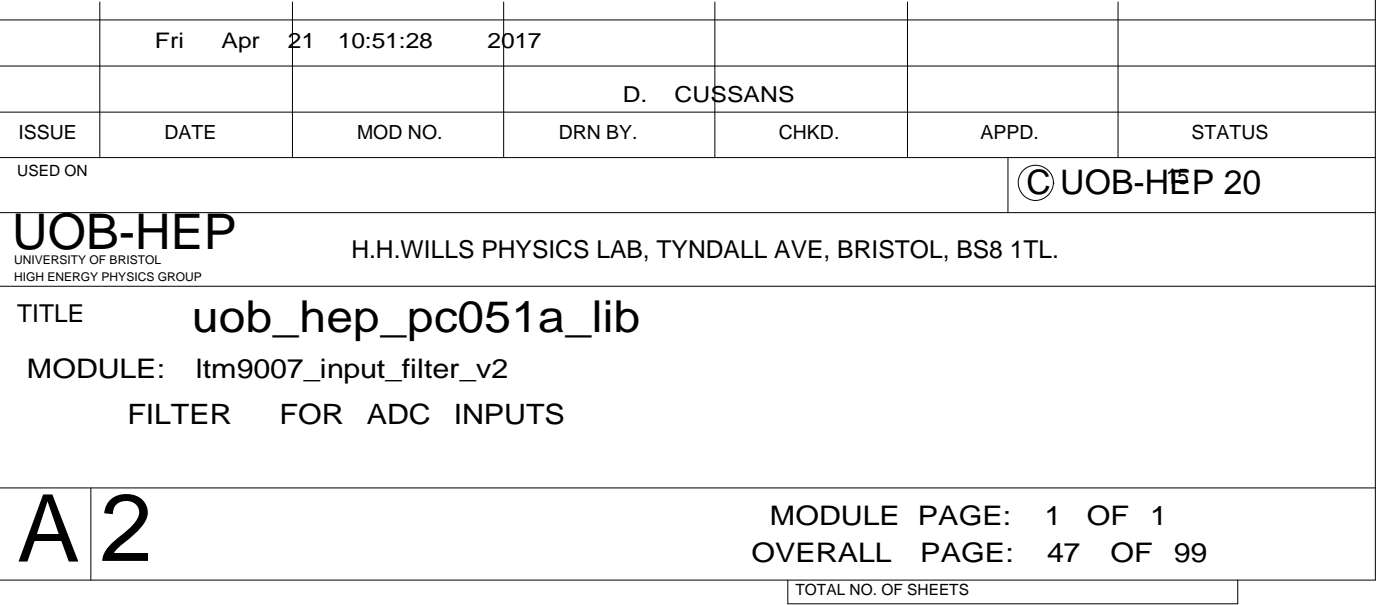
OUT

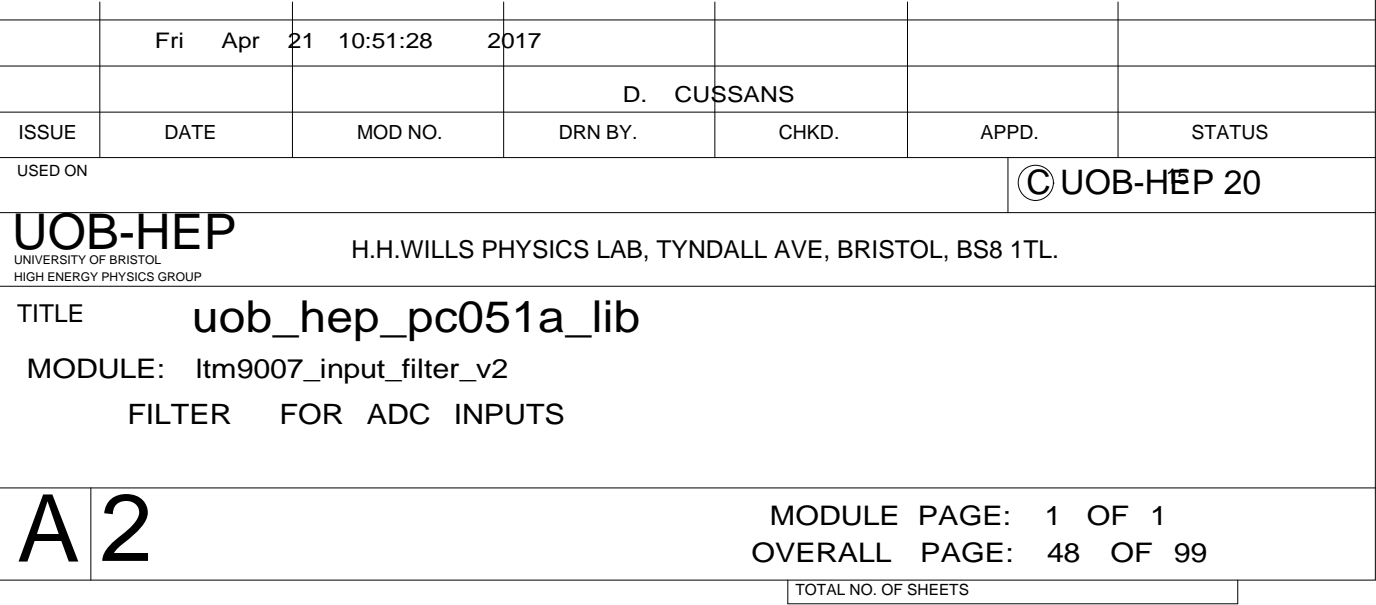
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MODULE:		Itm9007_input_filter_v2						
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A 2		MODULE PAGE: 1 OF 1 OVERALL PAGE: 44 OF 99						

TOTAL NO. OF SHEETS









1234567

A

B

C

A 2

AIN+

IN

1

R11_3
200
1%

2

AFILT+

OUT

C19_3
100PF
50V
16

AIN-

IN

1

R18_3
200
1%

2

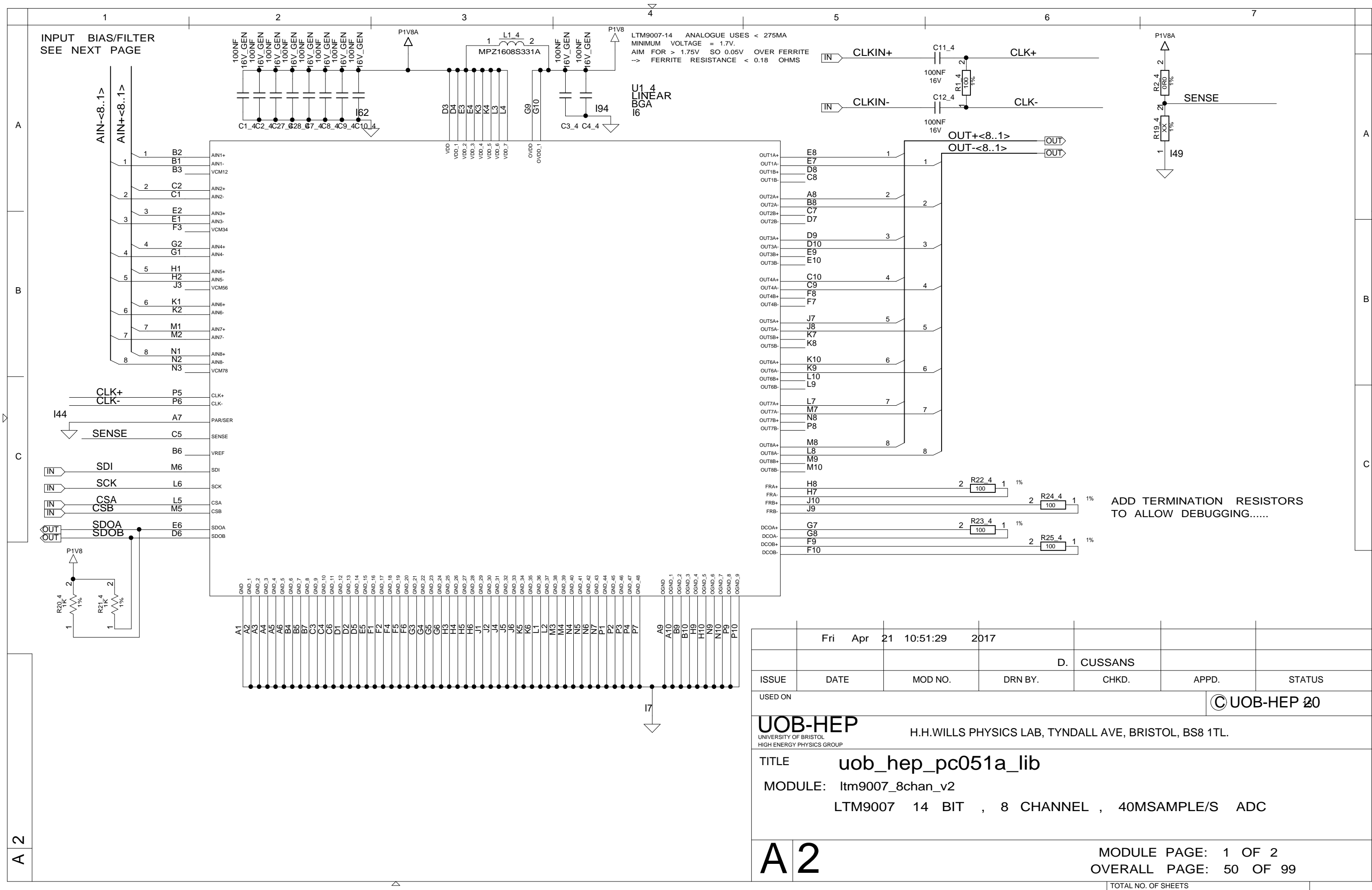
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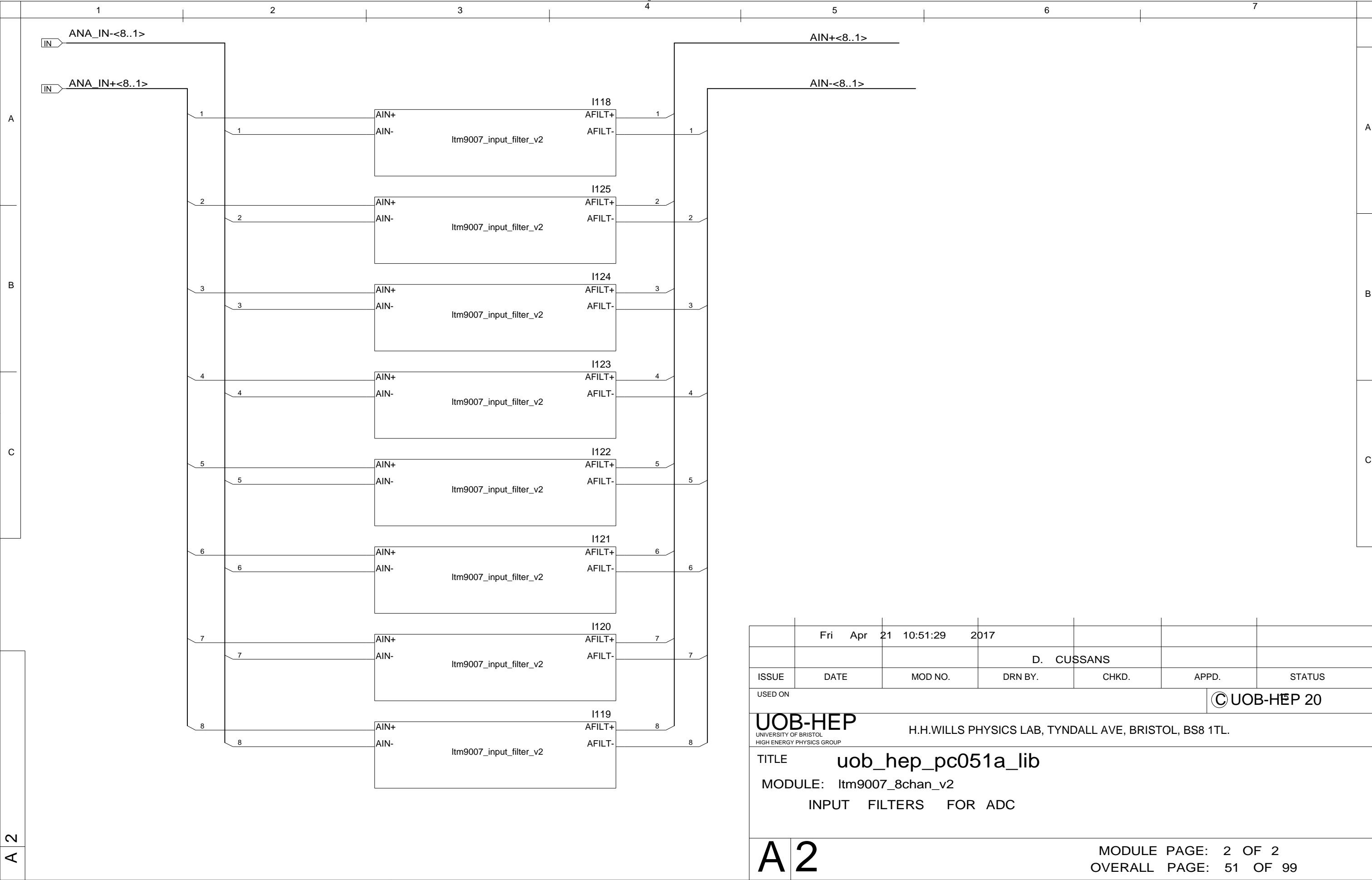
OUT

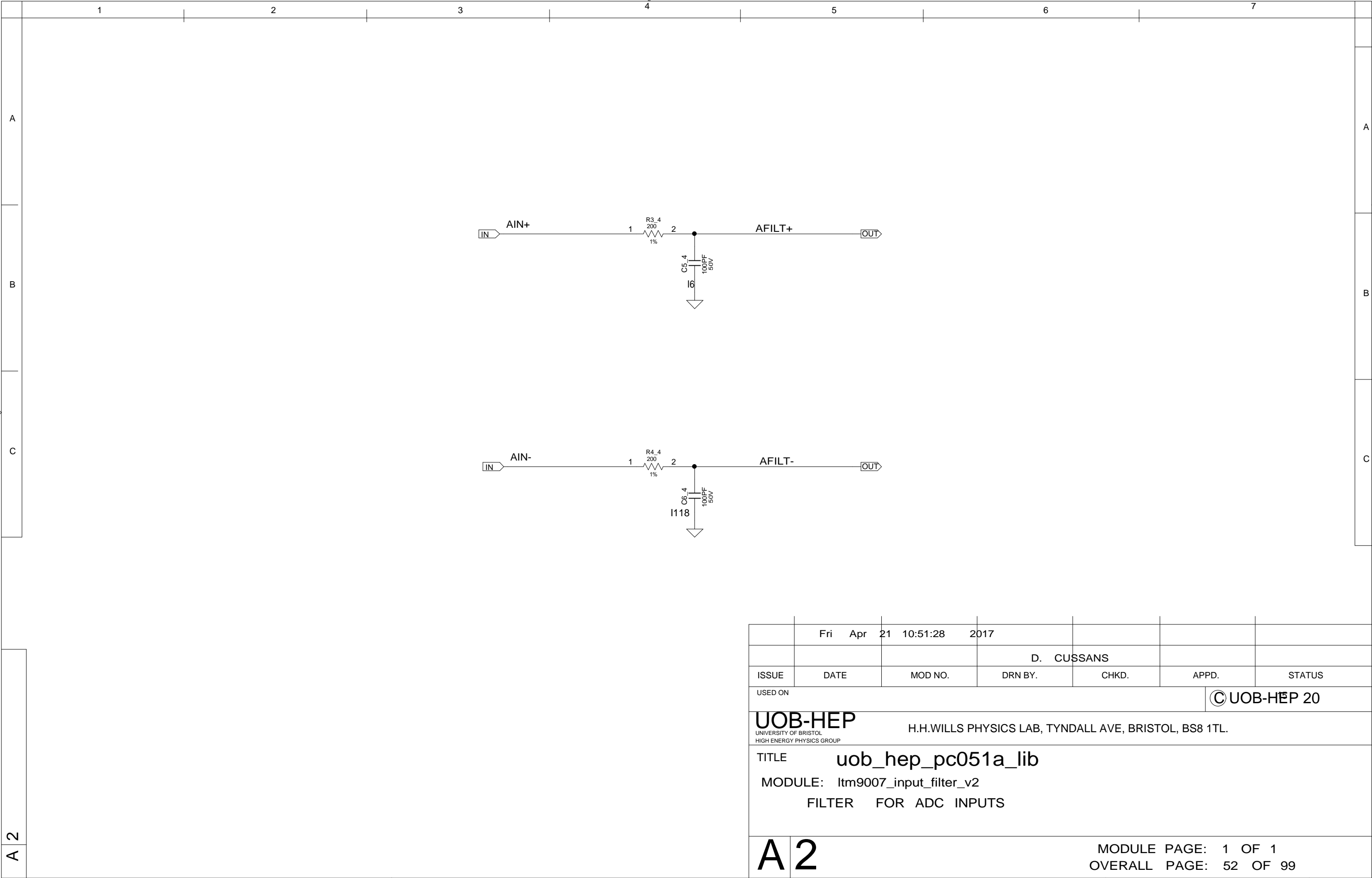
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100PF
50V
1118

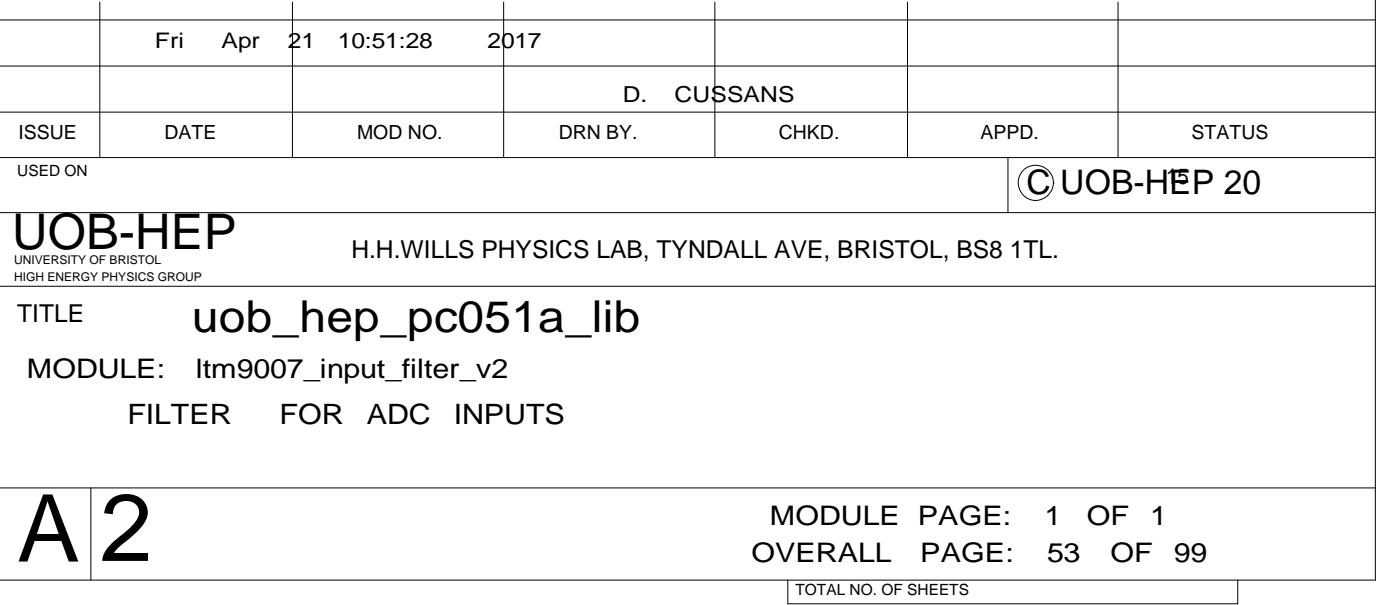
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A 2		MODULE PAGE: 1 OF 1 OVERALL PAGE: 49 OF 99						

TOTAL NO. OF SHEETS









1234567

A

B

C

A 2

AIN+

IN

1

R6_4
200
1%

2

AFILT+

OUT

C14_4
100PF
50V
I6

AIN-

IN

1

R13_4
200
1%

2

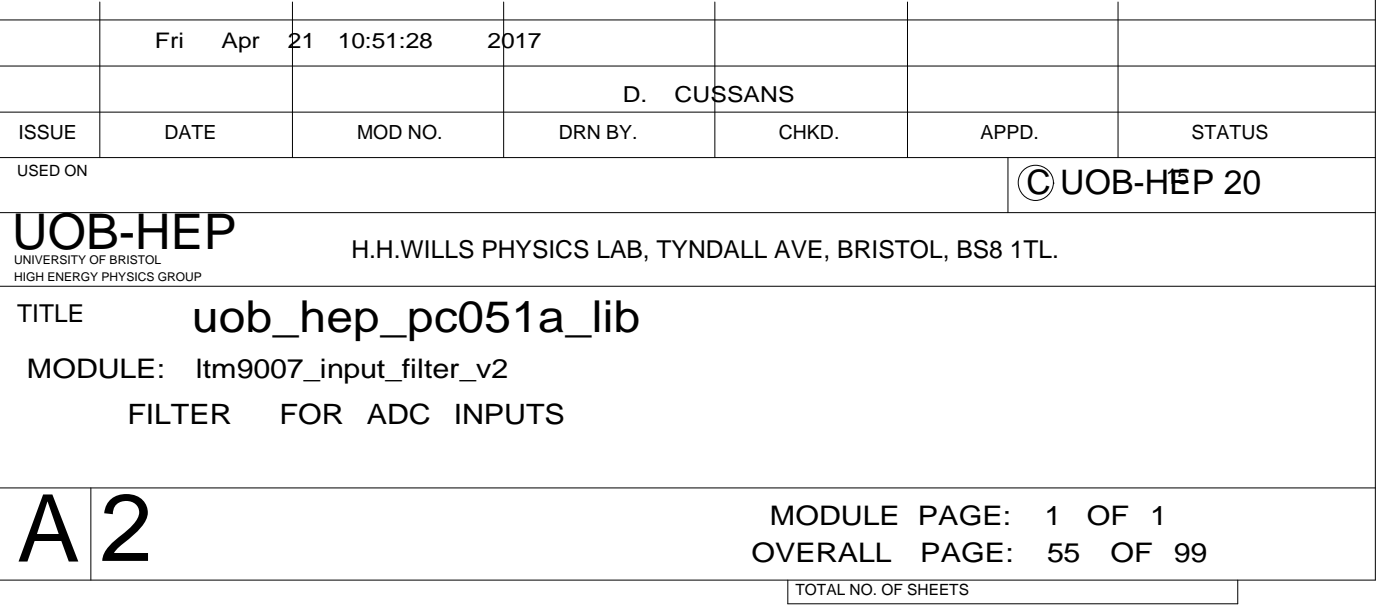
AFILT-

OUT

C21_4
100PF
50V
I118

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A 2		MODULE PAGE: 1 OF 1 OVERALL PAGE: 54 OF 99						

TOTAL NO. OF SHEETS



1234567

A

B

C

A 2

AIN+

IN

1

R9_4
200
1%

2

AFILT+

OUT

C17_4
100PF
50V
I6

AIN-

IN

1

R16_4
200
1%

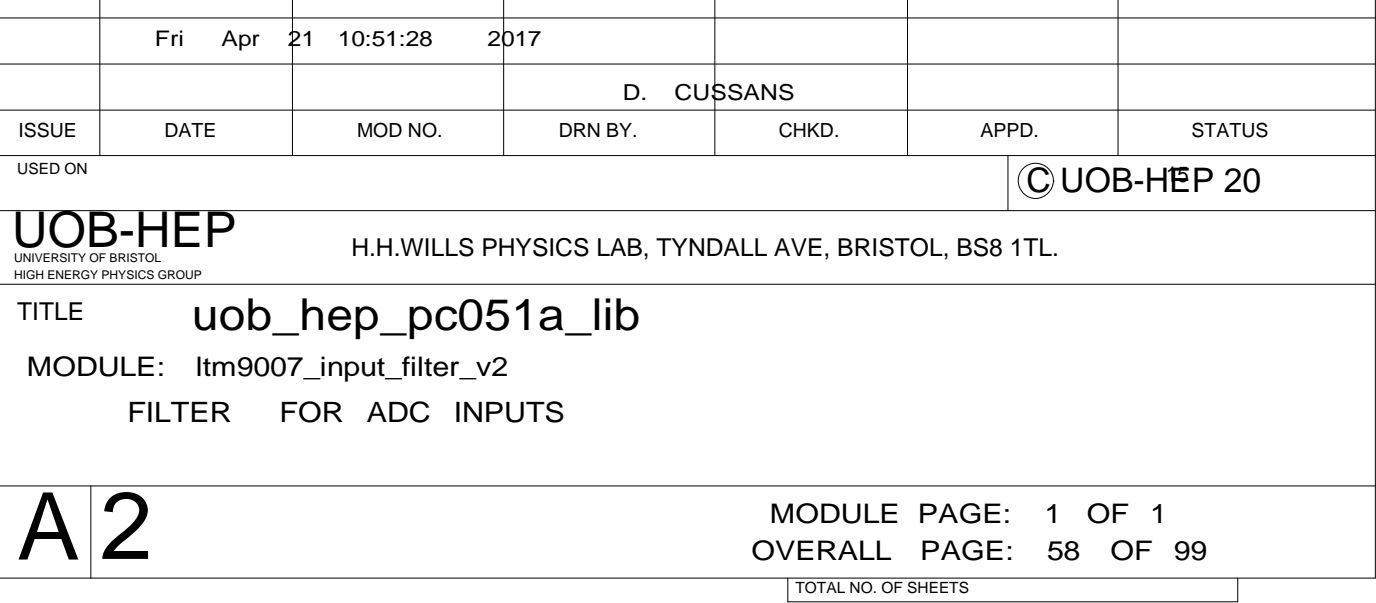
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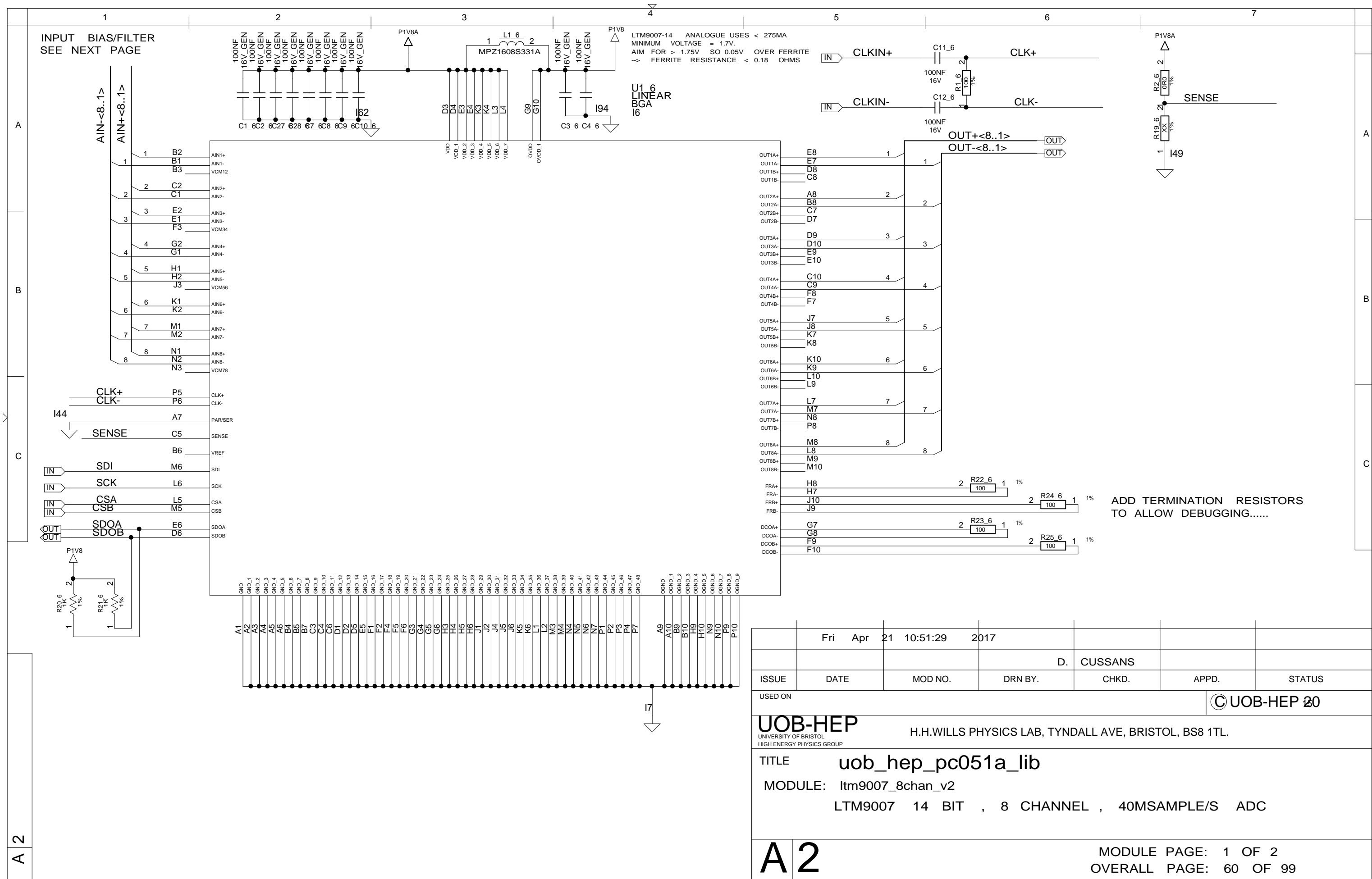
AFILT-

OUT

C24_4
100PF
50V
I118

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TITLE			uob_hep_pc051a_lib					
MODULE:			Itm9007_input_filter_v2					
			FILTER FOR ADC INPUTS					
A 2			MODULE PAGE: 1 OF 1 OVERALL PAGE: 57 OF 99					
TOTAL NO. OF SHEETS								







1234567

A

B

C

A 2

AIN+

IN

1

R3_6
200
1%

2

C5_6
100PF
50V
I6

AFILT+

OUT

AIN-

IN

1

R4_6
200
1%

2

C6_6
100PF
50V
I118

AFILT-

OUT

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A 2			MODULE PAGE: 1 OF 1 OVERALL PAGE: 62 OF 99					

TOTAL NO. OF SHEETS

