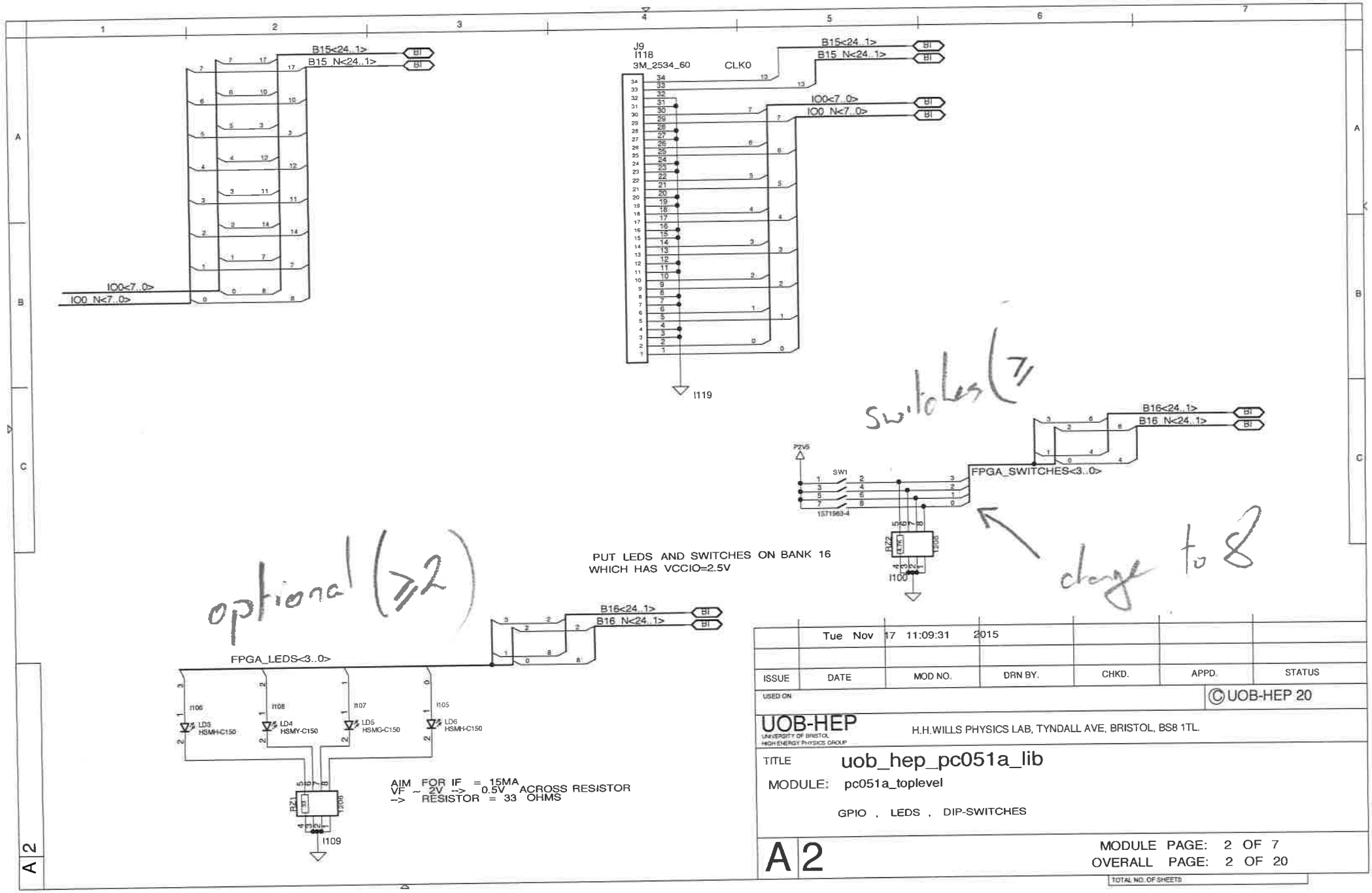

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TITLE uob_hep_pc051a_lib

MODULE: pc051a_toplevel

GPIO , LEDS , DIP-SWITCHES

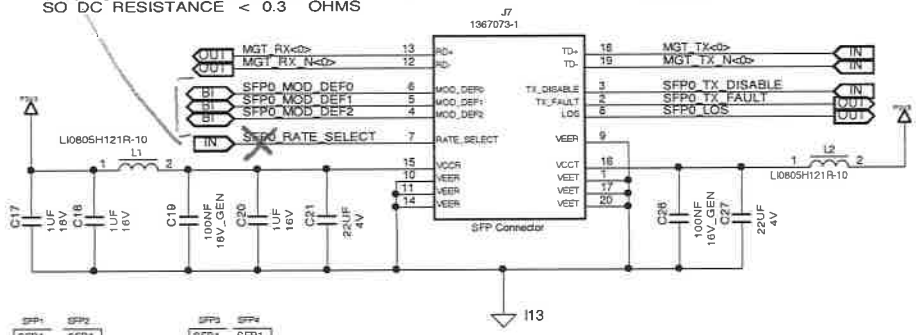
A2

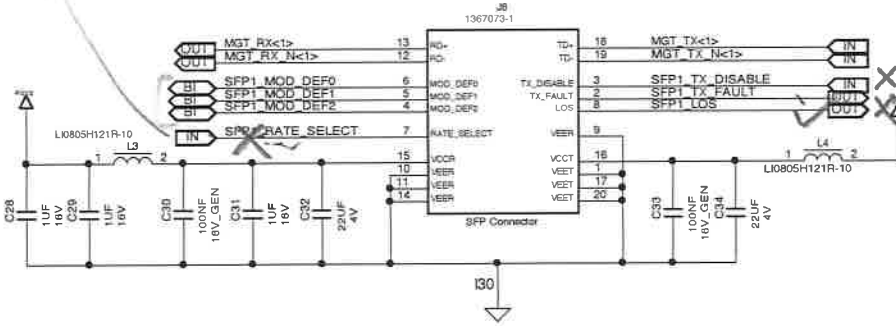
MODULE PAGE: 2 OF 7
OVERALL PAGE: 2 OF 20

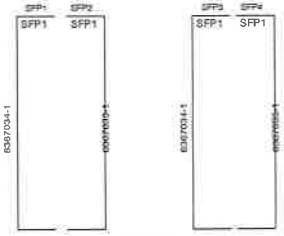
TOTAL NO. OF SHEETS

SFP POWER CONSUMPTION UP TO 1W
TRY TO LIMIT DROP OVER FILTER TO < 0.1V
SO DC RESISTANCE < 0.3 OHMS

ORIENT CONNECTOR WITH
PINS 1-10 TOWARDS BEZEL
USE WITH A TYCO SFP CAGE
6367035-1





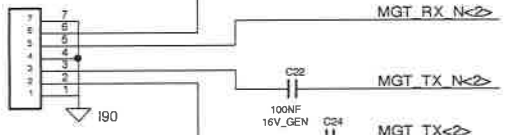


SFP CAGES IN TWO PARTS
SEPARATE FROM SFP CONNECTOR

GND=GND_SIGNAL GND=GND_SIGNAL
GND=GND_SIGNAL GND=GND_SIGNAL

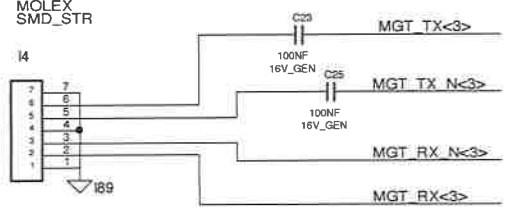
MGT_TX<3..0> OUT
MGT_TX_N<3..0> OUT
MGT_RX<3..0> IN
MGT_RX_N<3..0> IN

NORMAL CONNECTIONS
(MASTER) U1
MOLEX
SMD_STR
I8



TX/RX SWAPPED (SLAVE)

U2
MOLEX
SMD_STR
I4



CROSS-OVER MEANS
CAN PLUG MASTER INTO SLAVE
WITH NORMAL SATA CABLE

SATA CONNECTORS

ONE NORMAL CONNECTIONS (MASTER)
OTHER WITH RX/TX SWAPPED (SLAVE)

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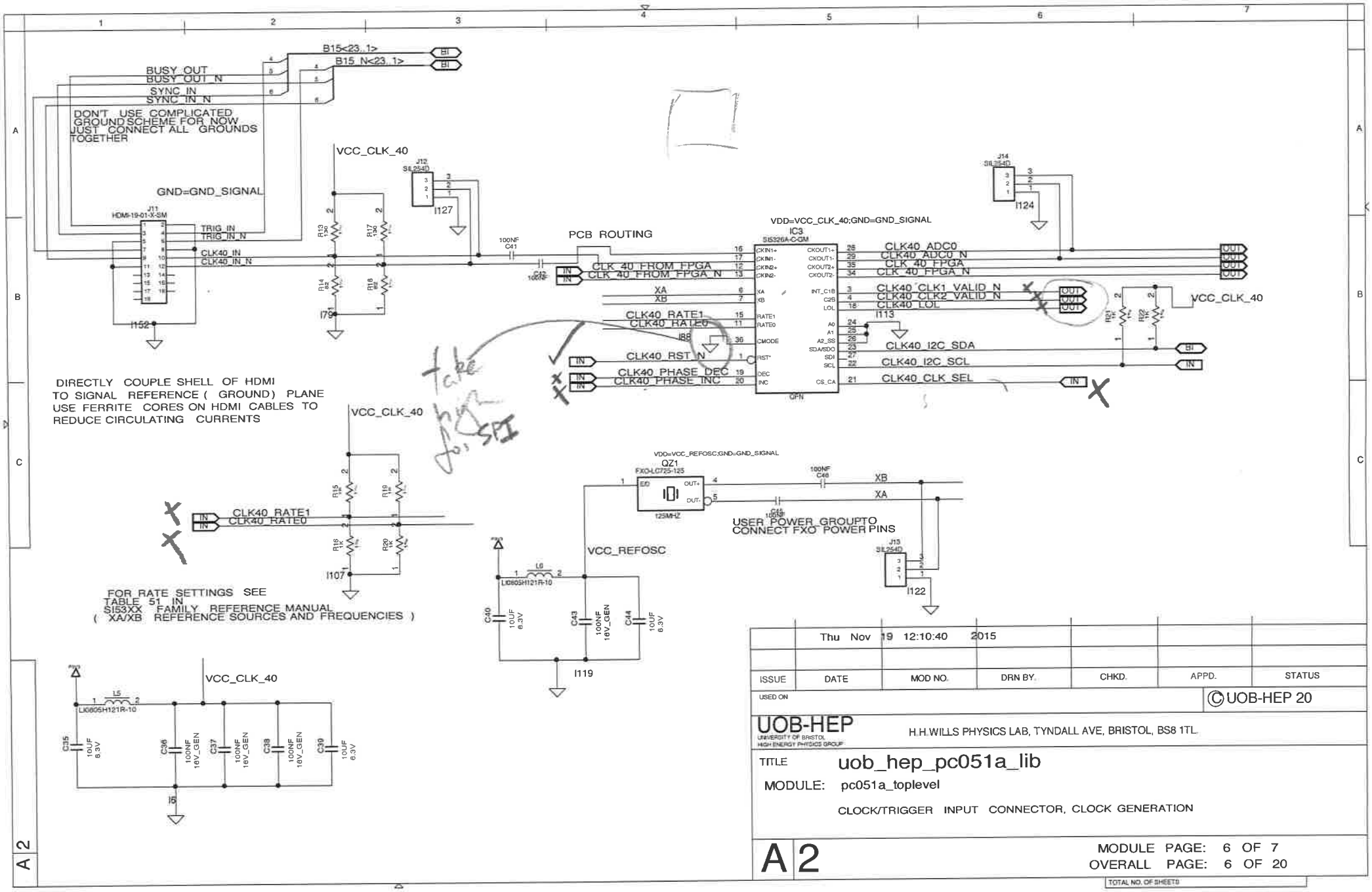
TITLE uob_hep_pc051a_lib

MODULE: pc051a_toplevel
GIGABIT SERIAL LINKS
TWO SFP , TWO ESATA

A2

MODULE PAGE: 5 OF 7
OVERALL PAGE: 5 OF 20

TOTAL NO. OF SHEETS



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TITLE uob_hep_pc051a_lib

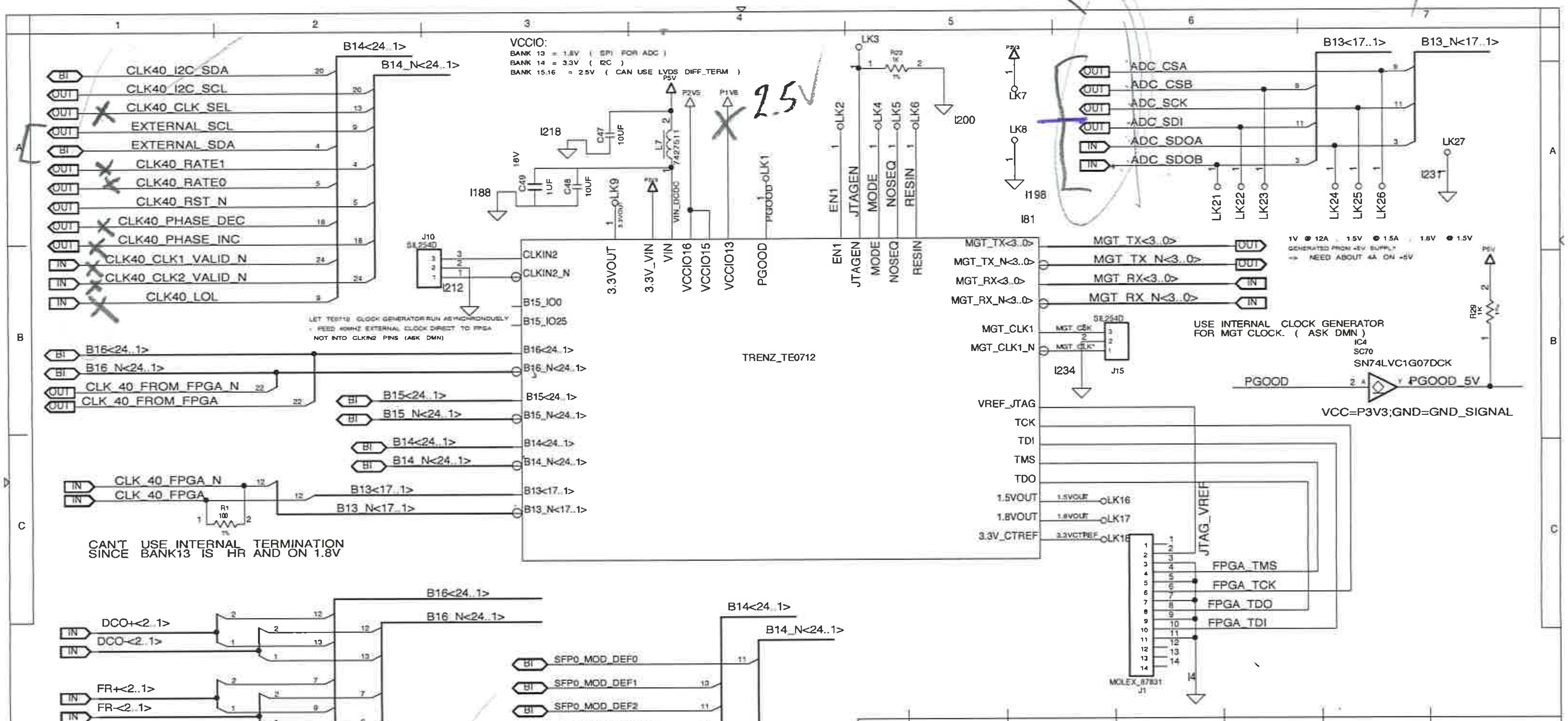
MODULE: pc051a_toplevel

CLOCK/TRIGGER INPUT CONNECTOR, CLOCK GENERATION

A2

MODULE PAGE: 6 OF 7
OVERALL PAGE: 6 OF 20

TOTAL NO. OF SHEETS



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TITLE uob_hep_pc051a_lib

MODULE: pc051a_toplevel

TRENZ TE0712 MODULE

A2

MODULE PAGE: 7 OF 7

OVERALL PAGE: 7 OF 20

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