MACHINE
BIT_VECTOR_DEFINITION

SEES
BIT_DEFINITION

CONSTANTS

BIT_VECTOR,
bv_catenate,
bv_sub,
bv_zero,
bv_one,
bv_size,
bv_not,
bv_and,
bv_or,
bv_xor,
bv_get,
bv_set,
bv_clear,
bv_put,
bv_index

PROPERTIES
$BIT\_VECTOR = \mathbf{seq1}(BIT)$

$\wedge\ bv\_size \in BIT\_VECTOR \rightarrow \mathcal{N}_1$
$\wedge\ bv\_size = \lambda\ (bv).(bv \in BIT\_VECTOR \mid \mathbf{size}(bv))$

$\wedge\ bv\_size \in BIT\_VECTOR \rightarrow \mathcal{N}_1$
$\wedge\ bv\_size = \lambda\ (bv).(bv \in BIT\_VECTOR \mid \mathbf{size}(bv))$

$\wedge\ bv\_catenate \in BIT\_VECTOR \times BIT\_VECTOR \rightarrow BIT\_VECTOR$
$\wedge\ bv\_catenate = \lambda\ (v1,v2)\ .\ (v1 \in BIT\_VECTOR \wedge v2 \in BIT\_VECTOR \mid v1 \frown v2)$

$\wedge\ bv\_index \in BIT\_VECTOR \rightarrow \mathcal{P}\ (\ \mathcal{N}\ )$
$\wedge\ bv\_index = \lambda\ (v1)\ .\ (v1 \in BIT\_VECTOR \mid 0\ ..\ (bv\_size(v1)\text{-}1))$

$\wedge\ bv\_sub \in BIT\_VECTOR \times \mathcal{N} \times \mathcal{N} \rightarrow BIT\_VECTOR$
$\wedge\ bv\_sub = \lambda\ (bv,low,high)\ .\ (bv \in BIT\_VECTOR \wedge low \in 0\ ..\ (bv\_size(bv)\text{-}1) \wedge high \in low\ ..\ (bv\_size(bv)\text{-}1)$
$\mid \lambda\ (i0).(i0 \in 1\ ..\ (high\text{-}low{+}1) \mid bv(i0{+}low)\ ))$

$\wedge\ bv\_zero \in \mathcal{N}_1 \rightarrow BIT\_VECTOR$
$\wedge\ bv\_zero = \lambda\ (sz)\ .\ (sz \in \mathcal{N}_1 \mid (1\ ..\ sz) \times \{0\})$

$\wedge\ bv\_one \in \mathcal{N}_1 \rightarrow BIT\_VECTOR$

$\wedge\ bv\_one = \lambda\ (sz)\ .\ (sz \in \mathcal{N}_1 \mid (1\ ..\ sz) \times \{1\})$

$\wedge\ bv\_not \in BIT\_VECTOR \rightarrow BIT\_VECTOR$
$\wedge\ bv\_not = \lambda\ (v1).(v1 \in BIT\_VECTOR \mid \lambda\ (idx).(idx \in 1\ ..\ bv\_size(v1) \mid bit\_not(v1(idx))))$

$\wedge\ bv\_and \in BIT\_VECTOR \times BIT\_VECTOR \rightarrow BIT\_VECTOR$
$\wedge\ bv\_and = \lambda\ (v1,v2).(v1 \in BIT\_VECTOR \wedge v2 \in BIT\_VECTOR \wedge bv\_size(v1) = bv\_size(v2) \mid$
$\lambda\ (idx).(idx \in 1\ ..\ bv\_size(v1) \mid bit\_and(v1(idx),v2(idx))))$

$\wedge\ bv\_or \in BIT\_VECTOR \times BIT\_VECTOR \rightarrow BIT\_VECTOR$
$\wedge\ bv\_or = \lambda\ (v1,v2).(v1 \in BIT\_VECTOR \wedge v2 \in BIT\_VECTOR \wedge bv\_size(v1) = bv\_size(v2) \mid$
$\lambda\ (idx).(idx \in 1\ ..\ bv\_size(v1) \mid bit\_or(v1(idx),v2(idx))))$

$\wedge\ bv\_xor \in BIT\_VECTOR \times BIT\_VECTOR \rightarrow BIT\_VECTOR$
$\wedge\ bv\_xor = \lambda\ (v1,v2).(v1 \in BIT\_VECTOR \wedge v2 \in BIT\_VECTOR \wedge bv\_size(v1) = bv\_size(v2) \mid$
$\lambda\ (idx).(idx \in 1\ ..\ bv\_size(v1) \mid bit\_xor(v1(idx),v2(idx))))$

$\wedge\ bv\_get \in BIT\_VECTOR \times \mathcal{N} \rightarrow BIT$
$\wedge\ bv\_get = \lambda\ (v1,idx).(v1 \in BIT\_VECTOR \wedge idx \in 0\ ..\ (bv\_size(v1)\text{-}1) \mid v1(idx+1))$

$\wedge\ bv\_set \in BIT\_VECTOR \times \mathcal{N} \rightarrow BIT\_VECTOR$
$\wedge\ bv\_set = \lambda\ (v1,idx).(v1 \in BIT\_VECTOR \wedge idx \in 0\ ..\ (bv\_size(v1)\text{-}1) \mid v1 \leftarrow \{\ (idx+1) \mapsto 1\ \})$

$\wedge\ bv\_clear \in BIT\_VECTOR \times \mathcal{N} \rightarrow BIT\_VECTOR$
$\wedge\ bv\_clear = \lambda\ (v1,idx).(v1 \in BIT\_VECTOR \wedge idx \in 0\ ..\ (bv\_size(v1)\text{-}1) \mid v1 \leftarrow \{\ (idx+1) \mapsto 0\ \})$

$\wedge\ bv\_put \in BIT\_VECTOR \times \mathcal{N} \times BIT \rightarrow BIT\_VECTOR$
$\wedge\ bv\_put = \lambda\ (v1,idx,bit).(v1 \in BIT\_VECTOR \wedge idx \in 0\ ..\ (bv\_size(v1)\text{-}1) \wedge bit \in BIT \mid v1 \leftarrow \{\ (idx+1) \mapsto bit\ \})$

ASSERTIONS

$\forall\ bv.(bv \in BIT\_VECTOR \Rightarrow bv\_size(bv\_not(bv)) = bv\_size(bv));$

$\forall\ (bv,indx).(bv \in BIT\_VECTOR \wedge indx \in 0\ ..\ (bv\_size(bv)\text{-}1) \Rightarrow (bv\_get(bv\_not(bv\_not(bv)),indx) = bv\_get(bv,indx)\ ));$

$\forall\ (v1,v2).\ (v1 \in BIT\_VECTOR \wedge v2 \in BIT\_VECTOR \Rightarrow$
$bv\_size(bv\_catenate(v1,\ v2)) = bv\_size(v1) + bv\_size(v2));$

$\forall\ (bv,low,high).\ (bv \in BIT\_VECTOR \wedge low \in 0\ ..\ (bv\_size(bv)\text{-}1) \wedge high \in 0\ ..\ (bv\_size(bv)\text{-}1) \wedge low \leq high \Rightarrow$
$bv\_size(bv\_sub(bv,\ low,\ high)) = high\text{-}low);$

$\forall\ (v1,v2).\ (v1 \in BIT\_VECTOR \wedge v2 \in BIT\_VECTOR \wedge bv\_size(v1) = bv\_size(v2) \Rightarrow$
$bv\_size(bv\_and(v1,\ v2)) = bv\_size(v2));$

$\forall$ (*v1*,*v2*,*indx*). (*v1* $\in$ *BIT_VECTOR* $\wedge$ *v2* $\in$ *BIT_VECTOR* $\wedge$ *bv_size*(*v1*) = *bv_size*(*v2*) $\wedge$ *indx* $\in$ 0 .. (*bv_size*(*v1*)-1) $\Rightarrow$
(*bv_get*(*bv_and*(*v1*, *v2*),*indx*) = *bv_get*(*bv_and*(*v2*, *v1*),*indx*)));

$\forall$ (*v1*,*v2*,*v3*,*indx*). (*v1* $\in$ *BIT_VECTOR* $\wedge$ *v2* $\in$ *BIT_VECTOR* $\wedge$ *v3* $\in$ *BIT_VECTOR* $\wedge$ *bv_size*(*v1*)=*bv_size*(*v2*) $\wedge$ *bv_size*(*v1*)=*bv_size*(*v3*)
$\wedge$ *indx* $\in$ 0 .. (*bv_size*(*v1*)-1) $\Rightarrow$ (*bv_get*(*bv_and*(*v1*, *bv_and*(*v2*,*v3*)),*indx*) = *bv_get*(*bv_and*(*bv_and*(*v1*, *v2*), *v3*),*indx*)));

$\forall$ (*bv*,*indx*). (*bv* $\in$ *BIT_VECTOR* $\wedge$ *indx* $\in$ 0 .. (*bv_size*(*bv*)-1) $\Rightarrow$ (*bv_get*(*bv_and*(*bv*, *bv_zero*(*bv_size*(*bv*))),*indx*) =
bv_get(*bv_zero*(*bv_size*(*bv*)),*indx*)));

$\forall$ (*bv*,*indx*). (*bv* $\in$ *BIT_VECTOR* $\wedge$ *indx* $\in$ 0 .. (*bv_size*(*bv*)-1) $\Rightarrow$ (*bv_get*(*bv_and*(*bv*, *bv_one*(*bv_size*(*bv*))),*indx*) = *bv_get*(*bv*,*indx*)));

$\forall$ (*v1*,*v2*). (*v1* $\in$ *BIT_VECTOR* $\wedge$ *v2* $\in$ *BIT_VECTOR* $\wedge$ *bv_size*(*v1*) = *bv_size*(*v2*) $\Rightarrow$ *bv_size*(*bv_or*(*v1*, *v2*)) = *bv_size*(*v1*));

$\forall$ (*v1*,*v2*,*indx*). (*v1* $\in$ *BIT_VECTOR* $\wedge$ *v2* $\in$ *BIT_VECTOR* $\wedge$ *bv_size*(*v1*) = *bv_size*(*v2*) $\wedge$ *indx* $\in$ 0 .. (*bv_size*(*v1*)-1) $\Rightarrow$
(*bv_get*(*bv_or*(*v1*, *v2*),*indx*) = *bv_get*(*bv_or*(*v2*, *v1*),*indx*)));

$\forall$ (*v1*,*v2*). (*v1* $\in$ *BIT_VECTOR* $\wedge$ *v2* $\in$ *BIT_VECTOR* $\wedge$ *bv_size*(*v1*) = *bv_size*(*v2*) $\Rightarrow$
bv_size(*bv_or*(*v1*, *v2*)) = *bv_size*(*v2*));

$\forall$ (*v1*,*v2*,*v3*,*indx*). (*v1* $\in$ *BIT_VECTOR* $\wedge$ *v2* $\in$ *BIT_VECTOR* $\wedge$ *v3* $\in$ *BIT_VECTOR* $\wedge$ *bv_size*(*v1*) = *bv_size*(*v2*) $\wedge$ *bv_size*(*v1*) = *bv_size*(*v3*)
$\wedge$ *indx* $\in$ 0 .. (*bv_size*(*v1*)-1) $\Rightarrow$ (*bv_get*(*bv_or*(*v1*, *bv_or*(*v2*,*v3*)),*indx*) = *bv_get*(*bv_or*(*bv_or*(*v1*, *v2*), *v3*),*indx*)));

$\forall$ (*bv*,*indx*). (*bv* $\in$ *BIT_VECTOR* $\wedge$ *indx* $\in$ 0 .. (*bv_size*(*bv*)-1) $\Rightarrow$ (*bv_get*(*bv_or*(*bv*, *bv_one*(*bv_size*(*bv*))),*indx*) = *bv_get*(*bv_one*(*bv_size*(*bv*)),*indx*)));

$\forall$ (*bv*,*indx*). (*bv* $\in$ *BIT_VECTOR* $\wedge$ *indx* $\in$ 0 .. (*bv_size*(*bv*)-1) $\Rightarrow$ (*bv_get*(*bv_or*(*bv*, *bv_zero*(*bv_size*(*bv*))),*indx*) = *bv_get*(*bv*,*indx*)));

$\forall$ (*v1*,*v2*). (*v1* $\in$ *BIT_VECTOR* $\wedge$ *v2* $\in$ *BIT_VECTOR* $\wedge$ *bv_size*(*v1*) = *bv_size*(*v2*) $\Rightarrow$ *bv_size*(*bv_xor*(*v1*, *v2*)) = *bv_size*(*v1*));

$\forall$ (*v1*,*v2*). (*v1* $\in$ *BIT_VECTOR* $\wedge$ *v2* $\in$ *BIT_VECTOR* $\wedge$ *bv_size*(*v1*) = *bv_size*(*v2*) $\Rightarrow$ *bv_size*(*bv_xor*(*v1*, *v2*)) = *bv_size*(*v2*));

$\forall$ (*v1*,*v2*,*indx*). (*v1* $\in$ *BIT_VECTOR* $\wedge$ *v2* $\in$ *BIT_VECTOR* $\wedge$ *bv_size*(*v1*) = *bv_size*(*v2*) $\wedge$ *indx* $\in$ 0 .. (*bv_size*(*v1*)-1) $\Rightarrow$
(*bv_get*(*bv_xor*(*v1*, *v2*),*indx*) = *bv_get*(*bv_xor*(*v2*, *v1*),*indx*)));

$\forall$ (*bv*,*indx*). (*bv* $\in$ *BIT_VECTOR* $\wedge$ *indx* $\in$ 0 .. (*bv_size*(*bv*)-1) $\Rightarrow$ *bv_get*(*bv_xor*(*bv*,*bv*),*indx*) = *bv_get*(*bv_zero*(*bv_size*(*bv*)),*indx*))
END