

**Design model**

```
graph TD; A[Design model] -- refinement --> B[Algorithmic model]; B -- "code generation" --> C[Programming language code]; C -- "Compilation" --> D[Binary assembly code];
```

A vertical flowchart illustrating the compilation process. It starts with a rectangular box labeled 'Design model' with a starburst 'V' in the top right corner. An arrow points down to a parallelogram labeled 'refinement'. Another arrow points down to a second rectangular box labeled 'Algorithmic model', also with a starburst 'V' in the top right corner. A third arrow points down to a wide rectangular box labeled 'Programming language code'. A final arrow points down to a rectangular box labeled 'Binary assembly code'.

*refinement*

**Algorithmic model**

*code generation*

**Programming language code**

*Compilation*

Binary assembly code