

Debug Host

Debugger
(eg. gdb)

Debug Translator
(eg. OpenOCD)

Debug Transport
Hardware
(eg. JTAG debug probe)

RISC-V Platform

Debug Module (DM)

reset/halt
control

abstract
commands

bus access

Debug Module
Interface (DMI)

Debug Transport
Module (DTM)

RISC-V Core

Hardware Thread

Debug Mode

Hardware
Trigger
Module

Program
Buffer (4B-64B)

System
Bus

