

Z180 Serial (ASCI) Baud Rates by Clock Rate

		CPU Clock PHI (MHz)								
		6.144	7.373	9.216	12.288	14.746	18.432	24.576	29.491	36.864
Z180 ASCI Divisors	122880	50	60	75	100	120	150	200	240	300
	61440	100	120	150	200	240	300	400	480	600
	40960	150	180	225	300	360	450	600	720	900
	30720	200	240	300	400	480	600	800	960	1200
	20480	300	360	450	600	720	900	1200	1440	1800
	15360	400	480	600	800	960	1200	1600	1920	2400
	10240	600	720	900	1200	1440	1800	2400	2880	3600
	7680	800	960	1200	1600	1920	2400	3200	3840	4800
	5120	1200	1440	1800	2400	2880	3600	4800	5760	7200
	3840	1600	1920	2400	3200	3840	4800	6400	7680	9600
	2560	2400	2880	3600	4800	5760	7200	9600	11520	14400
	1920	3200	3840	4800	6400	7680	9600	12800	15360	19200
	1280	4800	5760	7200	9600	11520	14400	19200	23040	28800
	960	6400	7680	9600	12800	15360	19200	25600	30720	38400
	640	9600	11520	14400	19200	23040	28800	38400	46080	57600
	480	12800	15360	19200	25600	30720	38400	51200	61440	76800
	320	19200	23040	28800	38400	46080	57600	76800	92160	115200
	160	38400	46080	57600	76800	92160	115200	153600	184320	230400
		Serial Port Baud Rate								

ASCI Baud = CPU Clock (PHI) / ASCI Divisor