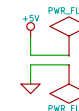
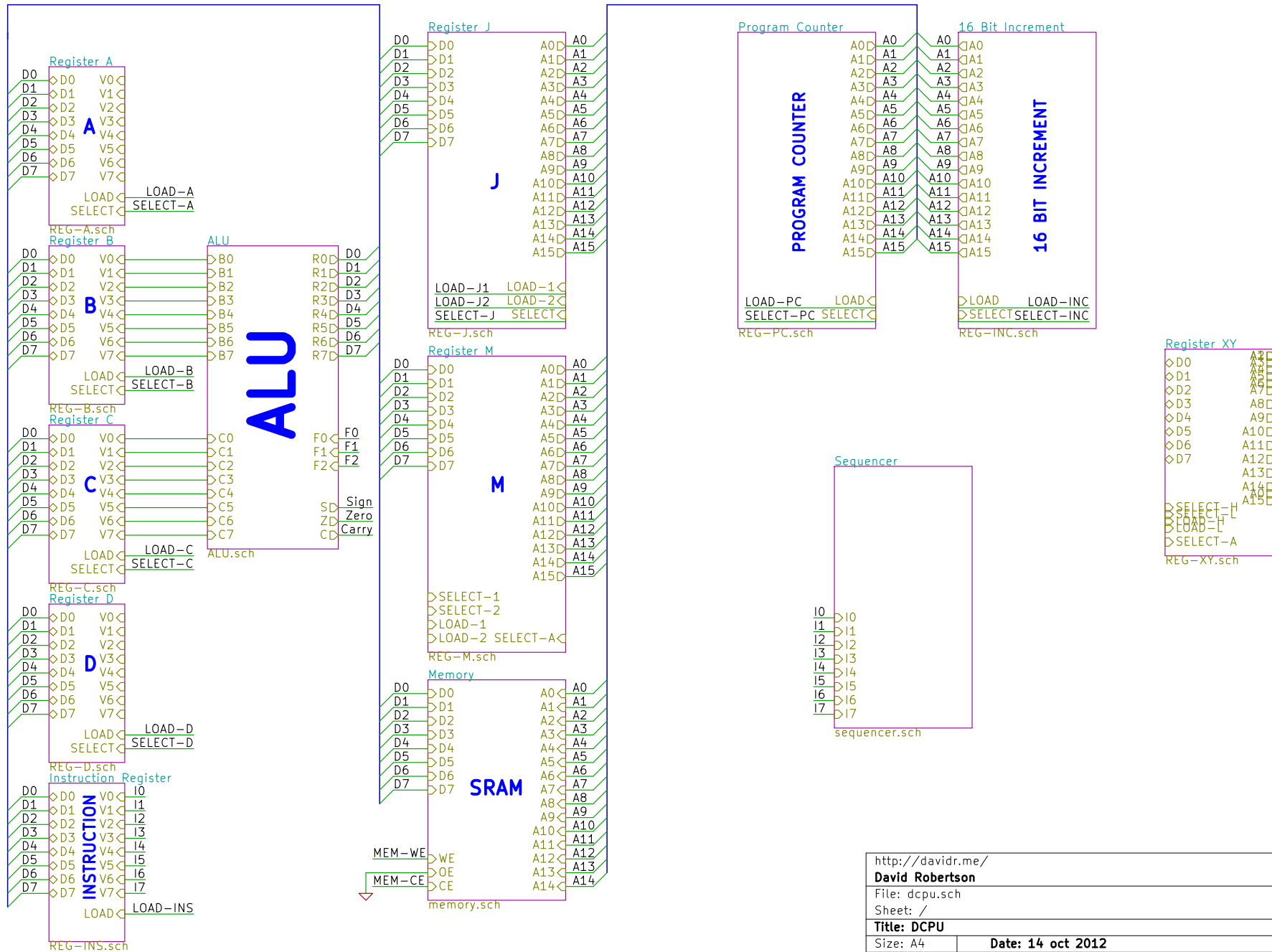


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Size: A4	Date: 14 oct 2012	Rev:
KiCad E.D.A.		Id: 2/16

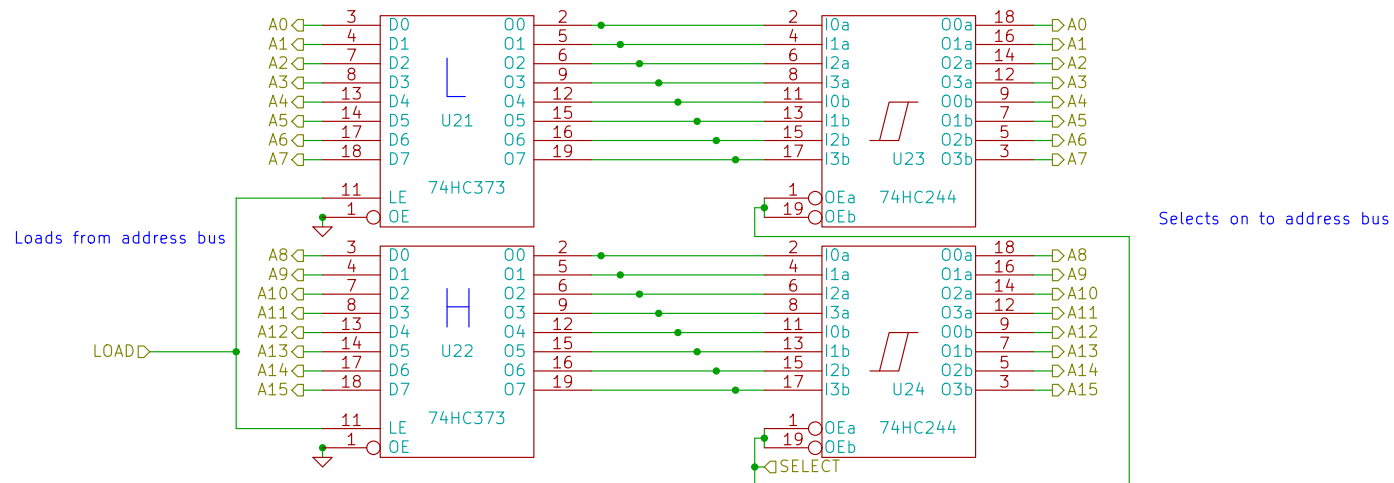
# DATA BUS

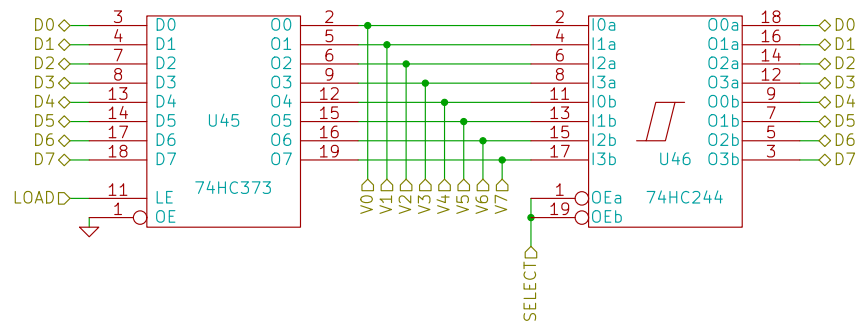
# ADDRESS BUS

All LOAD signals active HIGH  
All SELECT signals active LOW



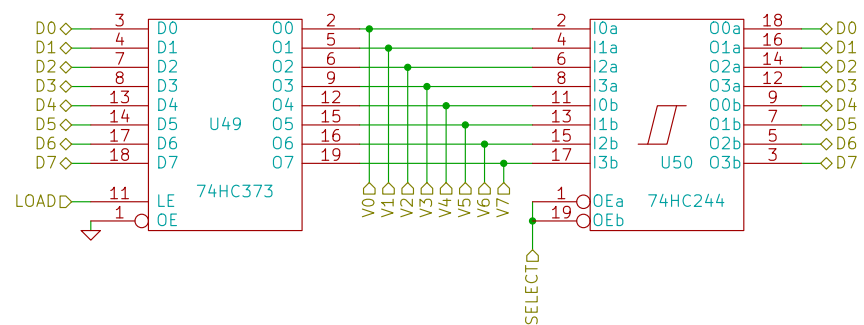
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David Robertson		
File: dcpu.sch		
Sheet: /		
Title: DCPU		
Size: A4	Date: 14 oct 2012	Rev: A
KiCad E.D.A.		Id: 1/16

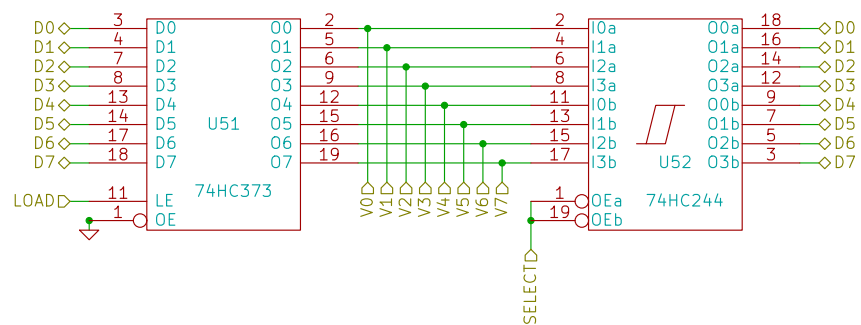












<a href="http://davidr.me/">http://davidr.me/</a>		
<b>David Robertson</b>		
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<b>Title: Register D</b>		
Size: A4	<b>Date: 14 oct 2012</b>	<b>Rev: A</b>
KiCad E.D.A.		Id: 14/16

Loads from data bus

Selects on to address bus

