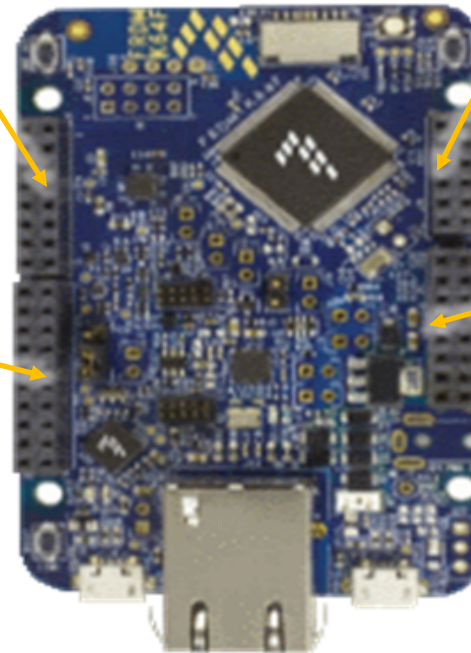


# Header Signal

J1 (odd)		
Pin No	Name	Signal
1	I2S_TX_BCLK	PTB18
3	I2S_TX_FS	PTB19
5	I2S_TXD	PTC1
7	I2S_MCLK	PTC8
9	I2S_RX_BCLK	PTC9
11	USB_SOF	PTC0
13	I2S_RX_FS	PTC7
15	I2S_RXD	PTC5

J2 (odd)		
Pin	Name	Signal
1	-	PTE26
3	-	GND
5	-	ADC0_DP0/ADC1_DP3
7	-	ADC0_DM0/ADC1_DM3
9	-	-
11	-	ADC1_DM0/ADC0_DM3
13	-	ADC1_DP0/ADC0_DP3
15	-	-
17	-	ADC1_SE18
19	-	-



J4 (odd)		
Pin No	Name	Signal
1	-	ADC0_DP1
3	-	ADC0_DM1
5	-	ADC1_DP1
7	-	ADC1_DM1
9	-	PTB20
11	-	DAC0_OUT/CMP1_IN3/ADC0_SE23

J3 (odd)		
Pin	Name	Signal
1	-	N.C.
3	-	N.C.
5	-	N.C.
7	-	N.C.
9	-	N.C.
11	-	N.C.
13	-	N.C.
15	-	N.C.