Kinetis	K64 (100qfp)	Pin Name	DEFAULT	ALT0	ALT1	ALT2	ALT3	ALT4	ALT5	ALT6	ALT7	EZPORT
1	1	PTEO/CLKOUT32K	ADC1_SE4a	ADC1_SE4a	PTEO/CLKOUT32K	SPI1_PCS1	UART1_TX	SDHC0_D1	TRACE_CLKOUT	I2C1_SDA	RTC_CLKOUT	
2	2	PTE1/LLWU_P0	ADC1_SE5a	ADC1_SE5a	PTE1/LLWU_P0	SPI1_SOUT	UART1_RX	SDHC0_D0	TRACE_D3	I2C1_SCL	SPI1_SIN	
3	3	PTE2/LLWU P1	ADCO DP2/ADC1 SE6a	ADCO DP2/ADC1 SE6a	PTE2/LLWU P1	SPI1 SCK	UART1 CTS b	SDHC0 DCLK	TRACE D2			
4	4	PTE3	ADCO DM2/ADC1 SE7a	ADCO DM2/ADC1 SE7a	PTE3	SPI1 SIN	UART1 RTS b	SDHC0 CMD	TRACE D1		SPI1 SOUT	
12	5	PTE4/LLWU_P2	DISABLED		PTE4/LLWU_P2	SPI1_PCS0	UART3_TX	SDHC0_D3	TRACE_D0			
13	6	PTES	DISABLED		PTES	SPI1_PCS2	UART3_RX	SDHC0_D2		FTM3_CH0		
14	7	PTE6	DISABLED		PTE6	SPI1 PCS3	UART3 CTS b	12SO MCLK		FTM3 CH1	USB0 SOF OUT	
23	8	VDD	VDD	VDD								
24	9	VSS	VSS	VSS								
33	10	USB0_DP	USBO_DP	USB0_DP								
34	11	USB0 DM	USB0 DM	USB0 DM								
35	12	VOUT33	VOUT33	VOUT33								
37	13	VREGIN	VREGIN	VREGIN								
53	14	ADCO_DP1	ADCO_DP1	ADCO_DP1								
54	15	ADCO DM1	ADC0_DM1	ADCO DM1								
55	16	ADC1_DP1	ADC1_DP1	ADC1_DP1								
56	17	ADC1_DM1	ADC1_DM1	ADC1_DM1								
59	18	ADCO DPO/ADC1 DP3	ADCO DPO/ADC1 DP3	ADCO DPO/ADC1 DP3								-
60	19	ADCO DMO/ADC1 DM3	ADCO DMO/ADC1 DM3	ADCO DMO/ADC1 DM3								-
61	20	ADC1_DP0/ADC0_DP3	ADC1_DP0/ADC0_DP3	ADC1_DP0/ADC0_DP3								
62	21	ADC1_DM0/ADC0_DM3	ADC1_DM0/ADC0_DM3	ADC1_DM0/ADC0_DM3								
63	22	VDDA	VDDA	VDDA								-
64	23	VREFH	VREFH	VREFH								-
66	24	VREFL	VREFL	VREFL								-
67	25	VSSA	VSSA	VSSA								
88	26 27	VREF OUT/CMP1 INS/CMP0 INS/ADC1 SE18	VREF OUT/CMP1 INS/CMP0 INS/ADC1 SE18	VREF OUT/CMP1 INS/CMP0 INS/ADC1 SE18								-
95		DACO_OUT/CMP1_IN3/ADCO_SE23	DACO_OUT/CMP1_IN3/ADCO_SE23	DACD_OUT/CMP1_IN3/ADC0_SE23								-
103	28	XTAL32	XTAL32	XTAL32								
104	29	EXTAL32	EXTAL32	EXTAL32								_
105	30	VBAT	VBAT	VBAT ADCO SE17	Aura.		UART4 TX		1200 000	FMBA OUT !		1
116	31 32	PTE24	ADCO_SE17	ADC0_SE17 ADC0_SF18	PTE24 PTE25		UART4_TX HART4_RX		12C0_SCL	EWM_OUT_b		-
117	32	PTE25 PTE26/CLKOUT32K	ADCO_SE18 DISABLED	ADC0_SE18	PTE25	ENEX 4500 G100			I2CO_SDA	EWM_IN BTC_CLKOUT	11000 011111	
118	33	PTE26/CLKOUT32K			PTE26/CLKOUT32K	ENET 1588 CLKIN UARTO CTS MUARTO COL M	UART4 CTS b			RTC CLKOUT	USBO CLKIN	FZP CLK
121 122		PTA0 PTA1	JTAG TCLK/SWD CLK/EZP CLK		PTAO		FTM0 CH5				JTAG TCLK/SWD CLK	EZP CLK
122	35	PTA1 PTA2	JTAG_TDI/EZP_DI JTAG_TDO/TRACE_SWO/EZP_DO		PTA1 PTA2	UARTO_RX UARTO TX	FTM0_CH6 FTM0_CH7				JIAG_TDI	EZP_DI EZP DO
123 124	36	PTA2 PTA3			PTA2 PTA3	UARTO_TX UARTO RTS b	FTM0_CH7 FTM0_CH0				JIAG_TDO/TRACE_SWO	EZP_DÔ
124 125	37 38	PTA3 PTA4/LLWU_P3	JTAG TMS/SWD DIO NMI_b/EZP_CS_b		PTA3	UARTO RTS b	FTM0 CH0 FTM0_CH1				JTAG TMS/SWD DIO NMI_b	EZP_CS_b
125	38	PIA4/LLWU_P3	NMI_B/EZP_CS_B		PTA4/LLWU_P3	11000 01100	FIMU_CH1	DATES OF THE OWEN	04403 0117	1200 701 2011	NMI D	EZP_CS_B
126	39 40	PTAS	DISABLED	LIND	PTAS	USBO_CLKIN	FTM0_CH2	KMIIU_KXER/MIIU_RXER	CMP2_OUT	12SO_TX_BCLK	JIAG_IRST_b	_
127		VDD	VDD	VDD								-
128 137	41	VSS PTA12	VSS CMP2_IN0	VSS CMP2 IN0	PTA12	CANO TX	FTM1 CH0	RMIIO RXD1/MIIO RXD1	I2C2 SCL	I2SO TXDO	FTM1 QD PHA	_
137	42	PTA12	CMP2_INU	CMP2_INU	PTA13/IIWII PA	CANU_IX	FTM1_CH0 FTM1 CH1	RMIIO RXDI/MIIO RXDI	12C2_SCL 12C2_SDA	1250_1XD0	FTM1_QD_PHA	-
138	43	PTA13/LLWU_P4	DISABLED	CMPZ_IN1	PTA13/LLWU_P4	SPIO PCSO	HARTO TX	RMIIO_RXDO/MIIO_RXDO	12C2 SCI	12SU_IX_FS	IZSO TXD1	
139	44	PTA14 PTA15	DISABLED DISABLED		PTA14	SPIO_PCSO SPIO_SCK	UARTO_TX		IZCZ_SCL	1250_RX_BCLK	12S0_TXD1	
140	45 46	PTA15 PTA16	DISABLED		PTA15 PTA16	SPIO SCK SPIO SOUT	UARTO RX UARTO CTS b/UARTO COL b	RMIIO TXEN/MIIO TXEN RMIIO TXDO/MIIO TXDO		12SO RXDO	1250 RXD1	_
142	46	PTA17	ADC1 SE17	ADC1 SE17	PTA17	SPIO_SOUT	UARTO RTS b	RMIIO_IXDO/MIIO_IXDO		12SU_RX_FS	1250_KXD1	_
143	47	VDD	VDD VDD	ADCI_SEI/ VDD	PIA1/	SPIU_SIN	UARIU_RIS_B	RMIIO_IXD1/MIIO_IXD1		12SU_MCLK		+
150	48	VSS	VSS	VSS						1		+
151	50	VSS PTA18	VSS EXTALO		PTA18		FTM0_FLT2	FTM_CLKIN0		1		+
151	51	PTA18 PTA19	XTALO	EXTALO XTALO	PTA19		FTM1_FLT0	FTM_CLKIND		LPTMR0_ALT1		+
152	52	PIAI9	RESET b	RESET b	PIAIS		FIMI_FLI0	FIM_CLKIN1		LPTMRU_ALT1		+
153 164	52	RESET b	ADOD SER/ADOL SER	ADCO SES/ADC1 SES	PTB0/LLWU PS	1200 SCI	FTM1 CH0	DANIO ANDIO MAIIO ANDIO		FTM1 OD PHA		+
165	54	PTR1	ADCD_SER/ADC1_SER	ADCO_SES/ADC1_SES	PTB1	12C0_SCL	FTM1_CH1	RMIIO MDIC/MIIO MDIC		FTM1_QD_PHB		+
166	54 55	PTB1	ADCO_SE9/ADCI_SE9	ADCU_SE9/ADC1_SE9	PTB1	12CU_SDA 12C0_SC1	HARTO RTS b	FNFTO 1588 TMRO		FTM1_QD_PHB		+
167	56	PTB2	ADCO_SE12	ADCO SE12 ADCO SE13		12C0_SCE 12C0_SDA	HARTO CTS M/HARTO COL M	ENETO_1588_TMR0 ENETO_1588_TMR1		FTM0_FLT0		+
175	56 57	PTB3 PTB9	DISABLED	ADCU SEI3	PTB3	SPI1 PCS1	UARTO CIS B/UARTO CUL B	ENEIU 1588 IMRI	FB AD20	FIMU FLIU		+
176	58	PTB10	ADC1 SE14	ADC1 0014	PTB10	SPI1_PCSI	UARTS RX		FB_AD20	FTM0 FLT1		+
177	59	PTB11	ADCI_SEI4 ADCI SEI5	ADCI_SEI4	PTB11	SPI1_PCSU	UARTS TX		FB_AD19	FTM0_FLT2		
178	60	VSS	VSS VSS	VSS	FIBII	3FII_3CK	UARIS_IX		FB_AD18	FIMO_FLI2		+
178	61	ADD A22	VDD VSS	VDD						1		+
187	62	PTB16	DISABLED	VDD	PTB16	SPI1_SOUT	UARTO_RX	FTM_CLKIN0	FB_AD17	EWM_IN		+
100	63	P1010	DISABLED		PTB17	SPII_3001	UARTO TX	FTM_CLKIN1	FB AD16	EWIN OUT L		+
188 189	64	PTB17 PTB18	DISABLED		PTB18	SPI1 SIN CANO TX	FTM2_CH0	IZSO TX BCLK	FB AD15	FTM2 QD PHA		
192	65	PTR19	DISABLED		PIB18 PTR19	CAND IX	FTM2 CH1	12SU TX BLLK 12SU TX FS	FR OF h	FTM2 QD PHA		
193	66	PTR20	DISABLED		PTR20	SPI2 PCS0	111112_0112	12.0_17_13	FR AD31	CMPO OUT		
196	67	PTB20	DISABLED		PTB21	SPI2_FC30			FB_AD31	CMP1 OUT		
196	68	PTB22	DISABLED		PTR22	SPIZ SCK SPIZ SOUT			FB AD30 FB AD29	CMP2 OUT		
198	69	PTB23	DISABLED		PTB23	SPI2 SIN	SPID PCSS		FB AD28			
199	70	PTCO	ADCO SE14	ADC0 SE14	PTCO	SPIO_PCS4	PDB0_EXTRG	USB0 SOF OUT	FB_AD14	12S0_TXD1		
200	71	PTC1/LLWU P6	ADCO SE15	ADCO SE15	PTC1/LLWU P6	SPIO PCS3	UART1 RTS b	FTM0 CH0	FB AD13	I2SO TXDO		
201	72	PTC2	ADC0_SE4b/CMP1_IN0	ADCO SE4b/CMP1 INO	PTC2	SPIO_PCS2	UART1_CTS_b	FTM0 CH1	FB_AD12	I2SO_TX_FS		
202	73	PTC3/LLWU_P7	CMP1 IN1	CMP1 IN1	PTC3/LLWU_P7	SPIO_PCS1	UART1_RX	FTM0_CH2	CLKOUT	1250_TX_BCLK		
204	74	VSS	VSS	VSS								
205	75	VDD	VDD	VDD								
212	76	PTC4/LLWU_P8	DISABLED		PTC4/LLWU_P8	SPIO_PCSO	UART1_TX	FTM0_CH3	FB_AD11	CMP1_OUT		
213	77	PTCS/LLWU_P9	DISABLED		PTC5/LLWU_P9	SPIO_SCK	LPTMRO_ALT2	12S0_RXD0	FB_AD10	CMP0_OUT	FTM0_CH2	
214	78	PTC6/LLWU P10	CMPO INO	CMP0 IN0	PTC6/LLWU P10	SPIO SOUT	PDB0 EXTRG	12SO RX BCLK	FB AD9	12SO MCLK		
215	79	PTC7	CMPO IN1	CMPO IN1	PTC7	SPIO SIN	USBO SOF OUT	12SO RX FS	FB AD8			
216	80	PTC8	ADC1_SE4b/CMP0_IN2	ADC1_SE4b/CMP0_IN2	PTC8		FTM3_CH4	I2SO_MCLK	FB_AD7			
217	81	PTC9	ADC1_SE5b/CMP0_IN3	ADC1_SE5b/CMP0_IN3	PTC9		FTM3_CH5	12SO_RX_BCLK	FB_AD6	FTM2_FLT0		
218	82		ADC1 SE6b	ADC1 SE6b	PTC10	I2C1 SCL	FTM3 CH6	12SO RX FS	FB ADS			
220		PTC10										
	83	PTC11/LLWU_P11	ADC1_SE7b	ADC1_SE7b	PTC11/LLWU_P11	IZC1_SDA	FTM3_CH7	12S0_RXD1	FB_RW_b			
221	83 84	PTC11/LLWU_P11 PTC12	ADC1_SE76 DISABLED	ADC1_SE7b	PTC12	IZC1_SDA	UART4_RTS_b	12S0_RXD1	FB_AD27	FTM3_FLT0		
221 222	84 85	PTC11/LLWU_P11 PTC12 PTC13	ADC1_SE7b DISABLED DISABLED	ADC1_SE7b	PTC12 PTC13	IZCI_SDA		12S0_RXD1	FB_AD27 FB_AD26	FTM3_FLT0		
221 222 223	84 85 86	PTC11/LLWU_P11 PTC12 PTC13 PTC14	ADC1_SE76 DISABLED DISABLED DISABLED	ADC1_SE7b	PTC12 PTC13 PTC14	IZCI_SDA	UART4_RTS_b UART4_CTS_b UART4_RX	1250_RXD1	FB_AD27 FB_AD26 FB_AD25	FTM3_FLT0		
221 222 223 226	84 85 86 87	PTC11/LLWU_P11 PTC12 PTC13 PTC14 PTC15	ADC1_SE76 DISABLED DISABLED DISABLED DISABLED DISABLED DISABLED	ADC1_SE76	PTC12 PTC13	IZCI_SDA	UART4_RTS_b	12S0_RXD1	FB_AD27 FB_AD26	FTM3_FLT0		
221 222 223	84 85 86	PTC11/LLWU_P11 PTC12 PTC13 PTC14	ADC1_SE76 DISABLED DISABLED DISABLED	ADC1_SE7b	PTC12 PTC13 PTC14	IZCI_SDA	UART4_RTS_b UART4_CTS_b UART4_RX	12S0_RXD1	FB_AD27 FB_AD26 FB_AD25	FTM3_FLT0		
221 222 223 226	84 85 86 87	PTC11/LLWU_P11 PTC12 PTC13 PTC14 PTC15	ADC1_SE76 DISABLED DISABLED DISABLED DISABLED DISABLED DISABLED	ADC1_567b VSS VDD	PTC12 PTC13 PTC14	IZCI_SDA	UART4_RTS_b UART4_CTS_b UART4_RX	1250_RXD1	FB_AD27 FB_AD26 FB_AD25	FTM3_FLT0		
221 222 223 226 229	84 85 86 87 88 89	PTC1/LWU_P11 PTC12 PTC13 PTC14 PTC15 VSS VOO PTC16	AOCI_SE7b DISABLED DISABLED DISABLED DISABLED DISABLED VSS VSS		PTC12 PTC13 PTC14 PTC15	IZCI_SDA	UART4_RTS_b UART4_CTS_b UART4_RX	I2SO_RXD1	FB_AD27 FB_AD26 FB_AD25	FTM3_FLT0		
221 222 223 226 229 230	84 85 86 87 88	PTC13/LWU P11 PTC12 PTC13 PTC14 PTC14 PTC15 VSS V00 PTC16 PTC17	ADCI_SE76 DISABLED DISABLED DISABLED DISABLED DISABLED VSS VOD		PTC12 PTC13 PTC14 PTC15	IZCI_SOA	UART4_RTS_b UART4_CTS_b UART4_RX UART4_TX UART3_RX UART3_RX UART3_TX		FB AD27 FB AD26 FB AD25 FB AD25 FB AD24 FB CS5 5/FB TSZ1/FB BE23 16 BIS15 8 b FB CS4 M/FB TSZ0/FB BE31 24 BIS17 0 b	FTM3_FLT0		
221 222 223 226 229 230 233	84 85 86 87 88 89	PTC1/LWU_P11 PTC12 PTC13 PTC14 PTC15 VSS VOO PTC16	AOC 1,57b DISABLED DISABLED DISABLED DISABLED VSS VDD DISABLED DISABLED		PTC12 PTC13 PTC14 PTC15	IZCL SOA	UART4_RTS_b UART4_CTS_b UART4_RX	ENETO 1588 TMRO	FB AD27 FB AD26 FB AD25 FB AD24 FB AD24	FTM3_FLT0		
221 222 223 226 229 230 233 234 235 237	84 85 86 87 88 89 90 91 92	PFCLIALWUP11 PFCL3 PFCL3 PFCL4 PFCL5 VSS VSS VSS VSS PFCL9 P	ADC1, SF7b DISABLED	VDD	PTC12 PTC13 PTC14 PTC15	IZCL SOA	UARTA RTS b UART4 CTS b UART4 RX UART4 RX UART3 TX UART3 TX UART3 TX UART3 TX UART3 RTS b UART2 RTS b	ENETO 1588 TMRO ENETO 1588 TMR1	FB ADD7 FB ADD5 FB ADD5 FB ADD4 FB CSS b/FB TS22/FB BE23 16 BLS15 8 b FB CSS b/FB TS22/FB BE23 16 BLS15 8 b FB CSS b/FB TS22/FB BE23 28 BLS23 36 b FB TBST b/FB CSS b/FB BEES 58 BLS23 16 b	FTM3_FLTO		
221 222 223 226 229 230 233 234 235 237	84 85 86 87 88 89 90 91 91 92 93	PICLIALMUP 11 PICL2 PICL3 PICL3 PICL4 PICL5 VSS VDD PICL6 PICL7 PICL8 PICL9 PICL9	ADC1_SE7b DISABIED DISABIED DISABIED DISABIED DISABIED VSS VOD DISABIED ADC0_SESb	VDD	PTC12 PTC13 PTC14 PTC15 PTC15 PTC16 PTC17 PTC18 PTD0/LLWU P12 PTD1	IZCL SOA SPIO PCSO SPIO SCS	UART4_RTS_b UART4_CTS_b UART4_TS_b UART4_TR UART4_TX UART4_TX UART4_TX UART3_TX UART3_TX UART3_RTS_b UART2_CTS_b UART2_CTS_b	ENETO 1588 TMR0 ENETO 1588 TMR1 ENETO 1588 TMR2 FTM3 CH0 FTM3 CH1	FB ADD7 FB ADD5 FB ADD5 FB ADD4 FB CSS b/FB TS22/FB BE23 16 BLS15 8 b FB CSS b/FB TS22/FB BE23 16 BLS15 8 b FB CSS b/FB TS22/FB BE23 28 BLS23 36 b FB TBST b/FB CSS b/FB BEES 58 BLS23 16 b	FTM3_FLTO		
221 222 223 226 229 230 233 234 235 237 238 239	84 85 86 87 88 89 90 91 92 93 94 95	PFCLI/LIMUM P11 PFCL3 PFCL3 PFCL3 PFCL4 PFCL5 V80 PFCL6 PFCL6 PFCL6 PFCL6 PFCL6 PFCL6 PFCL7 P	ADC1_SETD DISABLED ADC0_SESD DISABLED		PTC12 PTC13 PTC14 PTC15 PTC16 PTC16 PTC17 PTC18 PTD0/LLWU P12 PTD1 PTD2/LLWU P13	SPIO PCSO SPIO SOX SPIO SOX SPIO SOX	UARTA RTS b UARTA CTS b UARTA TX UARTA TX UARTA TX UARTA TX UARTA TX UARTA TX UARTA TX UARTA TX UARTA TS b UARTZ CTS b UARTZ CTS b UARTZ RX	ENETO 1588 TMR0 ENETO 1588 TMR1 ENETO 1588 TMR2 ENETO 1588 TMR2 ETMS GHD ETMS GHD ETMS GHZ	F8_A027 F8_A025 F8_A025 F8_A025 F8_CSS_MF8_TS22LF8_E822_16_E835_8 b F8_CSS_MF8_TS22LF8_E822_16_E835_8 b F8_CSS_MF8_TS22LF8_E832_16_E835_0 b F8_AUTR_CSC_MF8_TS3_E832_16_b F8_AUTR_CSC_MF8_TS3_E832_16_b	FTM3_FLTO	1200_501	
221 222 223 226 229 230 233 234 235 237 238 238 239 240	84 85 86 87 88 89 90 91 92 93 94 95 96	PFCLIACKUV P11 PFCL12 PFCL13 PFCL3 PFCL4 VSS VSD PFCL6 PFCL7 PFCL7 PFCL9 PF	ADC1,587b DISABLED	VDD	PTC12 PTC13 PTC14 PTC15 PTC16 PTC17 PTC17 PTC18 PTC19 PTD2/LLWU P12 PTD2/LLWU P13 PTD2/LLWU P13	SPIO_SOUT SPIO_SIN	UARTE RTS b UARTE CTS b UARTE RX UARTE RX UARTE TX UARTE TX UARTE TX UARTE TS b UARTE TS b UARTE RTS B UARTE CTS b	ENETO 1588 TMRO ENTO 1588 TMR1 ENTO 1588 TMR2 FTM3 CH0 FTM3 CH1 FTM3 CH2 FTM3 CH2 FTM3 CH2	FB A027 FB A025 FB A025 FB A025 FB A024 FB		1200_501. 1200_50A	
221 222 223 226 229 230 233 234 235 237 238 239 240	84 85 86 87 88 89 90 91 92 92 93 94 95 96	PFCLI/LIMUM P11 PFCL2 PFCL3 PFCL3 PFCL4 PFCL5 VSS VOD PFCL6 PFCL6 PFCL6 PFCL6 PFCL6 PFCL7 PFD0.LIMU P12 PFD1 PFD1 PFD2.LIMU P13 PFD3 PFD3	ADC1_SETD DISABLED	VDD ADC0 SES6	PTC12 PTC13 PTC14 PTC15 PTC16 PTC16 PTC17 PTC18 PTC0/LWU P12 PTD1 PTD2/LLWU P13 PTD3 PTD3/LLWU P14	SPIO_SOUT SPIO_SIN SPIO_PCS1	UARTE RTS b UARTE CTS b UARTE RK UARTE RK UARTE TK UARTE TS b UARTE TS b UARTE TS b UARTE TS b UARTE TS CTS b UARTE TS CTS b UARTE TS CTS CTS CTS CTS CTS CTS CTS CTS CTS	ENTO 1588 TMR0 ENTO 1588 TMR1 ENTO 1588 TMR2 ENTO 1588 TMR2 ETMR3 CH ETMR3 CH ETMR3 CH ETMR3 CH ETMR3 CH ETMR3 CH	F8_A027 F8_A025 F8_A025 F8_A025 F8_A025 F8_C05 F8_A027 F8_A027 F8_A03 F8_A03 F8_A03 F8_A03	EWM IN	I2CO_SDA SPI1 PCSO	
221 222 223 226 229 230 233 234 235 237 238 239 240 241	84 85 86 87 88 89 90 91 92 93 94 95 96 97	PFCLIALMUM P11 PFCL2 PFCL3 PFCL4 PFCL5 PFCL5 PFCL6 PFCL7 PFCL7 PFCL7 PFCL7 PFCL7 PFCL7 PFCL9	ADCI_SETS DISABLED ADCO_SESS	VD0 ADC0 565b ADC0, 566b	PTC12 PTC13 PTC14 PTC15 PTC16 PTC16 PTC17 PTC18 PTC19	SPIO_SOUT SPIO_SIN SPIO_PCS1 SPIO_PCS2	UARTE RTS b UARTE CTS b UARTE RX UARTE RX UARTE TX UARTE TX UARTE TS b UARTE TS b UARTE RTS b UARTE RTS b UARTE RTS b UARTE CTS D UARTE	ENETO 1588 TMRO ENTO 1588 TMR1 ENTO 1588 TMR2 ENTO 1588 TMR2 FFM3 CH0 FFM3 CH1 FFM3 CH2 FFM3 CH2 FFM4 CH2 FFM0 CH4 FFM0 CH5	FB_A027 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_CC4_FFB_TS027FB_ED2_16_BLSSS_B_b FB_CC4_FFB_TS027FB_ED2_16_BLSS_B_B FB_CC4_FFB_TS027FB_ED2_16_BLSS_B_B FB_A025_FFB_TS025_B FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025	EWM IN EWM OUT b	I2CO_SDA SPI1_PCSO SPI1_SCK	
221 222 223 226 229 230 233 234 235 237 238 239 240 241 242	84 85 86 87 88 89 90 91 92 93 94 95 96 97 98	PICLIALMU P11 PFC13 PFC13 PFC14 PFC15 PFC14 PFC15 VSS VOD PFC16 PFC16 PFC16 PFC17 PF	ADC1_SETD DISABLED DISAB	VDD ADC0 SES6	PTC12 PTC13 PTC14 PTC15 PTC16 PTC16 PTC17 PTC18 PTC01WU P12 PTD1 PTD21LWU P13 PTD3 PTD41LWU P14 PTD5 PTD5 PTD5 PTD5 PTD5 PTD5 PTD6 PTD7 PTD7 PTD7 PTD7 PTD7 PTD7 PTD7 PTD7	SPIO_SOUT SPIO_SIN SPIO_PCS1 SPIO_PCS2 SPIO_PCS3	LIMATE RTS D LIMATE RE LIMATE RTS D LIMATE RTS D LIMATE RTS D LIMATE RTS D LIMATE CR L	INITO 1588 TARRO ITAMO 001 ITAMO 001 ITAMO 001 ITAMO 002 ITAMO 004 ITAMO 045 ITAMO 045 ITAMO 046	F8_A027 F8_A025 F8_A025 F8_A025 F8_A025 F8_C05 F8_A027 F8_A027 F8_A03 F8_A03 F8_A03 F8_A03	EWM IN EWM OUT 6	I2CO_SDA SPI1 PCSO	
221 222 223 226 229 230 233 234 235 237 238 239 240 241	84 85 86 87 88 89 90 91 92 93 94 95 96 97	PFCLIALMUM P11 PFCL2 PFCL3 PFCL4 PFCL5 PFCL5 PFCL6 PFCL7 PFCL7 PFCL7 PFCL7 PFCL7 PFCL7 PFCL9	ADCI_SETS DISABLED ADCO_SESS	VD0 ADC0 565b ADC0, 566b	PTC12 PTC13 PTC14 PTC15 PTC16 PTC16 PTC17 PTC18 PTC19	SPIO_SOUT SPIO_SIN SPIO_PCS1 SPIO_PCS2	UARTE RTS b UARTE CTS b UARTE RX UARTE RX UARTE TX UARTE TX UARTE TS b UARTE TS b UARTE RTS b UARTE RTS b UARTE RTS b UARTE CTS D UARTE	ENETO 1588 TMRO ENTO 1588 TMR1 ENTO 1588 TMR2 ENTO 1588 TMR2 FFM3 CH0 FFM3 CH1 FFM3 CH2 FFM3 CH2 FFM4 CH2 FFM0 CH4 FFM0 CH5	FB_A027 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_CC4_FFB_TS027FB_ED2_16_BLSSS_B_b FB_CC4_FFB_TS027FB_ED2_16_BLSS_B_B FB_CC4_FFB_TS027FB_ED2_16_BLSS_B_B FB_A025_FFB_TS025_B FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025 FB_A025	EWM IN EWM OUT b	I2CO_SDA SPI1_PCSO SPI1_SCK	