

IO Ports – structure and operations

KF6010 – Distributed Real Time Systems

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Lecture 1a

Useful documentation

Cortex-M4 Devices Generic User Guide [M4-UG] gives an overview of the device and details of the instruction-set.

Cortex-M4 Technical Reference Manual [M4-TRM] has details about the memory model (including bit-aliasing [M4-TRM, 3.7]) and the interrupt controller [M4-TRM, 6]

FRDM-K64F Freedom Module User's Guide [K64F-User] gives an overview of the board and the connectivity (circuit diagrams) of the devices on the board.

K64 Sub-Family Reference Manual [K64F-Ref]

M4-TRM. *Cortex-M4 Technical Reference Manual*. ARM Limited, 2010.

http://hesabu.net/kf6010/assets/ra/DDI0439B_cortex_m4_r0p0_trm.pdf.

M4-UG. *Cortex-M4 Devices Generic User Guide*. ARM Limited, 2010.

http://hesabu.net/kf6010/assets/ra/DUI0553A_cortex_m4_dgug.pdf.

K64F-Ref. *K64 Sub-Family Reference Manual*. Freescale Semiconductor, Inc., rev. 2 edition, January 2014.

<http://hesabu.net/kf6010/assets/ra/K64P144M120SF5RM.pdf>.

K64F-User. *FRDM-K64F Freedom Module User's Guide*. NXP Semiconductors, rev. 1 edition, August 2016.

<http://hesabu.net/kf6010/assets/ra/FRDMK64FUG.pdf>.