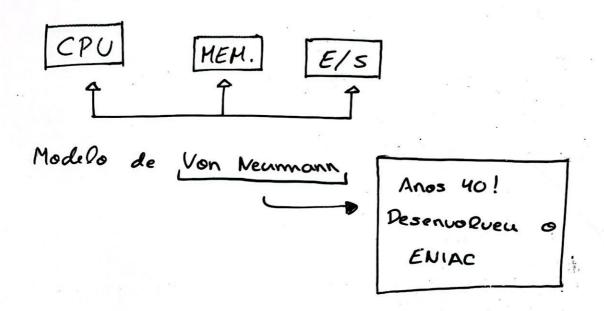
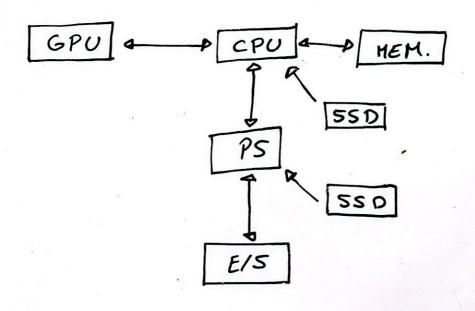
Sistermas Campulacionais (Teópica - 1)

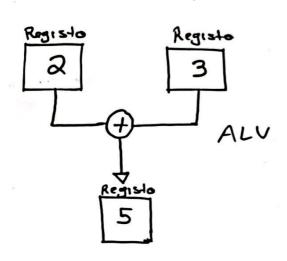


Modelo atual:



(Modelo de 2 chips)





/ A cache term 3 nrueis: 41 , La , L3.

Linguagens de Alto vivel (MLL):

C; C++; Python; dava ...

Linguagens de baijo nevel (LLL):

Assembly; 10117010110010...

Alnavés compiladores. (1 511

Relag



Relação entre o bit e o bate:

(1 bit = 85ytes)

10



· entre H/W.

