Introduction to VLSI

Lab 5

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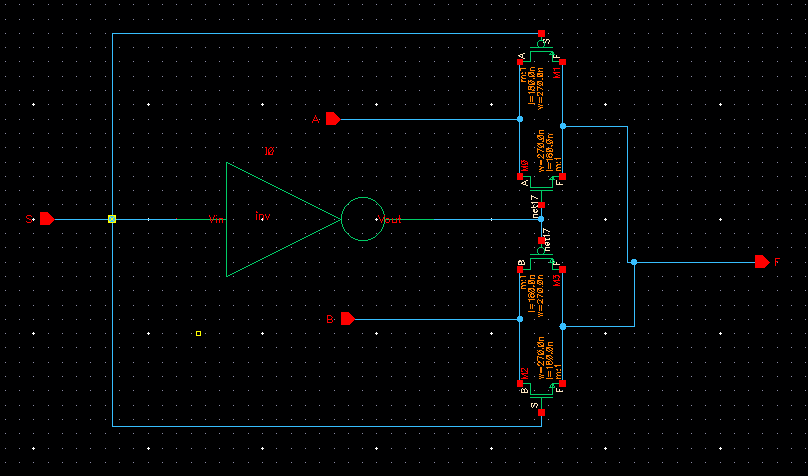
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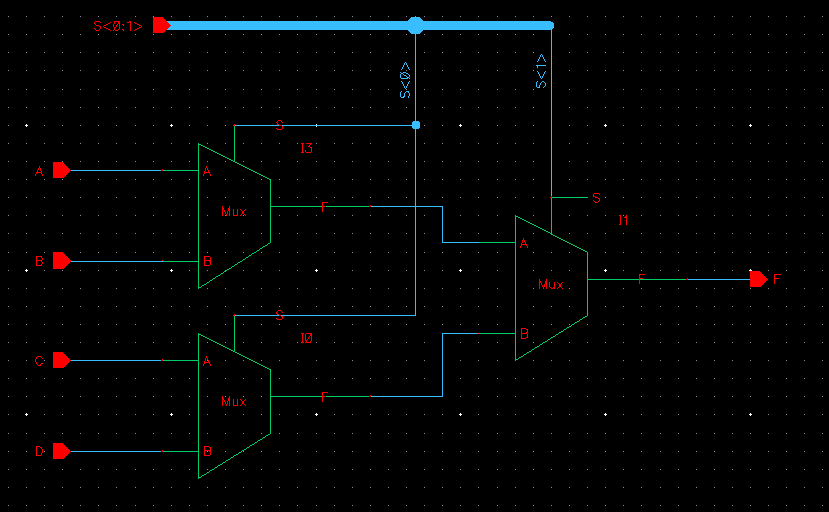
# 1 bit 2:1 MUX

## Schematic



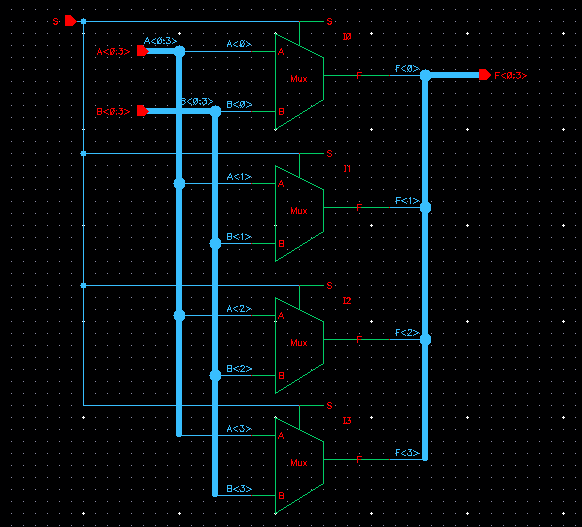
# 1 bit 4:1 MUX

## Schematic



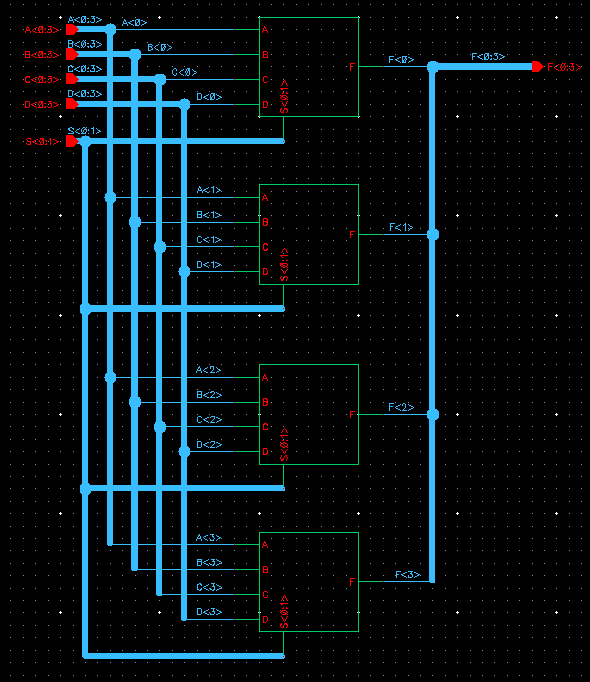
# 4 bit 2:1 MUX

## Schematic



# 4 bit 4:1 MUX

## Schematic



# ALU

## Schematic