

Input/Output Files and Makefile

1. Input files:

- Extracted timing context
 - `./solver20211008_3/sdc_parser/input/pt_est/u_core_context.sdc`
 - `./solver20211008_3/sdc_parser/input/pt_est/u_mem1_context.sdc`
 - `./solver20211008_3/sdc_parser/input/pt_est/u_mem2_context.sdc`
- Floorplan information
 - `./solver20211008_3/main/input/design_data_cLoc.json`

2. Output files:

- Floorplan information with routing solution
 - `./solver20211008_3/output/design_data_cLoc-out.json`
 - `./solver20211008_3/output/pin_grid_u_core_context.csv`
 - `./solver20211008_3/output/pin_grid_u_mem_context1.csv`
 - `./solver20211008_3/output/pin_grid_u_mem_context2.csv`

Execution Steps

In directory `"./solver20211008_3/"`:

1. To compile the binaries:

```
$ make build
```

2. Run the shell script by command:

```
$ bash run.sh
```

3. Output files shall be in `"./solver20211008_3/output/"`