**Input/Output Files and Makefile**

1. Input files:

* Extracted timing context
  + ./solver20211008\_3/sdc\_parser/input/pt\_est/u\_core\_context.sdc
  + ./solver20211008\_3/sdc\_parser/input/pt\_est/u\_mem1\_context.sdc
  + ./solver20211008\_3/sdc\_parser/input/pt\_est/u\_mem2\_context.sdc
* Floorplan information
  + ./solver20211008\_3/main/input/design\_data\_cLoc.json

1. Output files:

* Floorplan information with routing solution
  + ./solver20211008\_3/output/design\_data\_cLoc-out.json
  + ./solver20211008\_3/output/pin\_grid\_u\_core\_context.csv
  + ./solver20211008\_3/output/pin\_grid\_u\_mem\_context1.csv
  + ./solver20211008\_3/output/pin\_grid\_u\_mem\_context2.csv

**Execution Steps**

In directory “./solver20211008\_3/”:

1. To compile the binaries:

$ make build

1. Run the shell script by command:

$ bash run.sh

1. Output files shall be in “./solver20211008\_3/output/”