

Learning Objective: MOSFET Concepts

Problem 1. Consider an p-channel MOSFET with a gate oxide thickness of 10 nm, threshold voltage $V_{TH} = -0.6$ V, gate width of $25\text{ }\mu\text{m}$ and gate length of $1\text{ }\mu\text{m}$. Assume the hole channel mobility is $\mu_p = 100\text{ cm}^2/\text{V/s}$ at $T = 300\text{ K}$. Use $\epsilon_{ox} = 3.9\epsilon_0$, $n_i = 1.5 \times 10^{10}\text{ cm}^{-3}$ if needed.

- (a) Suppose $V_{GS} = -0.1\text{ V}$, $V_{DS} = -3\text{ V}$, determine the **operating region** and **drain-to-source current I_{DS}** .

$|V_{GS}| < |V_{TH}|$, so the MOSFET is in cut-off region, the drain-to-source $I_{DS} = 0\text{ A}$.

- (b) Suppose $V_{GS} = -3\text{ V}$, $V_{DS} = -5\text{ V}$, determine the **operating region** and **drain-to-source current I_{DS}** .

$|V_{GS}| > |V_{TH}|$ and $|V_{DS}| > |V_{GS}| - |V_{TH}|$, the MOSFET is in saturation region.

$$\begin{aligned} I_{SD}^{\text{sat}} &= \frac{1}{2} \mu_p C_{ox} \frac{W}{L} (V_{SG} + V_{TH})^2 \\ &= \frac{1}{2} (100\text{ cm}^2/\text{V/s}) \left(\frac{3.9 \times 8.854 \times 10^{-14}\text{ F/cm}}{10 \times 10^{-7}\text{ cm}} \right) \frac{25\text{ }\mu\text{m}}{1\text{ }\mu\text{m}} (3 - 0.6)^2 \\ &= 2.48\text{ mA} \\ I_{DS}^{\text{sat}} &= \textstyle\boxed{-2.48\text{ mA}} \end{aligned}$$

- (c) Suppose $V_{GS} = -5\text{ V}$, $V_{DS} = -0.1\text{ V}$, determine the **operating region** and **drain-to-source current I_{DS}** .

$|V_{GS}| > |V_{TH}|$ and $|V_{DS}| < |V_{GS}| - |V_{TH}|$, the MOSFET is in linear region.

$$\begin{aligned} I_{SD}^{\text{lin}} &= \mu_p C_{ox} \frac{W}{L} \left[(V_{SG} + V_{TH}) V_{SD} - \frac{1}{2} V_{SD}^2 \right] \\ &= (100\text{ cm}^2/\text{V/s}) \left(\frac{3.9 \times 8.854 \times 10^{-14}\text{ F/cm}}{10 \times 10^{-7}\text{ cm}} \right) \frac{25\text{ }\mu\text{m}}{1\text{ }\mu\text{m}} \left[(5 - 0.6)(0.1) - \frac{1}{2} (0.1)^2 \right] \\ &= 0.375\text{ mA} \\ I_{DS}^{\text{lin}} &= \textstyle\boxed{-0.375\text{ mA}} \end{aligned}$$

Problem 2. An n-channel MOSFET has parameters: $W = 15 \mu\text{m}$, $L = 2 \mu\text{m}$, $C_{ox} = 6.9 \times 10^{-8} \text{ F/cm}^2$. Use $\epsilon_{ox} = 3.9\epsilon_0$, $n_i = 1.5 \times 10^{10} \text{ cm}^{-3}$ if needed at 300 K.

- (a) Calculate the **oxide thickness** of the MOSFET.

$$\begin{aligned} C_{ox} &= \frac{\epsilon_{ox}}{t_{ox}} \\ t_{ox} &= \frac{\epsilon_{ox}}{C_{ox}} = \frac{3.9 \times 8.854 \times 10^{-14} \text{ F/cm}}{6.9 \times 10^{-8} \text{ F/cm}^2} \\ &= \boxed{50 \text{ nm}} \end{aligned}$$

- (b) Assume the drain current in the saturation region for high V_{DS} is $I_{DS} = 35 \mu\text{A}$ at $V_{GS} = 2.3 \text{ V}$ and $I_{DS} = 75 \mu\text{A}$ at $V_{GS} = 2.5 \text{ V}$. Calculate the **electron channel mobility** and **threshold voltage**.

Plug-in two operating points to the MOSFET I_D^{sat} equation

$$I_D^{\text{sat}} = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})^2$$

get:

$$\begin{aligned} 35 \times 10^{-6} &= \frac{1}{2} \mu_n (6.9 \times 10^{-8} \text{ F/cm}^2) \frac{15 \mu\text{m}}{2 \mu\text{m}} (2.3 - V_{TH})^2 \\ 75 \times 10^{-6} &= \frac{1}{2} \mu_n (6.9 \times 10^{-8} \text{ F/cm}^2) \frac{15 \mu\text{m}}{2 \mu\text{m}} (2.5 - V_{TH})^2 \end{aligned}$$

Solve the equations above get: $\boxed{\mu_n = 727.58 \text{ cm}^2/\text{V/s}}$ and $\boxed{V_{TH} = 1.869 \text{ V}}$.

- (c) Assume the drain current in the linear region for low $V_{DS} = 0.10 \text{ V}$ is $I_{DS} = 35 \mu\text{A}$ at $V_{GS} = 1.5 \text{ V}$ and $I_{DS} = 75 \mu\text{A}$ at $V_{GS} = 2.5 \text{ V}$. Calculate the **electron channel mobility** and **threshold voltage**.

Plug-in two operating points to the MOSFET I_D^{lin} equation

$$I_D^{\text{sat}} = \mu_n C_{ox} \frac{W}{L} \left[(V_{GS} + V_{TH}) V_{DS} - \frac{1}{2} V_{DS}^2 \right]$$

get:

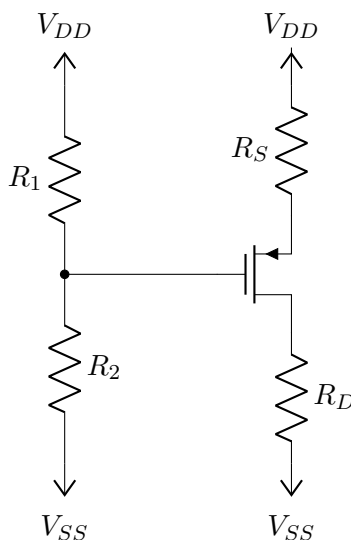
$$\begin{aligned} 35 \times 10^{-6} &= \mu_n (6.9 \times 10^{-8} \text{ F/cm}^2) \frac{15 \mu\text{m}}{2 \mu\text{m}} \left[(1.5 - V_{TH})(0.1) - \frac{1}{2} (0.1)^2 \right] \\ 75 \times 10^{-6} &= \mu_n (6.9 \times 10^{-8} \text{ F/cm}^2) \frac{15 \mu\text{m}}{2 \mu\text{m}} \left[(2.5 - V_{TH})(0.1) - \frac{1}{2} (0.1)^2 \right] \end{aligned}$$

Solve the equations above get: $\boxed{\mu_n = 772.95 \text{ cm}^2/\text{V/s}}$ and $\boxed{V_{TH} = 0.575 \text{ V}}$.

Learning Objective: MOSFET DC Biasing

Problem 3. Consider the circuit shown below. The transistor parameters are $V_{TH} = -0.8$ V and $K_p = 0.5 \text{ mA/V}^2$. The component values are $V_{DD} = 3$ V, $V_{SS} = -3$ V, $R_1 = 8 \text{ k}\Omega$, $R_2 = 22 \text{ k}\Omega$, and $R_D = 5 \text{ k}\Omega$.

- (a) Calculate V_{SG} , I_{SD} , and V_{SD} for $R_S = 0 \Omega$.
 (b) Calculate V_{SG} , I_{SD} , and V_{SD} for $R_S = 0.5 \text{ k}\Omega$.



For $R_S = 0 \Omega$: Assume MOSFET is in saturation:

$$\begin{aligned}
 V_{SG} &= V_S - V_G = V_{DD} - \left[(V_{DD} - V_{SS}) \left(\frac{R_2}{R_1 + R_2} \right) + V_{SS} \right] \\
 &= 3 - \left[(3 + 3) \left(\frac{22 \times 10^3}{8 \times 10^3 + 22 \times 10^3} \right) - 3 \right] \\
 &= \underline{1.6 \text{ V}} \\
 I_{SD} &= K_p (V_{SG} + V_{TP})^2 \\
 &= (0.5 \times 10^{-3} \text{ A/V}^2) (1.6 - 0.8)^2 \\
 &= \underline{0.32 \text{ mA}} \\
 V_{SD} &= (V_{DD} - V_{SS}) - I_{SD} R_D \\
 &= 6 - (0.32 \text{ mA})(5 \text{ k}\Omega) \\
 &= \underline{4.4 \text{ V}}
 \end{aligned}$$

Verify assumption: $|V_{DS}| = 4 \text{ V} > |V_{GS}| - |V_{TH}| = 0.8 \text{ V}$, the MOSFET is biased in saturation region. Therefore $V_{SG} = 1.6 \text{ V}$, $I_{SD} = 0.4 \text{ mA}$, and $V_{SD} = 4 \text{ V}$.

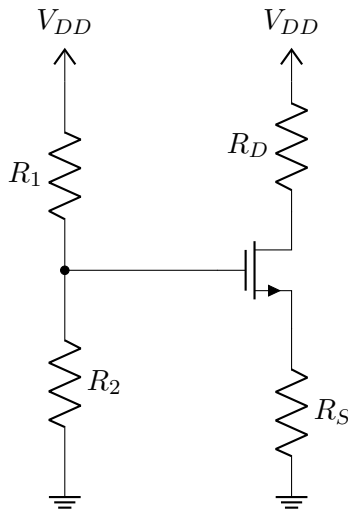
For $R_S = 0.5 \text{ k}\Omega$: Assume MOSFET is in saturation:

$$\begin{aligned}
 V_{SG} &= V_S - V_G = (V_{DD} - I_{SD}R_S) - \left[(V_{DD} - V_{SS}) \left(\frac{R_2}{R_1 + R_2} \right) + V_{SS} \right] \\
 &= [3 - I_{SD}(500)] - \left[(3 + 3) \left(\frac{22 \times 10^3}{8 \times 10^3 + 22 \times 10^3} \right) - 3 \right] \\
 &= \underline{1.6 - I_{SD}(500) \text{ V}} \\
 I_{SD} &= K_p(V_{SG} + V_{TP})^2 \\
 &= (0.5 \times 10^{-3} \text{ A/V}^2)(1.6 - I_{SD}(500) - 0.8)^2 \\
 &= \underline{\{ 10.96, 0.23 \} \text{ mA}} \\
 V_{SD} &= (V_{DD} - V_{SS}) - I_{SD}R_D - I_{SD}R_S \\
 &= 6 - (0.23 \text{ mA})(5 \text{ k}\Omega) - (0.23 \text{ mA})(0.5 \text{ k}\Omega) \\
 &= \underline{4.735 \text{ V}}
 \end{aligned}$$

Verify assumption: $|V_{DS}| = 4.735 \text{ V} > |V_{GS}| - |V_{TH}| = 0.685 \text{ V}$, the MOSFET is biased in saturation region. Therefore $V_{SG} = 1.485 \text{ V}$, $I_{SD} = 0.23 \text{ mA}$, and $V_{SD} = 4.735 \text{ V}$.

Problem 4. Consider the circuit shown below. The transistor parameters are $V_{TH} = 0.4 \text{ V}$ and $K_n = 0.5 \text{ mA/V}^2$. The component values are $V_{DD} = 5 \text{ V}$, $R_1 = 20 \text{ k}\Omega$, $R_2 = 5 \text{ k}\Omega$, $R_D = 2 \text{ k}\Omega$.

- Calculate V_{GS} , I_{DS} , and V_{DS} for $R_S = 0 \Omega$.
- Calculate V_{GS} , I_{DS} , and V_{DS} for $R_S = 1 \text{ k}\Omega$.



For $R_S = 0 \Omega$: Assume MOSFET is in saturation:

$$\begin{aligned}
 V_{GS} &= V_G - V_S = V_{DD} \left(\frac{R_2}{R_1 + R_2} \right) - 0 \\
 &= (5) \left(\frac{5 \times 10^3}{20 \times 10^3 + 5 \times 10^3} \right) - 0 \\
 &= \underline{1 \text{ V}} \\
 I_{DS} &= K_n (V_{GS} - V_{TH})^2 \\
 &= (0.5 \times 10^{-3} \text{ A/V}^2) (1 - 0.4)^2 \\
 &= \underline{0.18 \text{ mA}} \\
 V_{DS} &= V_{DD} - I_{DS} R_D \\
 &= (5) - (0.18 \times 10^{-3}) (2 \times 10^3) \\
 &= \underline{4.64 \text{ V}}
 \end{aligned}$$

Verify assumption: $|V_{DS}| = 4.64 \text{ V} > |V_{GS}| - |V_{TH}| = 0.6 \text{ V}$, the MOSFET is biased in saturation region. Therefore $\boxed{V_{GS} = 1 \text{ V}}$, $\boxed{I_{DS} = 0.18 \text{ mA}}$, and $\boxed{V_{DS} = 4.64 \text{ V}}$.

For $R_S = 1 \text{ k}\Omega$: Assume MOSFET is in saturation:

$$\begin{aligned}
 V_{GS} &= V_G - V_S = V_{DD} \left(\frac{R_2}{R_1 + R_2} \right) - I_{DS} R_S \\
 &= (5) \left(\frac{5 \times 10^3}{20 \times 10^3 + 5 \times 10^3} \right) - I_{DS} (1000) \\
 &= \underline{1 - I_{DS} (1000)} \\
 I_{DS} &= K_n (V_{GS} - V_{TH})^2 \\
 &= (0.5 \times 10^{-3} \text{ A/V}^2) (1 - I_{DS} (1000) - 0.4)^2 \\
 &= \underline{\{ \cancel{3.083}, 0.117 \} \text{ mA}} \\
 V_{DS} &= V_{DD} - I_{DS} R_D - I_{DS} R_S \\
 &= (5) - (0.117 \times 10^{-3}) (2 \times 10^3) - (0.117 \times 10^{-3}) (1 \times 10^3) \\
 &= \underline{4.649 \text{ V}}
 \end{aligned}$$

Verify assumption: $|V_{DS}| = 4.649 \text{ V} > |V_{GS}| - |V_{TH}| = 0.483 \text{ V}$, the MOSFET is biased in saturation region. Therefore $\boxed{V_{GS} = 0.883 \text{ V}}$, $\boxed{I_{DS} = 0.117 \text{ mA}}$, and $\boxed{V_{DS} = 4.649 \text{ V}}$.