

Implementation of MCX gate in classiq

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June 2024

Here we show 3 implementations of the Multi-Controlled X gate using Classiq platform

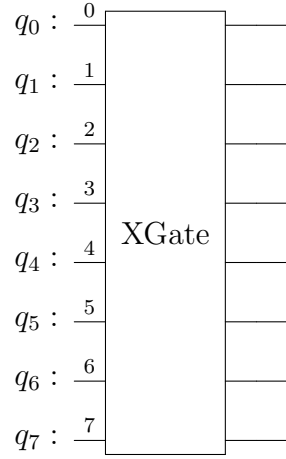
Task A: Synthesize 3 different implementations of an MCX (multi-control-x) with 5 control qubits and 1 target qubit (you should use the control quantum operation for implementing an MCX, follow this tutorial that can be open in the IDE). One implementation should be optimized for minimized depth, the other for minimized width, and the third somewhere in between (choose yourself what is the maximal width / depth you apply).

Export the 3 implementations as LaTeX files on the hierarchy level that demonstrates the differences between the implementations. Aggregate the implementations in 1 file and export it as a PDF and explain the key differences.

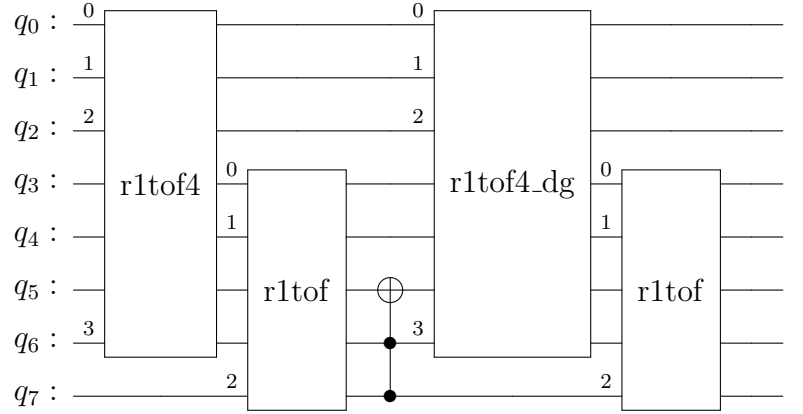
*Womanium Global Quantum+AI Event 2024

1 Implementation 1: With minimized depth

In this implementation, the circuit width is 8 and the circuit depth is 34

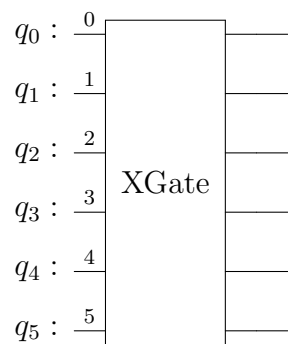


The circuit is further revealed one step down its hierarchy to show further implementation

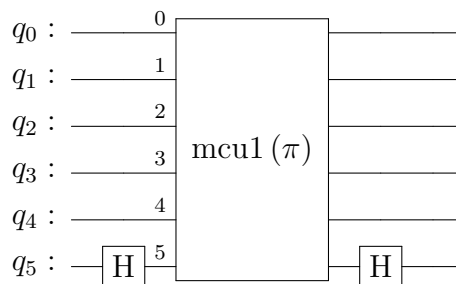


2 Implementation 2: With minimized width

In this implementation, the circuit width is 6 and the circuit depth is 117

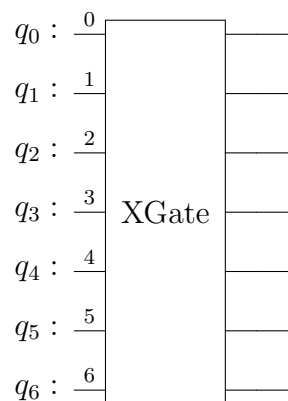


The circuit is further revealed one step down its hierarchy to show further implementation

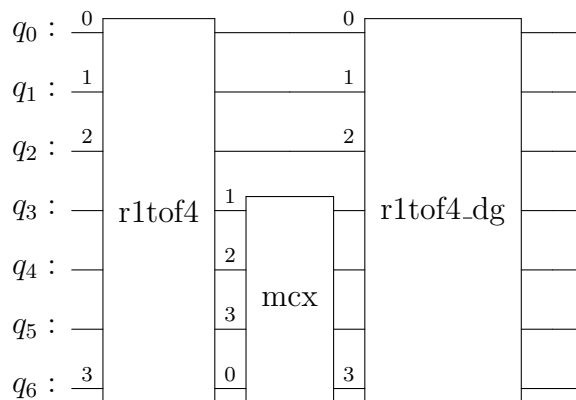


3 Implementation 3: optimal choice

In this implementation, the circuit width is 7 and the circuit depth is 51



The circuit is further revealed one step down its hierarchy to show further implementation



4 Explanation

The following can be deduced from the above implementations:

Implementation 1 has the least depth of 34. It shows that lesser 2qubits gate were implemented. To achieve this however, it is the circuit with maximum width of 8

Implementation 2 has the least width of 6. It shows a restriction to just 6 qubits and this is at the cost of more 2qubits gate resulting to a depth of 117

Implementation 3 with optimal choice of depth is well obtained at the cost of an extra qubit resulting to 7qubits usage at a depth of 51.