<u>Given:</u> Using VMLAB, write AVR that generates a multiplication table for SRAM addresses 0x0100 to 0x01FF. The value at each address is the product of the two least significant nibbles of the address. For example, at address 0x0123 the multiplicand is 3 and the multiplier is 2. Calculate the product (6 in this case) and store it at address 0x0123.

<u>Find:</u> AVR multiplication table described above Solution:

AVR Code

.include "C:\VMLAB\include\m168def.inc"

ldi r27, 0x01

ldi r26, 0x00

ldi r30, 0xff

main:

mov r16, r26

andi r16,0x0f

mov r17,r27

andi r17,0xf0

swap r17

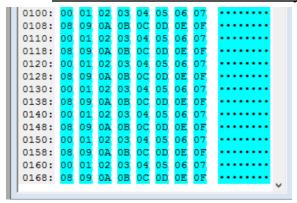
mul r17, r16

st x+, r16

dec r30

brne main

Screenshot of affected SRAM memory after code runs



I cannot get it to work. I worked on this, with Ken and some others in the class, for multiple days and this is all we could come up with. It seems to multiply the 3rd and 1st least significant nibble in the first 2 rows, then repeat itself. We didn't do AVR in 2013/2014 when I took 171. It was not apart of the curriculum, so I tried my hardest on this and this is the farthest I could get.