

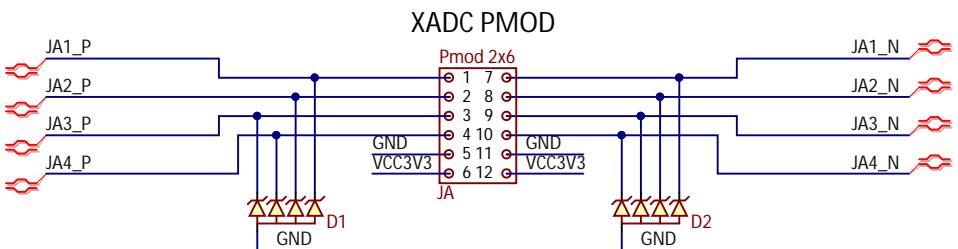
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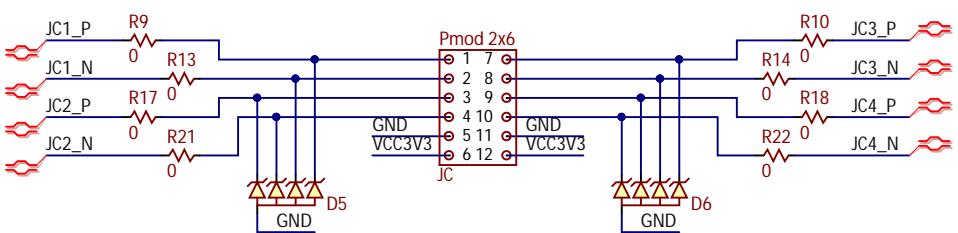
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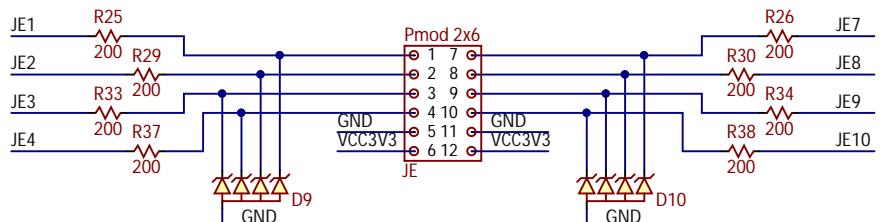
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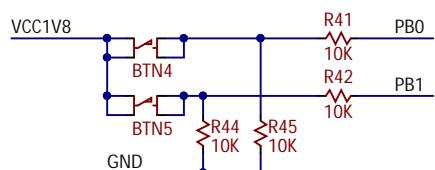
B



C



MIO BUTTON

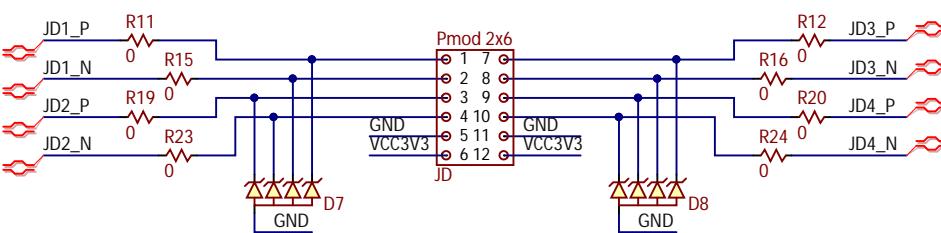


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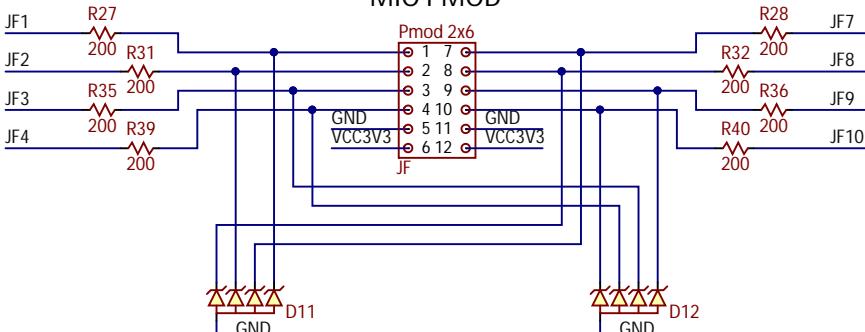
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D

Available on Z20 version only



MIO PMOD



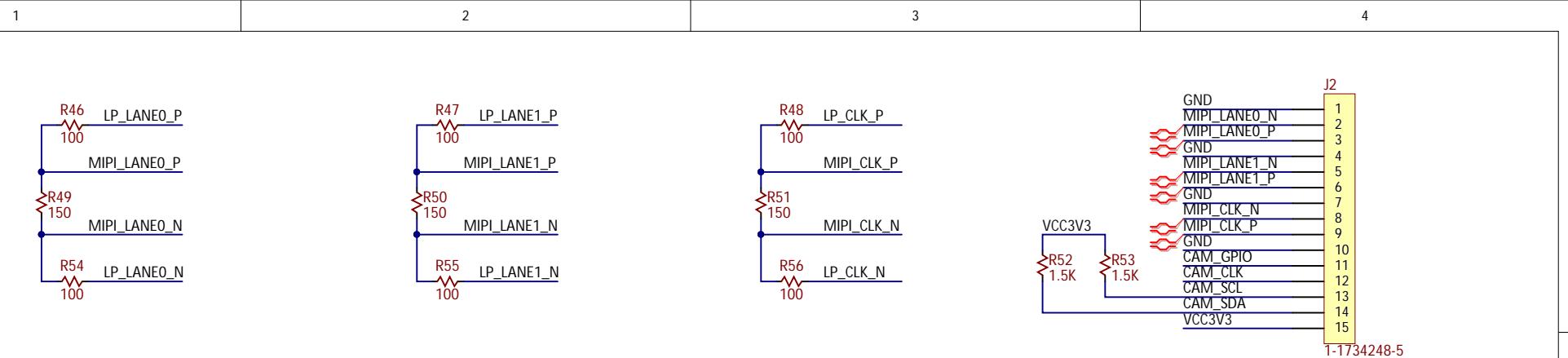
TP
1x1
1 → GND
J1
No Load

MIO LED
LD_MIO/VCFG0 → LD4 → GND

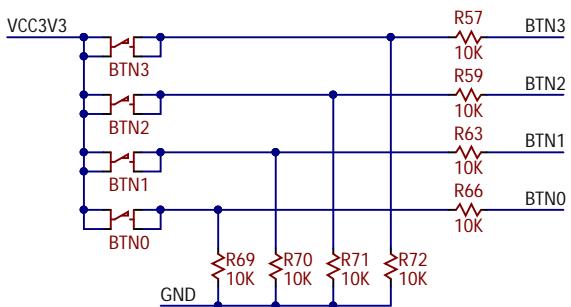
Foot
F1
Foot
F2
Foot
F3
Foot
F4

Title
ZYBO Z7
Rev
B.2
Copyright 2017
Circuit
PMODs, MIO
Doc# 500-351
Engineer EG
Author GTM
Date 5/31/2017
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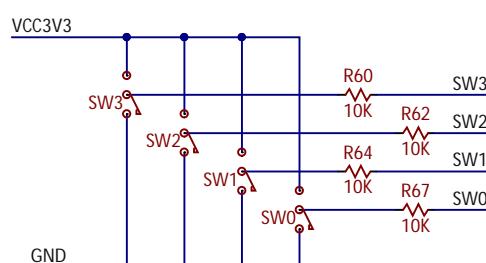
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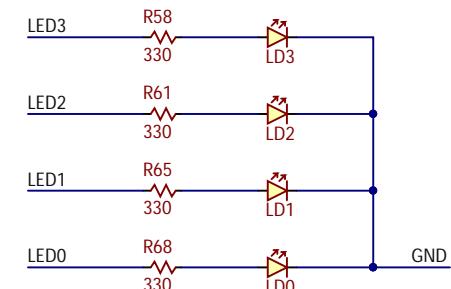
PL BUTTONS



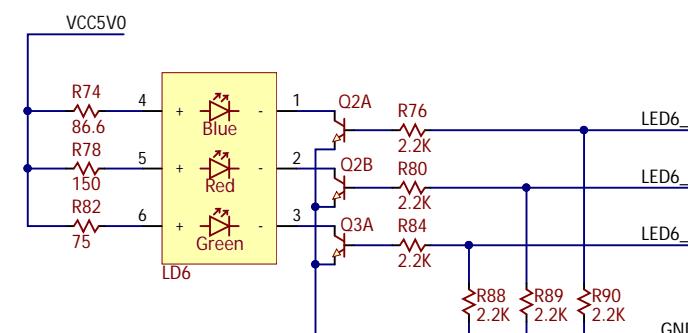
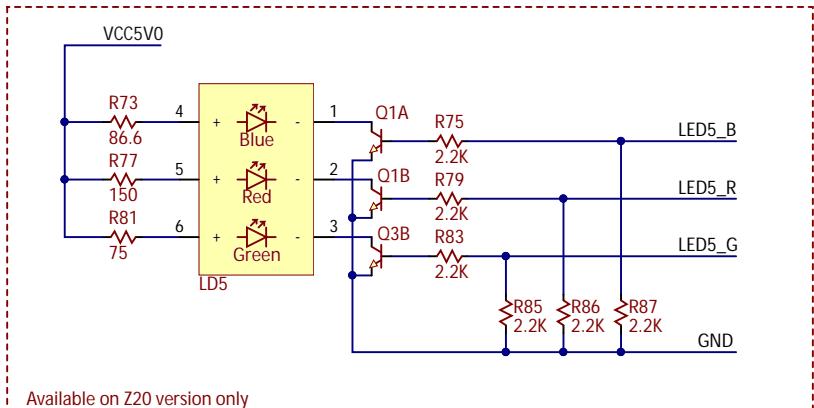
PL SWITCHES



PL LEDs



RGB LEDs

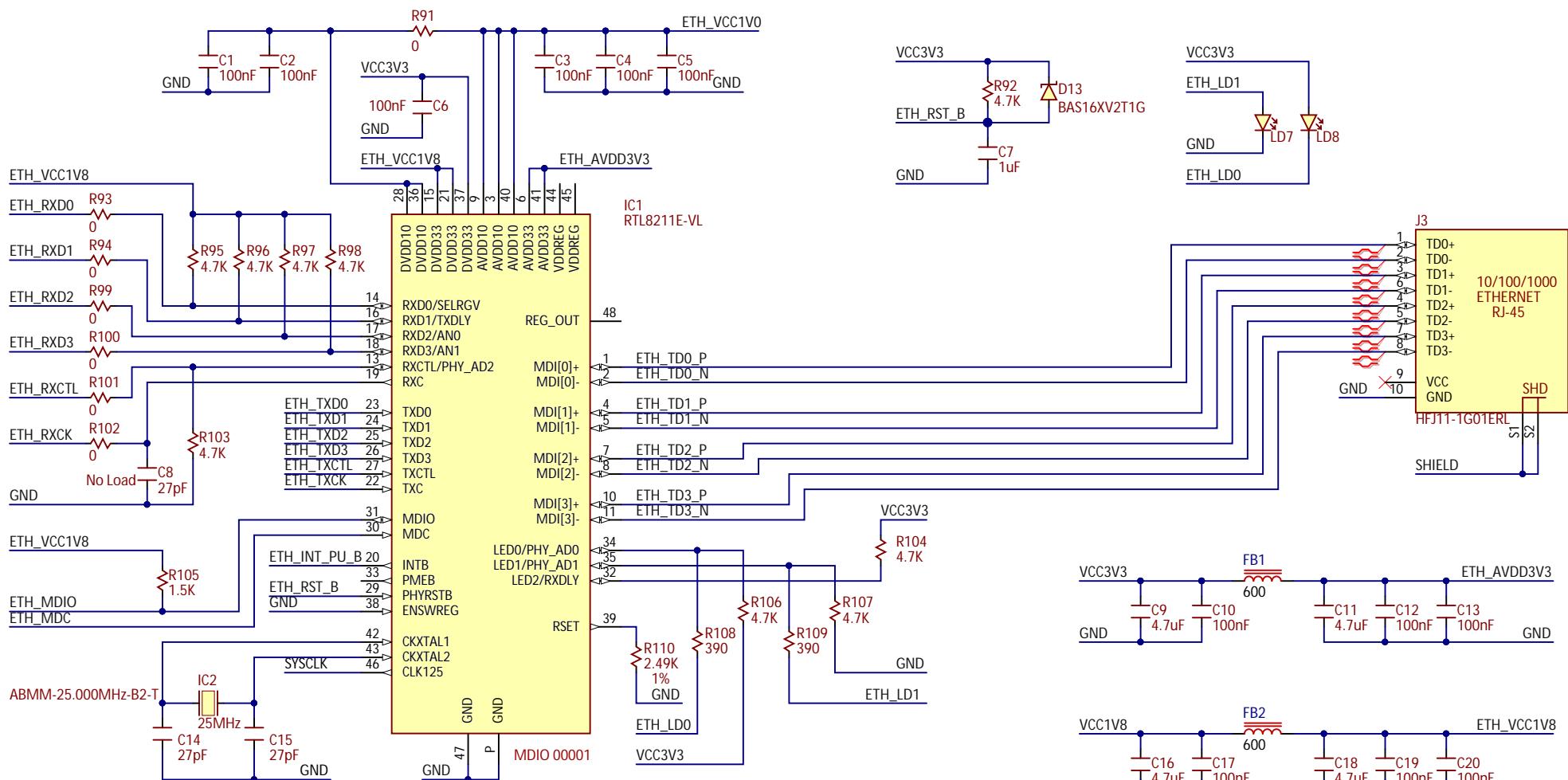


Title

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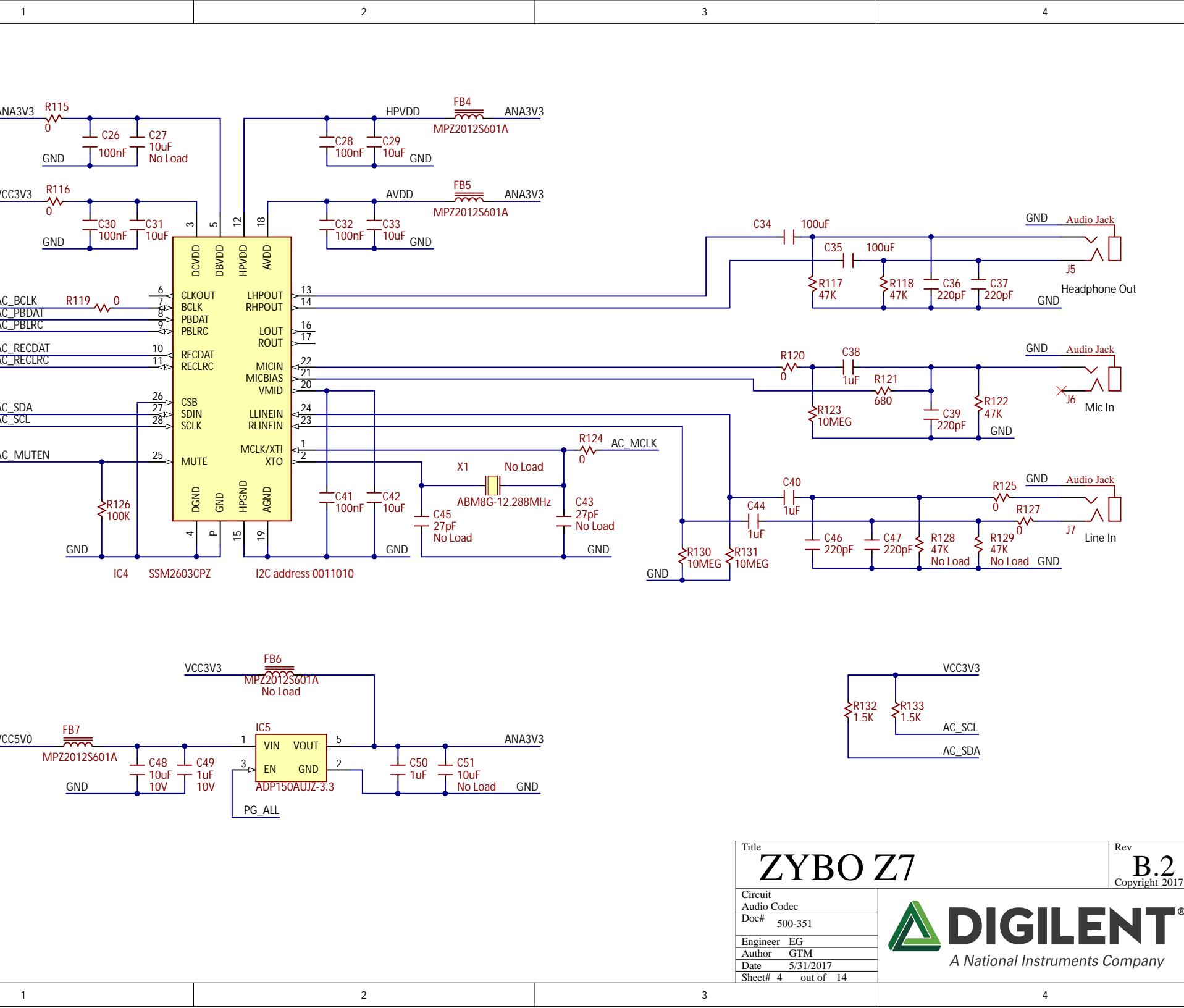
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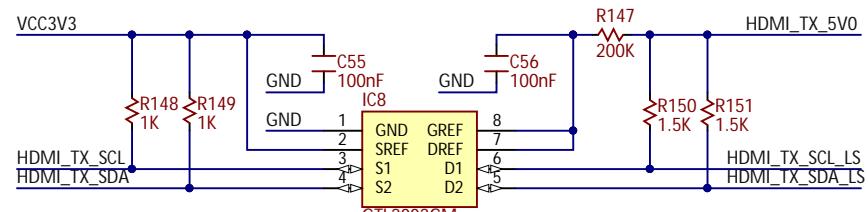
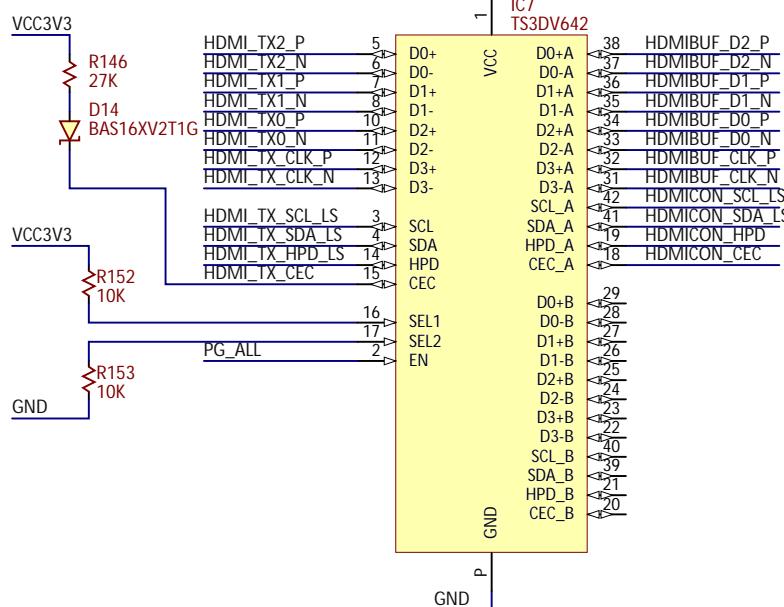
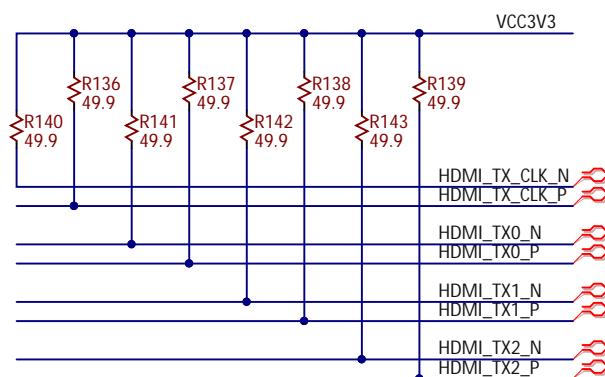
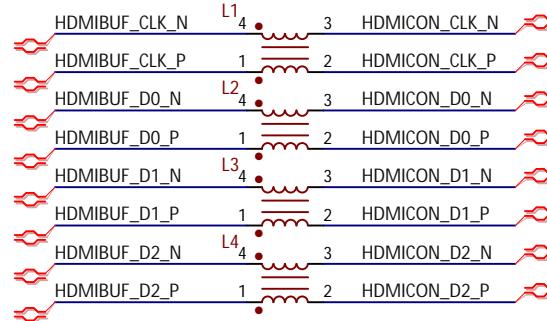
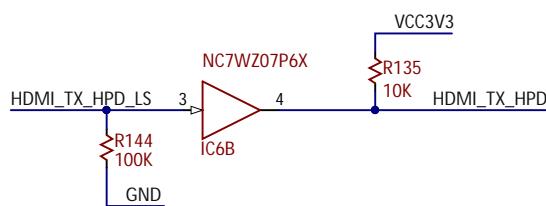
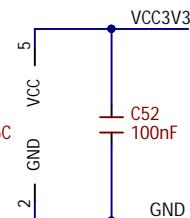
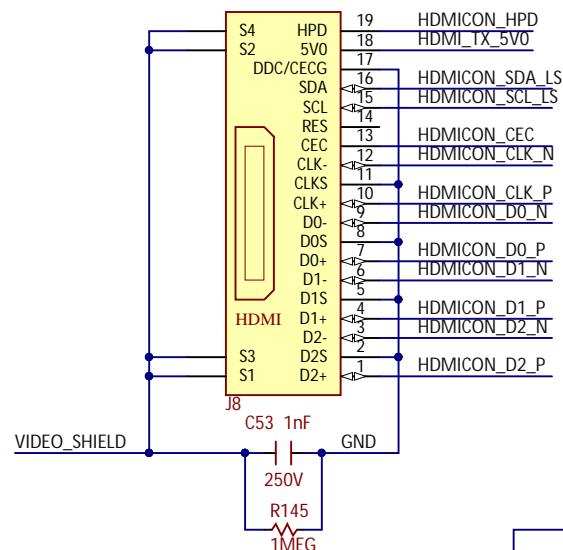
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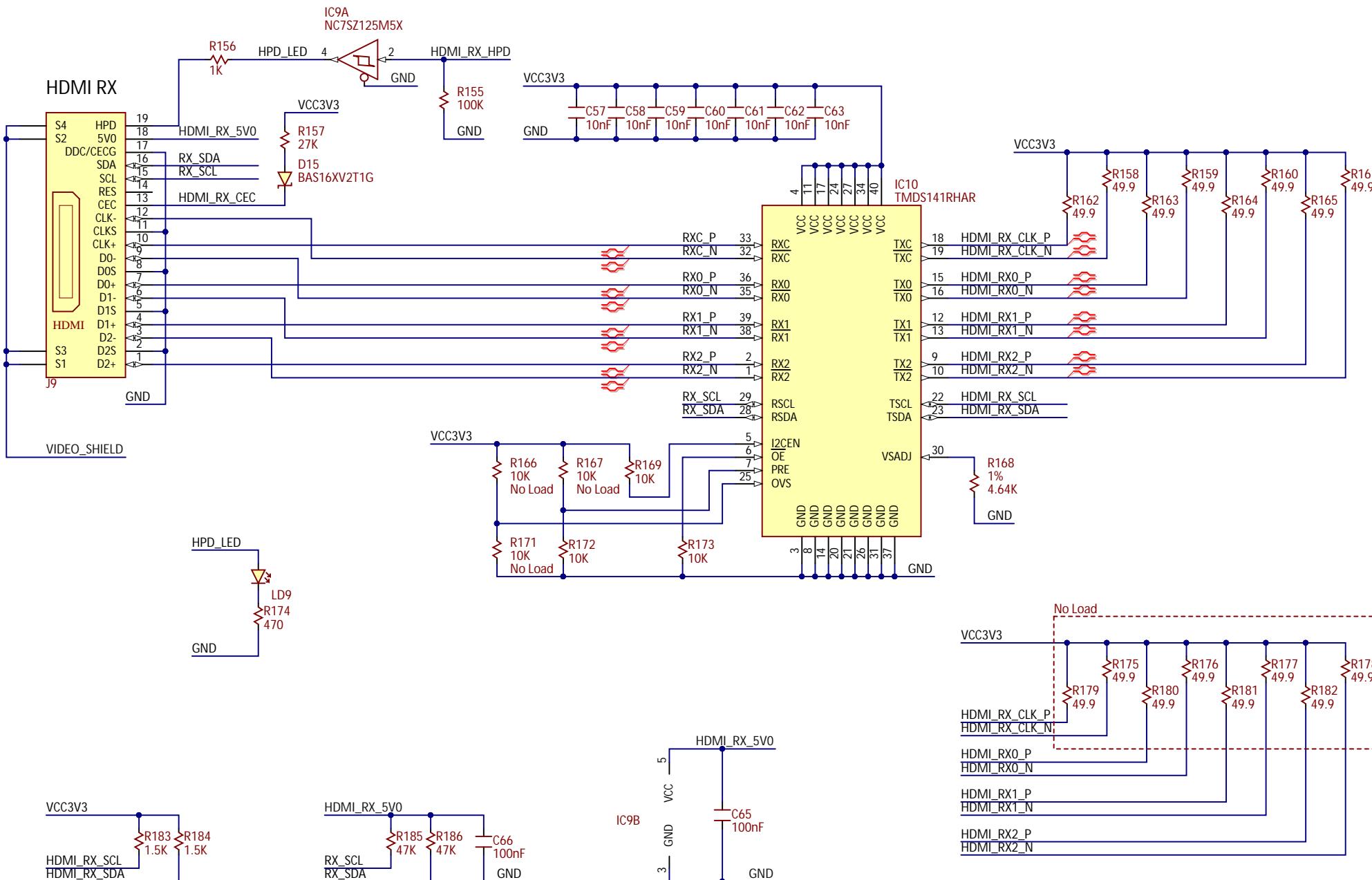


Title		Rev
ZYBO Z7		B.2
Circuit	Ethernet and SD Card	Copyright 2017
Doc#	500-351	
Engineer	EG	
Author	GTM	
Date	5/31/2017	
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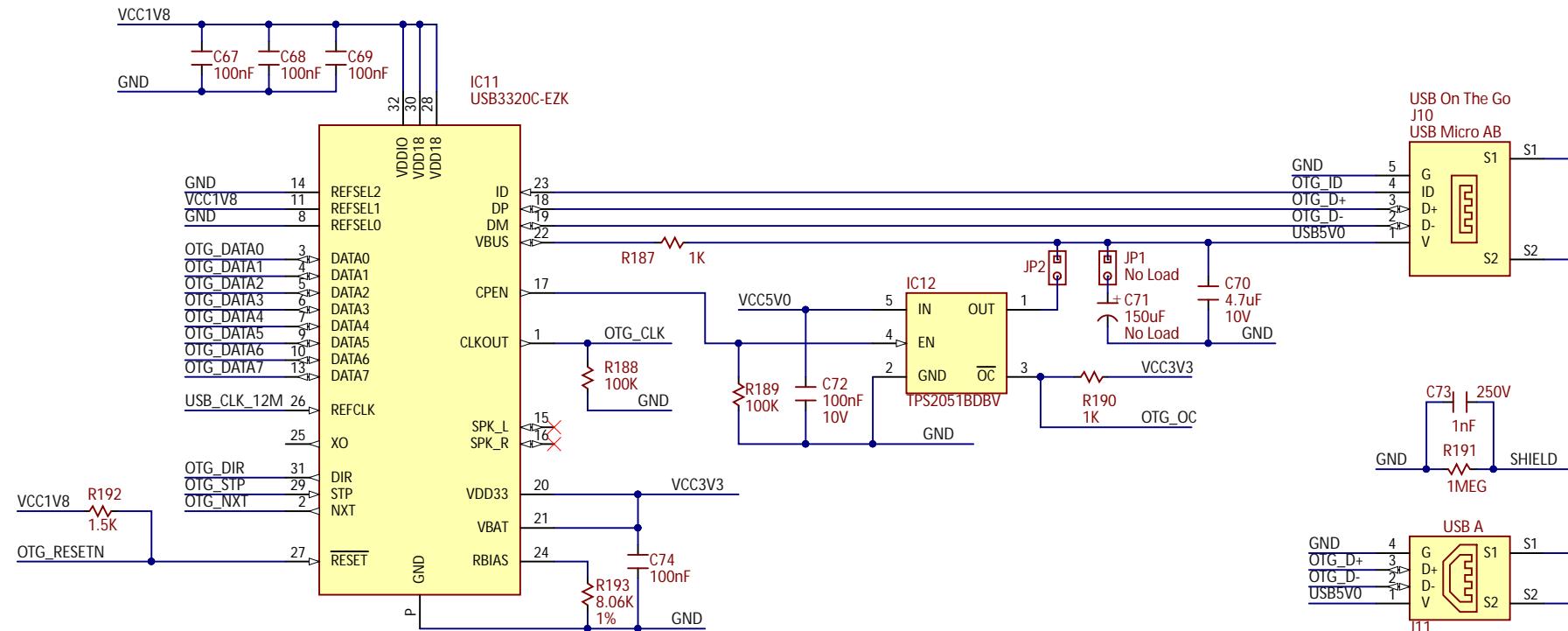
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**HDMI TX**



A



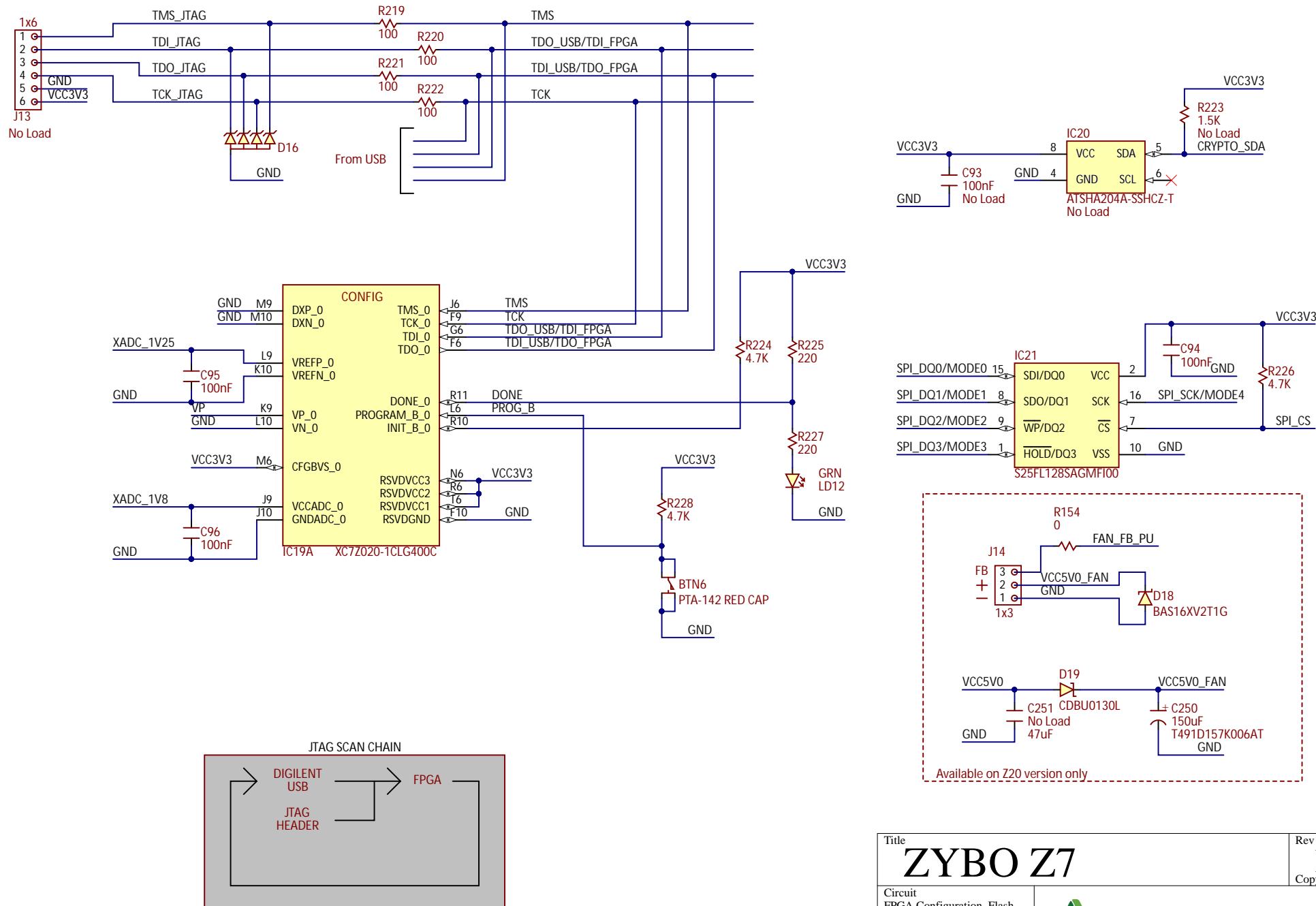
Title
ZYBO Z7

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B.2
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Circuit	USB OTG
Doc#	500-351
Engineer	EG
Author	GTM
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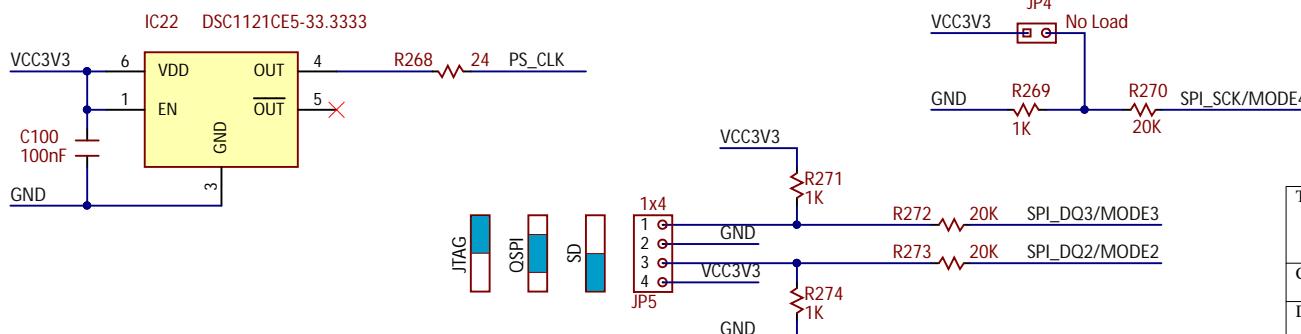
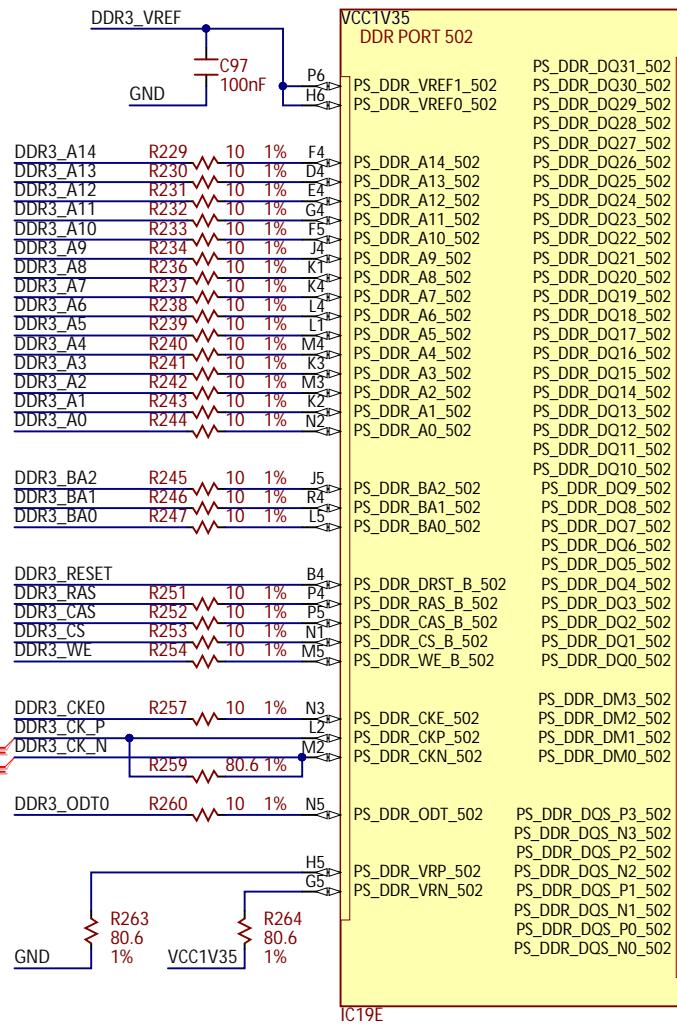


Title

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B.2
Copyright 2017

Circuit	FPGA Configuration, Flash
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Author	GTM
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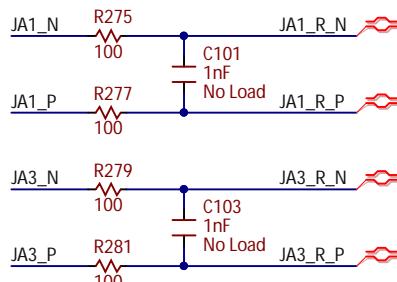
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VCC3V3

BANK 34	
IO_L0P_T0_34	R19 AC_BCLK
IO_L1P_T0_34	J11 JC2_P
IO_L1N_T0_34	J10 JC2_N
IO_L2P_T0_34	J12 JC4_P
IO_L2N_T0_34	U12 JC4_N
IO_L3P_TO_DOS_PUDC_B_34	U13 OTG_OC
IO_L3N_TO_DOS_34	V13 JE7
IO_L4P_T0_34	V12 JE1
IO_L4N_T0_34	W13 SW2
IO_L5P_T0_34	T14 JD1_P
IO_L5N_T0_34	T15 JD1_N
IO_L6P_T0_34	P14 JD2_P
IO_L6N_TO_VREF_34	R14 JD2_N
IO_L7P_T1_34	Y16 BTN3
IO_L7N_T1_34	Y17 JE10
IO_L8P_T1_34	W14 JC3_P
IO_L8N_T1_34	Y14 JC3_N
IO_L9P_T1_DOS_34	T16 SW3
IO_L9N_T1_DOS_34	U17 JE8
IO_L10P_T1_34	V15 JC1_P
IO_L10N_T1_34	W15 JC1_N
IO_L11P_T1_SRCC_34	U14 JD3_P
IO_L11N_T1_SRCC_34	U15 JD3_N
IO_L12P_T1_MRCC_34	U18 HDMI_RX_CLK_P
IO_L12N_T1_MRCC_34	U19 HDMI_RX_CLK_N
IO_L13P_T1_MRCC_34	N18 AC_SCL
IO_L13N_T2_MRCC_34	P19 CRYPTO_SDA
IO_L14P_T2_SRCC_34	N20 HDMI_RX2_P
IO_L14N_T2_SRCC_34	P20 HDMI_RX2_N
IO_L15P_T2_DOS_34	J20 HDMI_RX1_P
IO_L15N_T2_DOS_34	J20 HDMI_RX1_N
IO_L16P_T2_34	V20 HDMI_RX0_P
IO_L16N_T2_34	W20 HDMI_RX0_N
IO_L17P_T2_34	Y18 AC_RECLRC
IO_L17N_T2_34	Y19 HDMI_RX_SDA
IO_L18P_T2_34	V16 LED6_R
IO_L18N_T2_34	W16 JE2
IO_L19P_T3_34	R16 AC_RECDA
IO_L19N_T3_VREF_34	T17 JE9
IO_L20P_T3_34	V18 JD4_P
IO_L20N_T3_34	V18 JD4_N
IO_L21P_T3_DOS_34	W18 HDMI_RX_SCL
IO_L21N_T3_DOS_34	W19 HDMI_RX_HPD
IO_L22P_T3_34	N17 AC_SDA
IO_L22N_T3_34	P18 AC_MUTEN
IO_L23P_T3_34	P15 SW1
IO_L24P_T3_34	P16 BTN1
IO_L24N_T3_34	I19 AC_PBLRC
IO_25_34	

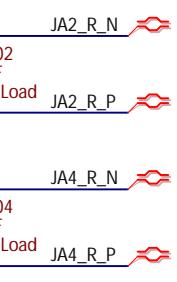
IC19C



VCC3V3

BANK 35	
IO_L0P_T0_35	G14 LED2
IO_L1P_T0_AD0P_35	C20 HDMI_TX1_P
IO_L1N_T0_AD0N_35	B20 HDMI_TX1_N
IO_L2P_T0_AD8P_35	B19 HDMI_TX2_P
IO_L2N_T0_AD8N_35	A20 HDMI_TX2_N
IO_L3P_TO_DOS_PUDC_B_35	E17 ETH_RST_B
IO_L3N_TO_DOS_35	D18 LED3
IO_L4P_T0_35	D19 HDMI_RX_P
IO_L4N_T0_35	D20 HDMI_RX_N
IO_L5P_T0_35	E18 HDMI_TX_HPD
IO_L5N_T0_35	E19 HDMI_TX_CEC
IO_L6P_T0_35	F16 ETH_INT_PU_B
IO_L6N_TO_VREF_35	F17 LED6_G
IO_L7P_T1_35	M19 MIPI_LANE0_P
IO_L7N_T1_AD2P_35	M20 MIPI_LANE0_N
IO_L7N_T1_AD2N_35	M17 LED6_B
IO_L8P_T1_AD10P_35	M18 LP_LANE0_N
IO_L8N_T1_AD10N_35	M19 LP_LANE0_P
IO_L9P_T1_DOS_AD3P_35	I20 LP_LANE1_N
IO_L9N_T1_DOS_AD3N_35	K19 BTN2
IO_L10P_T1_AD1P_35	J19 LP_CLK_N
IO_L10N_T1_AD11N_35	I16 MIPI_LANE1_P
IO_L11N_T1_SRCC_35	I17 MIPI_LANE1_N
IO_L12P_T1_MRCC_35	K17 SYSCLK
IO_L12N_T1_MRCC_35	K18 BTN0
IO_L13P_T2_MRCC_35	H16 HDMI_TX_CLK_P
IO_L13N_T2_MRCC_35	H17 HDMI_TX_CLK_N
IO_L14P_T2_AD4P_SRCC_35	I18 MIPI_CLK_P
IO_L14N_T2_AD4N_SRCC_35	H18 MIPI_CLK_N
IO_L15P_T2_DQS_AD12P_35	F19 CAM_SDA
IO_L15N_T2_DQS_AD12N_35	F20 CAM_SCL
IO_L16P_T2_35	G17 HDMI_TX_SCL
IO_L16N_T2_35	G18 HDMI_TX_SDA
IO_L17P_T2_AD5P_35	I20 LP_LANE1_P
IO_L17N_T2_AD5N_35	H20 LP_CLK_P
IO_L18P_T2_AD13P_35	G19 CAM_CLK
IO_L18N_T2_AD13N_35	G20 CAM_GPIO
IO_L19P_T3_35	H15 JE4
IO_L19N_T3_VREF_35	G15 SW0
IO_L20P_T3_AD6P_35	K14 JA4_R_P
IO_L20N_T3_AD6N_35	J14 JA4_R_N
IO_L21P_T3_DQS_AD14P_35	N15 JA1_R_P
IO_L21N_T3_DQS_AD14N_35	N16 JA1_R_N
IO_L22P_T3_AD7P_35	I14 JA2_R_P
IO_L22N_T3_AD7N_35	I15 JA2_R_N
IO_L23P_T3_35	M14 LED0
IO_L23N_T3_35	M15 LED1
IO_L24P_T3_AD15P_35	K16 JA3_R_P
IO_L24N_T3_AD15N_35	J16 JA3_R_N
IO_25_35	J15 JE3

IC19D



BANK 13

IO_L6N_T0_VREF_13	V5
IO_L11P_T1_SRCC_13	U7 JB2_P
IO_L11N_T1_SRCC_13	V7 JB2_N
IO_L12P_T1_MRCC_13	T9
IO_L12N_T1_MRCC_13	U10
IO_L13P_T2_MRCC_13	V7 JB3_P
IO_L13N_T2_MRCC_13	V6 JB3_N
IO_L14P_T2_SRCC_13	V9
IO_L14N_T2_SRCC_13	V8 HDMI_RX_CEC
IO_L15P_T2_DQS_13	V8 JB1_P
IO_L15N_T2_DQS_13	V8 JB1_N
IO_L16P_T2_13	W10
IO_L16N_T2_13	W9
IO_L17P_T2_13	U9
IO_L17N_T2_13	U8
IO_L18P_T2_13	W11
IO_L18N_T2_13	Y11 LED5_R
IO_L19P_T3_VREF_13	T5 LED5_G
IO_L20P_T3_13	U5
IO_L20N_T3_13	Y12 LED5_B
IO_L21P_T3_DQS_13	Y13 FAN_FB_PU
IO_L21N_T3_DQS_13	V11
IO_L22P_T3_13	V10
IO_L22N_T3_13	V6 JB4_P
IO_25_33	W6 JB4_N

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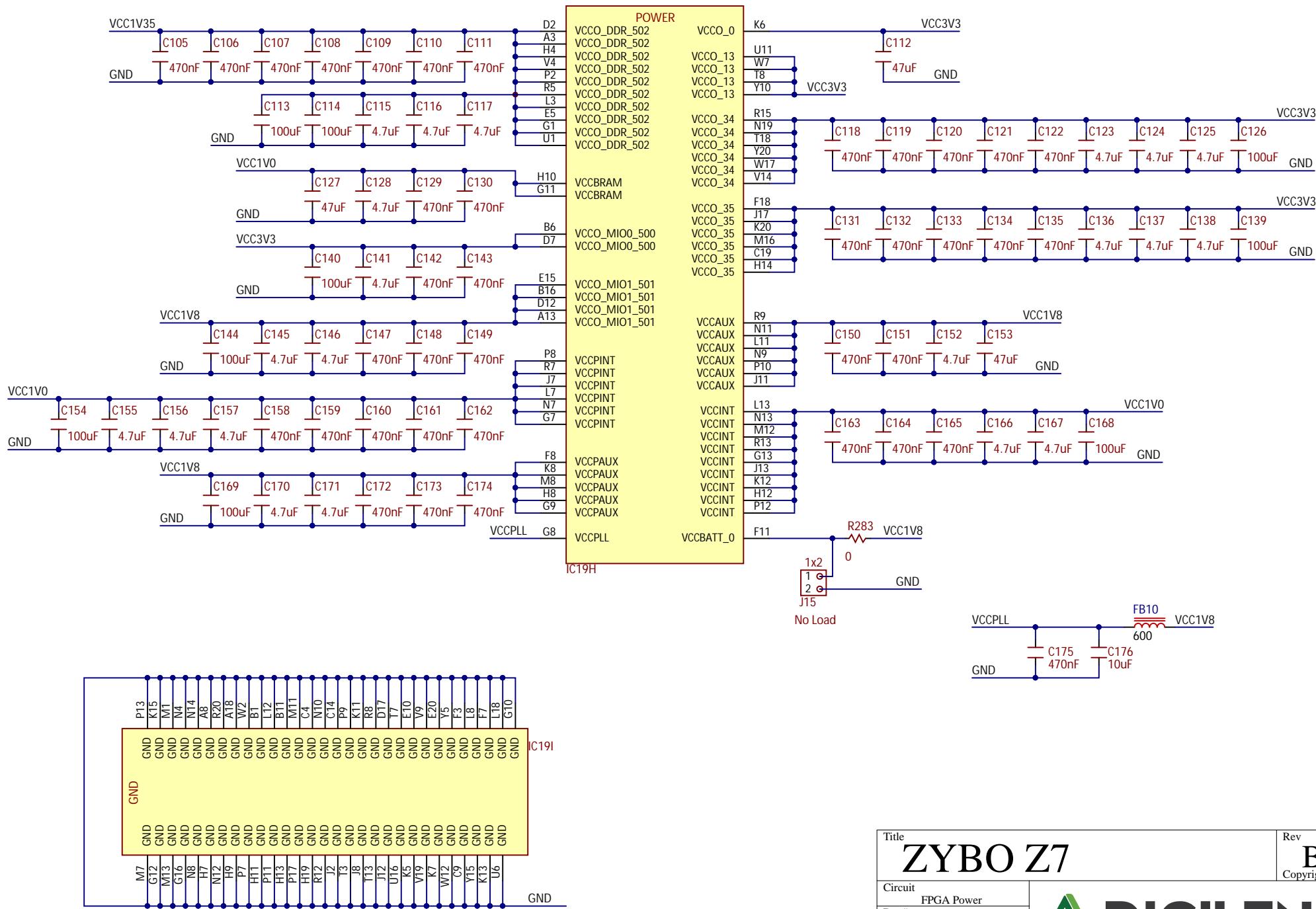
Title

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Circuit	Anti Alias Filters
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Author	GTM
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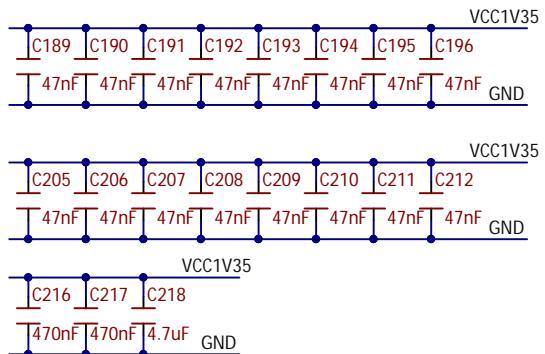
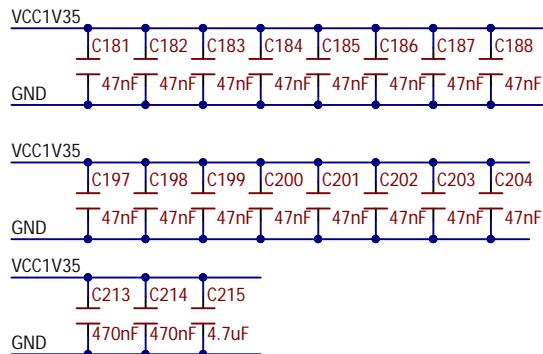
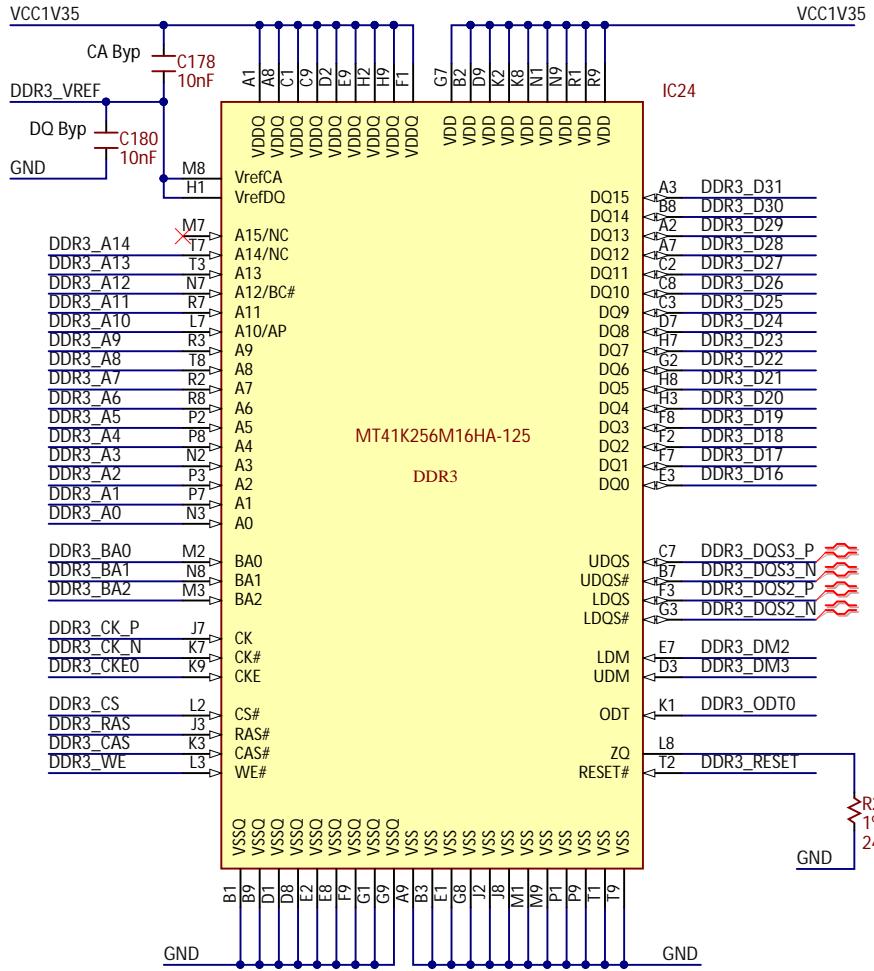
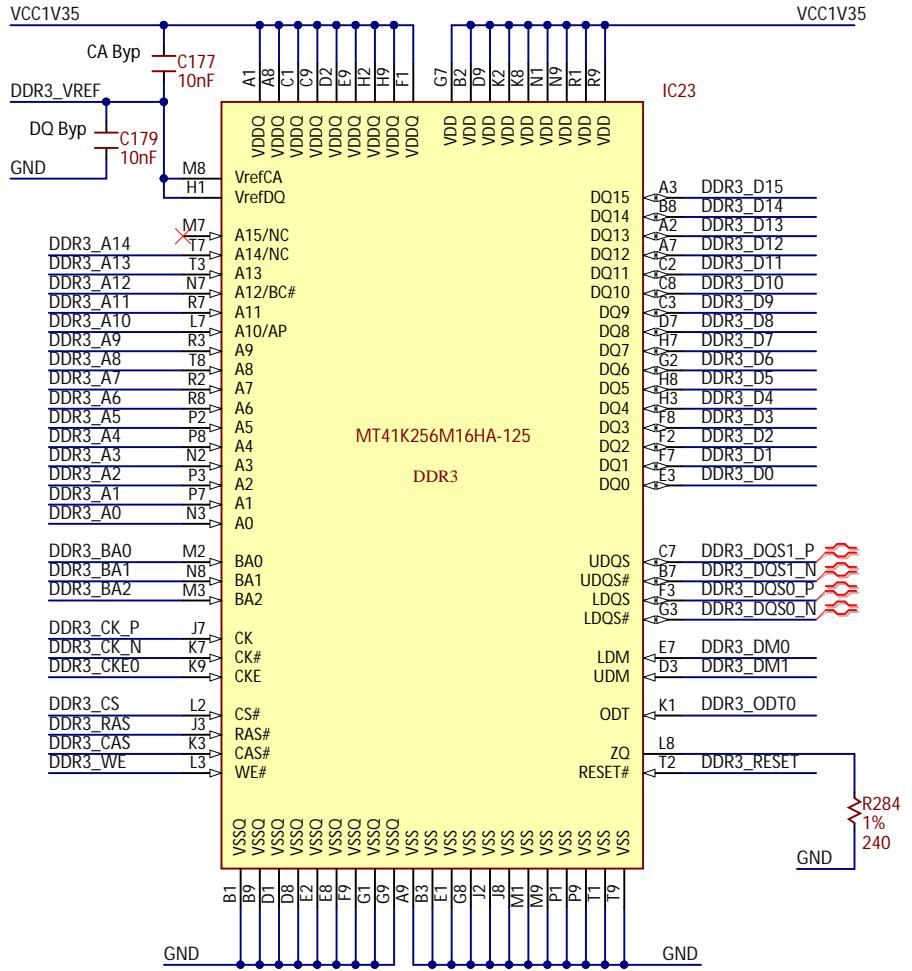




Title ZYBO Z7

Circuit	FPGA Pow
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