

DaxOS

PROJECT REPORT

submitted by

By

Nihal Narayan (MBT17CS081)

Antony S. Chirayil (MBT17CS023)

Mathew Koshy (MBT17CS068)

R Midhun Suresh (MBT17CS095)

to

the **APJ Abdul Kalam Technological University**

in partial fulfillment of the requirements for the award of the Degree

of

BACHELOR OF TECHNOLOGY

IN

COMPUTER SCIENCE AND ENGINEERING

June, 2021



DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

MAR BASELIOS COLLEGE OF ENGINEERING & TECHNOLOGY

Mar Ivanios Vidya Nagar, Nalanchira

Thiruvananthapuram 15

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

MAR BASELIOS COLLEGE OF ENGINEERING & TECHNOLOGY

Nalanchira, Thiruvananthapuram.



CERTIFICATE

*This is to certify that the report entitled **DaxOS** submitted by **Nihal Narayan (MBT17CS081)**, **Antony S. Chirayil (MBT17CS023)**, **Mathew Koshy (MBT17CS068)**, **R Midhun Suresh (MBT17CS095)** to the APJ Abdul Kalam Technological University in partial fulfillment of the requirements for the award of the Degree of Bachelor of Technology in Computer Science and Engineering and Technology is a bonafide record of the project work carried out by him/her under my/our guidance and supervision. This report in any form has not been submitted to any other University or Institute for any purpose*

Ms. Gayathri K. S.

Project Coordinator

Mr. V. S. Shibu

Guide

Dr. Tessy Mathew

Head of the Department

Place: Thiruvananthapuram

Date: 12/01/2021

Acknowledgement

We would like to take this opportunity to extend our sincere gratitude to **Mr. Shibu VS** for guiding us through the process of this project. We would also like to thank the seminar coordinator, **Mrs. Gayathri KS**, for providing us with this opportunity which has allowed us to explore a domain which would otherwise be beyond the confines of our academic course.

Nihal Narayan

Mathew Koshy

Antony S. Chirayil

R Midhun Suresh

ABSTRACT

Every computer science enthusiast is proficient in their operating system of choice. They may even know its underlying working from an old operating system course they took in college. However, it is usually the case that their knowledge and understanding is limited to theory and writing low-level system code is often considered an insurmountable challenge. This project hopes to change this attitude by developing a minimal yet functional 32-bit operating system that can be used in conjunction with theoretical teaching to promote and introduce systems programming. A minimal kernel guarantees easier to read source code (as opposed to the 27 million SLOC Linux kernel) and provides a gentler introduction to kernel development.

Contents

List of Figures	v
Nomenclature	vi
1 Introduction	1
1.1 Existing Technologies	2
1.1.1 Linux	2
1.1.2 Windows Research Kernel	2
1.1.3 MINIX	2
1.2 Proposed System	2
1.3 Technology Stack	3
2 Design Diagrams	4
2.1 Use-case diagram	4
2.2 Activity Diagram	6
2.3 Sequence Diagram	9
2.3.1 Keyboard Driver	9
2.3.2 User Command	10
2.3.3 Shutdown	12
3 Other Design Considerations	13
3.1 Kernel Architecture	13
3.2 Build Process	15
3.3 Keyboard Design Decisions	16
3.4 Memory Segmentation	16

4 Conclusion	18
REFERENCES	19

List of Figures

2.1	Use Case Diagram	5
2.2	Activity Diagram	8
2.3	Sequence Diagram for keyboard driver	10
2.4	Sequence Diagram for user command	11
2.5	Sequence Diagram for system shutdown	12
3.1	Illustration of Kernel Architecture	14

Nomenclature

<i>GDT</i>	Global Descriptor Table
<i>IDT</i>	Interrupt Descriptor Table
<i>ISR</i>	Interrupt Service Routine
<i>VGA</i>	Video Graphics Array
<i>APM</i>	Advanced Power Management

Chapter 1

Introduction

The Kernel is the fundamental interconnect between hardware and software of a computer system. Writing a kernel (kernel programming) is considered to be a difficult endeavor because development has to start from a bare metal state. DAX OS is a minimal 32-bit hobbyist operating system that can be used to provide a gentle introduction to students who wish to explore the domain of systems programming. The project is open source and licensed under GNU General Public License v3.0 to ensure unrestricted access and complete transparency. DAX OS comes with a terminal driver, keyboard and mouse driver and basic memory management. The project also uses appropriate development practises such as unit-testing and version control.

1.1 Existing Technologies

1.1.1 Linux

Linux Kernel, created by Linus Torvals, is extremely large and contains about 27.8 million lines of code. It is free and open-source and compiling takes a long time. Kernel code is written in the C programming language with GCC compiler.

1.1.2 Windows Research Kernel

Windows Research Kernel is not free and open-source. It is difficult to get access as one needs to get registered through the University. It is based on older windows XP / Server 2003 code. It is outdated and is no longer in development.

1.1.3 MINIX

Minix microkernel is the best introductory kernel and is developed by Andrew S. Tanenbaum. However, the kernel has a micro-kernel design instead of the familiar monolithic kernel design. Traditional UNIX based kernels (eg: linux) follow monolithic design. It was created for educational purposes and it is now open-source.

1.2 Proposed System

We propose to build a 32-bit kernel that has the following functionality:

1. Keyboard Driver

Dax-OS will include a fully functional PS/2 keyboard driver. The PS/2 keyboard driver will convert scan-codes generated when the user presses a key on the keyboard to an integer character code. It must be noted that all keyboards practically used in modern day utilize the USB standard. We stick with the PS/2 protocol because the USB protocol is massive and difficult to implement. However there are practically no disadvantages from such a decision because most motherboards will emulate USB keyboards as PS/2 keyboards.

2. Mouse Driver

Similar to the keyboard driver, the operating system will also contain a functional mouse driver. The mouse can be utilized to move the terminal window up and down.

3. Terminal Display Driver

DaxOS is a terminal based operating system i.e it does not support windowing. The display support will be implemented using VGA text-mode and real-mode.

4. Basic Memory Management

DaxOS will use a flat memory model. Since it is an 32-bit operating system, it will support at most 4GB of addressable memory. A custom memory allocator will also be implemented.

5. Graphics Support

DaxOS will provide basic graphics capability using the above mentioned VGA real-mode to draw primitive shapes with the available 16 colors.

1.3 Technology Stack

The bulk of the operating system is written in the C programming language. Certain functionality such as writing data to ports and loading tables (IDT, GDT) are implemented using either GCC inline assembly or using normal x86 assembly.

The project uses the GCC cross-compiler & binutils to target the generic i686 platform; which is a generic 32-bit Intel P6 architecture. The GNU Assembler and Linker are also used. The assembly syntax style used is AT&T.

The entire compilation process is driven using GNU Make which uses Makefiles to build the project. The compilation is initiated using BASH shell scripts. The kernel is tested using the qemu-i386 emulator and is developed on a stable Xubuntu distribution.

DAX OS uses git as its version control system of choice. The git repository is uploaded on Github and every new feature is developed on its own separate branch. Team communication and coordination are actualized using discord with Github integration enabled.

Chapter 2

Design Diagrams

2.1 Use-case diagram

The use case diagram illustrates how the user interacts with the operating system. There is a total of three interactions between user and the operating system in this case. The first interaction that the user initiates is the boot process.

This involves:

1. **Setting up interrupts**

The concept behind interrupts is that when a piece of hardware or software wants to interrupt the CPU to do something important, an interrupt is raised. The CPU executes a program called the ISR (Interrupt Service Routine) and returns back to what it was doing before.

2. **Loading VGA driver**

Using VGA to print text to the screen is quite simple and is achieved by using VGA text-mode. The user can directly write to the video memory located at address 0xB8000. Each character that is to be printed requires a two-byte representation: 1 byte, called the code-point is used to represent the character in ASCII. 1 byte, is used to set the background and foreground colors. There are 16 colors that can be used. In VGA text-mode a maximum of 80x25 characters can be printed on the screen at a time.

3. Load keyboard driver

The keyboard driver uses the PS/2 interface to enable communication between the keyboard and the computer. When a key is pressed on the keyboard, the PIC raises IRQ1. This triggers an ISR which on execution will store the pressed key on a circular buffer. The data from the circular buffer is read by primitives from the C standard library such as scanf in stdio. The data from keyboard is read from port 0x60 using inline assembly function **inportb()** defined in io.c file.

The second interaction involves the user issuing a command to the terminal. The command may require dynamic memory to be allocated. Dynamic memory is allocated by implementing the malloc C function. The malloc function acts as a wrapper around the kernels memory manager. This needs paging and virtual memory to be enabled.

The final and last interaction between the user and the system is the shutdown sequence. This can be implemented by supporting Intel's ACPI protocol or the now depreciated APM protocol. These protocols control the amount of power each device is given. Additionally shutting down specific emulators by writing data to specific ports can be done.

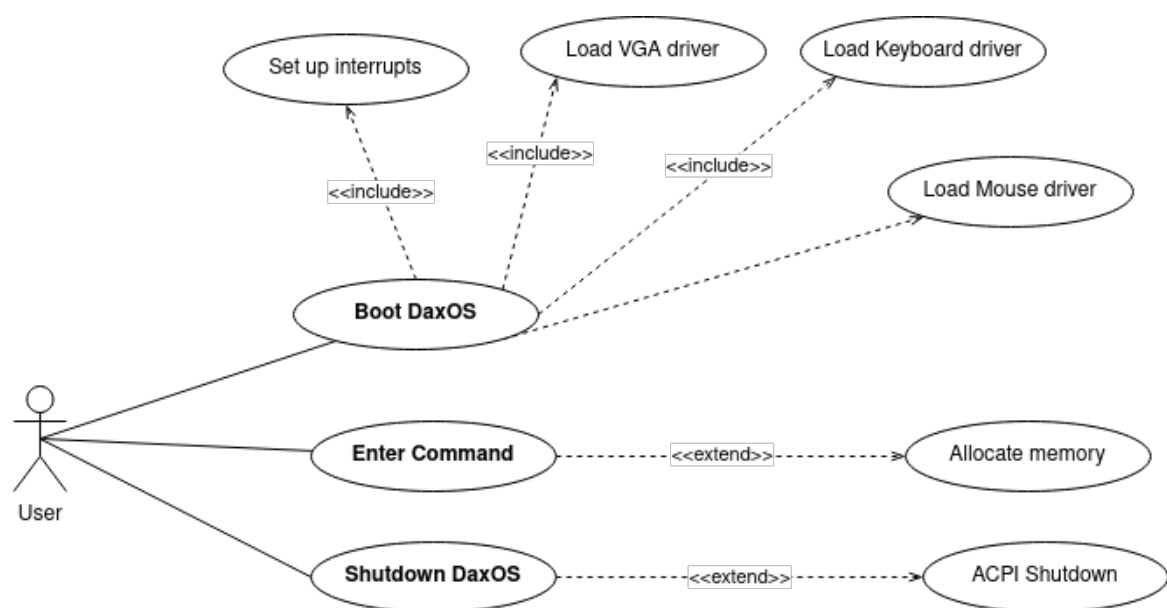


Figure 2.1: Use Case Diagram

2.2 Activity Diagram

The activity diagram illustrates the shell scripts that are used to build and compile the kernel.

The most important script here is the **build.sh** shell script which gives the make program to compile our source code. The build script relies further on two other shell scripts:

1. **headers.sh** script

Since in this project a version of C standard library is implemented the compilation of kernel is done by instructing the GCC cross-compiler to look for system headers in the SYSROOT directory. This script compiles the custom C std-lib into an archive named libk.a and places it in the SYSROOT/usr/lib directory. Also it copies all the .h header files into SYSROOT/usr/include directory. Now compiling the kernel is done by passing the `-sysroot=SYSROOT` parameter.

2. **config.sh** script

This script sets up all the environment variables used by GNU Make. This allows the user to easily change the core aspects and tooling used in the projects later. For example, we could change the compiler from GCC to CLANG by simply changing the environment variable CC to CLANG. Likewise SYSROOT directory can be changed by changing a single variable. Once these two scripts are executed the build script runs the make-install command on each of the folder directory. It copies the output DaxOS.kernel file into the boot directory.

The **iso.sh** script builds a bootable .iso image file from our kernel. It first calls the previously described build script and then produced an iso file by using the grub-mkrescue program. This iso file can be loaded into any emulator to run the operating system. The iso files are built into the isodir directory.

The **clean.sh** script removes all the build artifacts from the compilation. Build artifacts are files that are produced by the compilation process that can always be reproduced by the compiler. Since it can be reproduced it is usually removed to maintain a clean project structure.

There are three kinds of build artifacts:

1. **Object files** - .o files
2. **Make dependencies** - .d files
3. **Unwanted directories** - sysroot, isodir

The clean script works by executing the make clean commands in each of the project directories. The make clean commands use the rm bash command to delete the build artifacts.

The last script is the **create-bootable-usb.sh** script. This script creates a bootable USB of the operating system. It needs sudo permission because it accesses the UNIX block file of the USB device. This of the form /dev/sda or something similar. This script formats the USB device and copies the kernel along with the GRUB bootloader into the USB device. Therefore there is a chance that the data in the USB device can be destroyed if the user is not careful. To prevent this a safety check that checks if the device has more than 10 GB capacity is done. If this is true we do not format the device and exits with error. If the capacity is less than 10GB, the USB is made bootable.

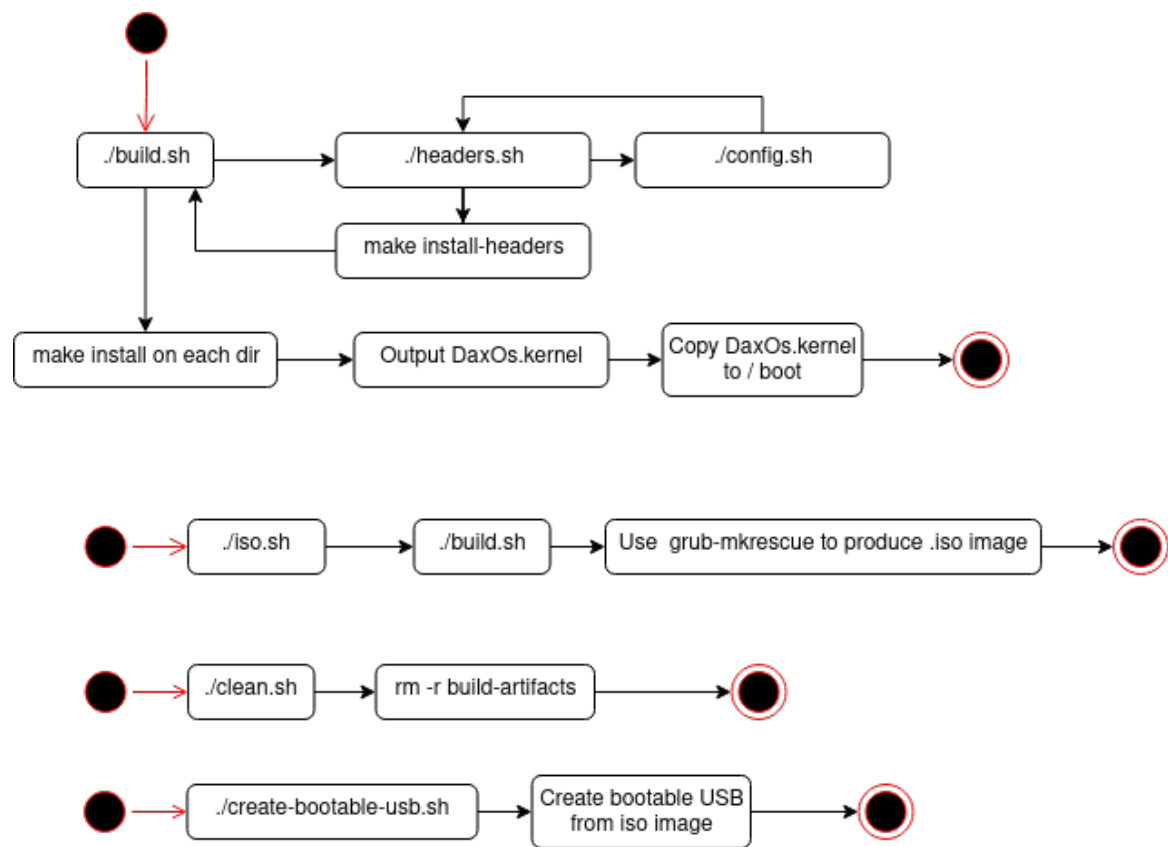


Figure 2.2: Activity Diagram

2.3 Sequence Diagram

2.3.1 Keyboard Driver

This diagram illustrates the time dependent and sequential interaction between the user, the OS and the keyboard driver. During boot, the Operating System initializes the keyboard driver. This involves:

1. Self Test

- (a) The keyboard device is disabled by sending command 0xAD and 0xA7 to the PS/2 controller.
- (b) The PS/2 controller's output buffer is flushed by reading from port 0x60.
- (c) Initiate the PS/2 controller self test by sending command 0xAA to it. A response of 0x55 indicates success.
- (d) Enable the keyboard device by sending commands 0xAE and 0xA8 to the PS/2 controller.
- (e) Reset the device by sending 0xFF to the keyboard.
- (f) Set the LED states by sending appropriate commands.

2. Handling Keypress

When the user presses a key the keyboard device initiates an interrupt. This is done by activating IRQ1 which is the standard interrupt line used by keyboards. In response to this interrupt, the CPU executes an ISR which updates a buffer with the read characters. The I/O functions in `stdio.h` like `scanf` will read from this buffer to perform the necessary computation and show the results back to the user.

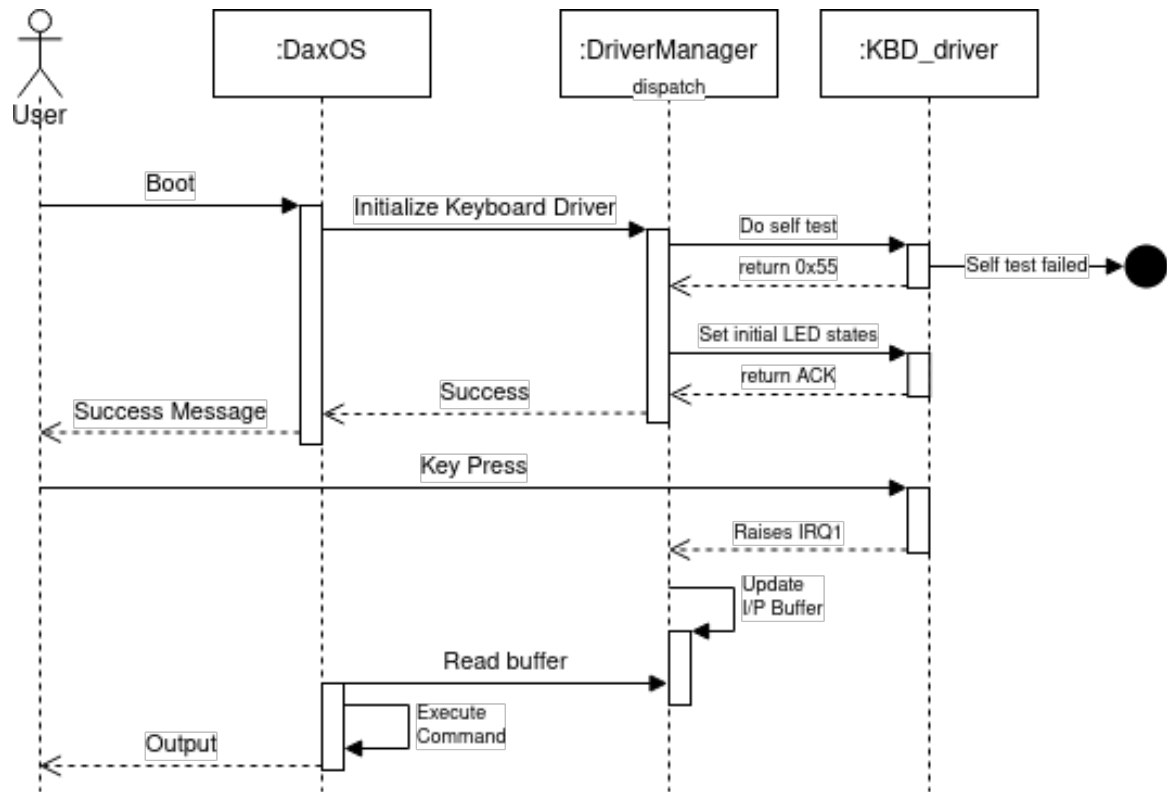


Figure 2.3: Sequence Diagram for keyboard driver

2.3.2 User Command

After boot the user interacts with DaxOS through a command prompt like windows.

It is here that the user enters commands which will be executed by the kernel.

These are not processes.

Here the following commands are implemented:

1. **gfx_demo**

This demo program will demonstrate some of the graphics and drawing capabilities.

2. **dcalc**

This is a calculator application that can evaluate mathematical expressions.

3. **term_art**

This will demonstrate some ASCII terminal art using VGA driver.

The sequence diagram illustrates the process of running the commands. All the commands use operations from standard C library. To take input and output text to

the screen the stdio functionality will be used. Then the command proceeds to do its computation. If dynamic memory is needed, the command again uses the malloc function from standard C library. The malloc function maintains a list of free stores - that is free blocks of memory. If the requested chunk of memory is already in the free store then the malloc function marks it as being used and returns a pointer to it. On the other hand if the memory in the list is not enough, the malloc function asks the kernel to provide more memory. In this case the kernel provides a new page to the malloc function and the malloc function promptly adds the block of memory into its free store and returns a pointer to it.

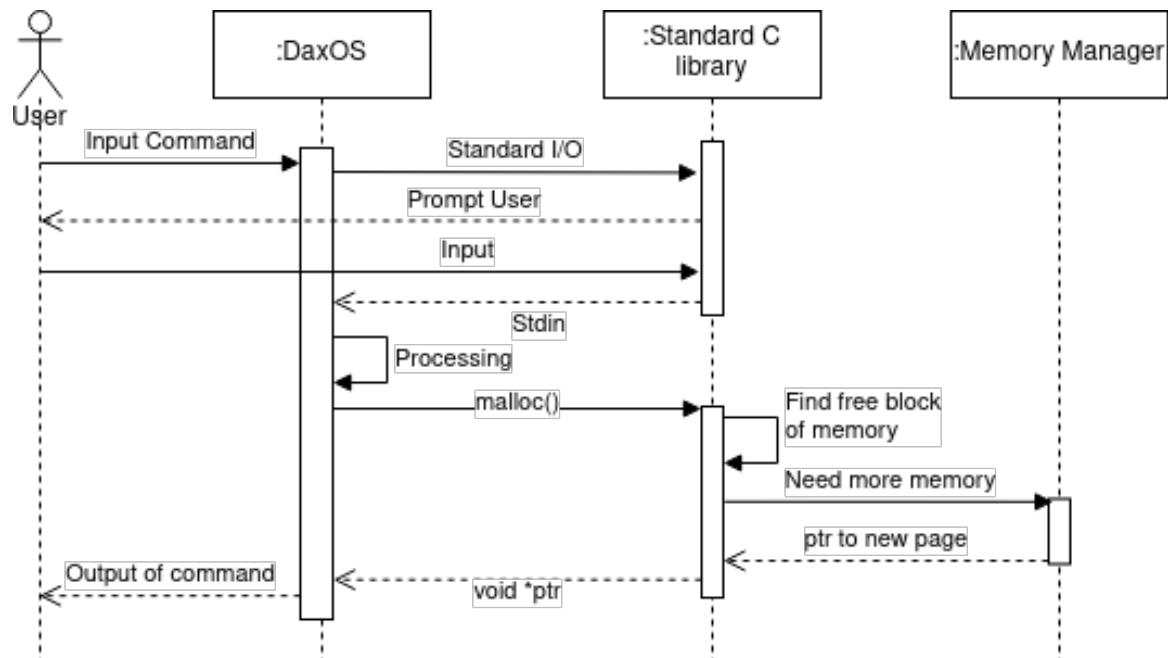


Figure 2.4: Sequence Diagram for user command

2.3.3 Shutdown

Shutdown is implemented using Advanced Power Management (APM).

Once the user initiates the shutdown command, there is no longer any interaction between the user and the OS. The OS will request the power management module to perform the APC shutdown procedure.

This involves calling a BIOS interrupt. This further requires switching from protected mode to real mode. Once in real mode, the BIOS is informed about the kind of operation that needs to be performed. The BIOS is informed that it is required to perform APC operation by setting the register AX with the value 0x5307. The BIOS is informed that the current operation is to be done on all connected devices by setting register BX with the value 0x0001. Lastly the value 0x03 is put in the CX register to indicate shutdown operation. Now the BIOS function is activated by raising software interrupt 0x15. The computer is powered off.

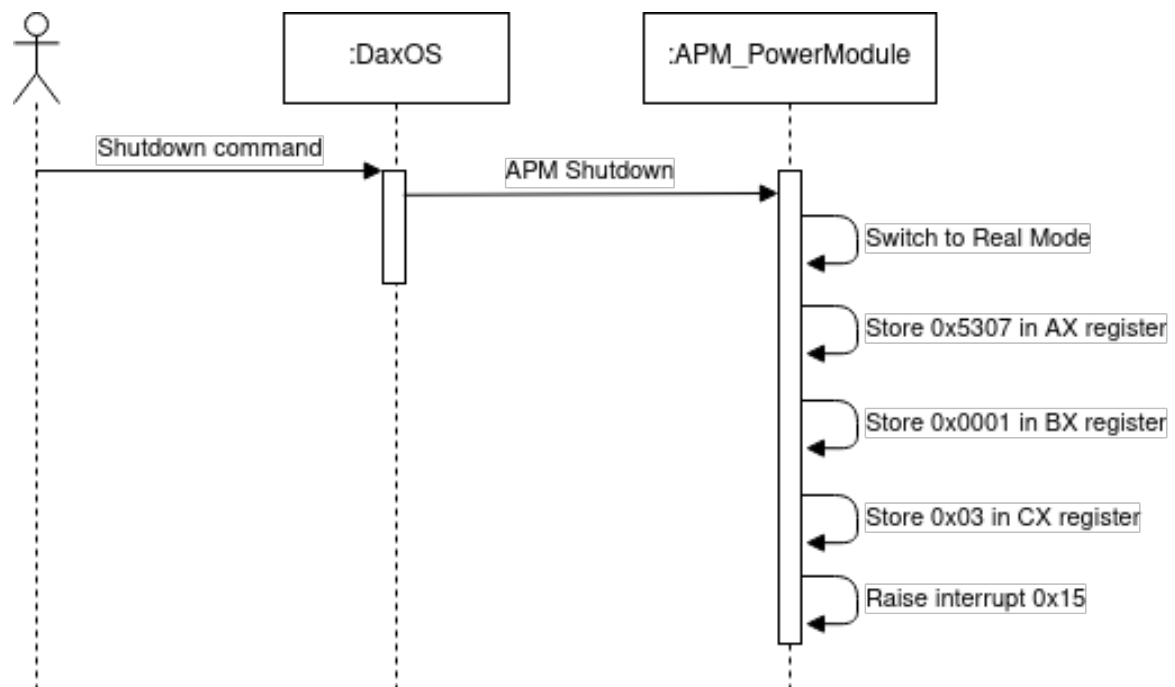


Figure 2.5: Sequence Diagram for system shutdown

Chapter 3

Other Design Considerations

3.1 Kernel Architecture

There are three types of kernel architectures:

1. **Micro Kernel**
2. **Monolithic Kernel**
3. **Hybrid Kernel**

This classification is based on what is in kernel space and what is in user space.

There are four protection rings that represent the access a piece of software has to the hardware and system. Out of these only two rings are of interest:

1. **Ring 0**

Code running in Ring 0 is said to be in supervisor mode. It has complete access to the hardware and system.

2. **Ring 3**

Code running in Ring 3 is said to be in user space. It has no direct access to hardware or the system. Instead it accesses the system through syscalls.

These protections are not simply implemented by the Operating system. Instead they are a part of the CPU architecture. They are activated by asm code.

- In a **micro kernel** very limited amount of code is in kernel space. For example, most drivers are in userspace.

- In a **monolithic kernel** device drivers and other similar modules operate in kernel space.
- A **hybrid kernel** is somewhere in the middle of what runs in kernel space and user space.

Dax OS has a monolithic design. This is motivated by two facts which are:

- The Linux kernel is monolithic. Therefore if we use a micro kernel architecture, students will not be able to transfer the knowledge that they acquired from our project into the linux development space.
- Our kernel operates completely in ring-0 i.e without any restrictions. Implementing user-mode is unnecessary since we have no user-mode programs. Additionally students need to be in ring-0 to understand how the hardware works.

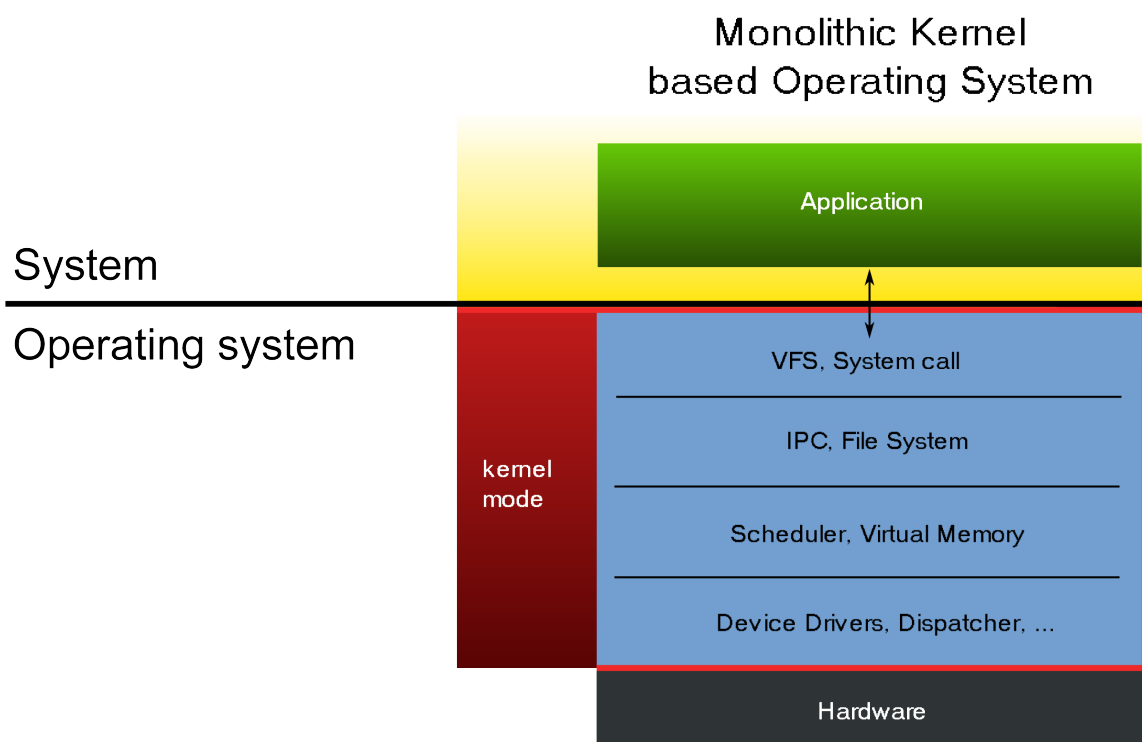


Figure 3.1: Illustration of Kernel Architecture

3.2 Build Process

The build architecture is a little involved because the C programming language does not have any real tooling for package management. Instead compiling and linking each translation unit must be done manually.

A tool called GNU Make is used to manage the compilation process. GNU Make is a tool which controls the generation of executables and other non-source files of a program from the program's source files. It does this by executing commands in a file called Makefile. The makefile contains rules which specify what prerequisites are needed to build a particular executable and the steps required to build it. We have multiple Makefiles in this project to recursively compile and build each of our kernel components. In fact, the Makefiles by themselves account for 25% of the codebase.

The Makefiles are executed by issuing corresponding make commands (for eg: **make install** or **make install headers**). Shell scripts are being used to drive the make program. There is a shell script called config.sh that sets up the system environment variables that are utilized by make. The compilation process installs the operating system into the system root directory. This is currently the directory **sysroot** within the project. Sysroot directory further contains two sub-directories - boot and usr. The boot directory contains **DaxOS.kernel** file and the usr directory contains the standard C library and unit tests. The kernel is booted by pointing the bootloader to the /boot directory. The built kernel is tested on qemu by calling the qemu.sh shell script. This works by first packaging the kernel into a .iso image file and running qemu with the iso file.

3.3 Keyboard Design Decisions

There are two design choices for the implementation of the keyboard driver:

1. **Polling Driven**

In polling, the CPU keeps checking the status of PS/2 keyboard constantly to detect a keypress. The command-ready bit indicates that the device needs servicing. This approach will lead to unnecessary loss of CPU cycles.

2. **Interrupt Driven**

When the user presses a key, the keyboard driver generates an interrupt which causes the CPU to run an ISR that handles the keypress. Interrupts are signalled by the interrupt request line.

Interrupt driven implementation is more efficient than polling driven keyboard drivers. Therefore interrupt driven keyboard driver will be implemented. The USB keyboard will be supported by emulation.

3.4 Memory Segmentation

Segmentation involves splitting the available memory into segments each having a particular purpose. For example, code may be stored in the Code Segment and data may be stored in the data segment. In real mode, the memory address is of the form S:O where S denotes the segment and O is the offset within that segment. This virtual memory address is converted to physical memory address using the following equation:

$$PhysicalAddress = (S * 0x10) + O$$

In 32-bit protected mode, segmentation is different in that it uses an entry in a table called GDT(Global Descriptor Table) instead of a segment number. Segmentation can be used to provide memory protection by controlling the memory access permission of each segment.

However, the use of segmentation to provide memory protection is often discouraged. For instance, you cannot use the C programming language if you use

segmentation for memory protection as most C compilers do not support it.

Therefore DaxOS does not use segmentation for memory protection.

Segmentation cannot be disabled but we can nullify the effects of segmentation by creating a flat memory model i.e we allow the CS and DS to overlap and extend across all the available memory. This is practically accomplished by adding two entries in the GDT - one for the CS and another for DS.

Chapter 4

Conclusion

This report has attempted to list the many design decisions that were available to the authors of this project and the justification for each such decision. UML diagrams for various interactions were also provided. This document and the decisions stated here are in no means final. DaxOS, like any other software project will undergo design changes as more functionality is implemented. However, the base of the design will more or less be static.

Interested readers can find the source code of this project at <https://github.com/DaxKernel/OS>.

You can also check out our website at <https://daxkernel.github.io/>.

REFERENCES

- [1] https://wiki.osdev.org/Bare_Bones
- [2] <http://www.osdever.net/tutorials/>