

Data path example for BZI  $pc=pc+3$

Control signals	Demux	Reg Write	ALU-1st-Src	ALUOp	ALU control	Mem Read	Mem Write	Mem2Reg	PC-Input-Mux-Src	PC-Mux-Src
OPTR r0	1	0	-	00001	-	-	-	-	-	1
BZI 3	0	0	1	10001	r0-0	0	0	0	0	0 if r0-0 ==0, 1 if !=

```
// IF r0==0, pc=pc+3; ELSE pc=pc+1
```

