

Data path example for STOREB data_mem[r0]=reg r1

Control signals	Demux	Reg Write	ALU-1st-Src	ALUop	ALU control	Mem Read	Mem Write	Mem2Reg	PC-Input-Mux-Src	PC-Mux-Src
OPTR r0	1	0	-	00001	-	-	-	-	-	1
STOREB r1	0	1	1	01011	Pass up-input directly	0	1	0	-	1

// store 1 byte from reg r1 to data_mem[r0], r0 is address

