

Data path example for BNZ $pc=pc+r1$

Control signals	Demux	Reg Write	ALU-1st-Src	ALUop	ALU control	Mem Read	Mem Write	Mem2Reg	PC-Input-Mux-Src	PC-Mux-Src
OPTR r0	1	0	-	00001	-	-	-	-	-	1
BNZ r1	0	0	1	10000	r0-0	0	0	0	1	1 if r0-0 == 0, 0 if !=

// IF r0!=0, pc=pc+r1; ELSE pc=pc+1

