

## Data path example for LUT

Control signals	Demux	Reg Write	ALU-1st-Src	ALUOp	ALU control	Mem Read	Mem Write	Mem2Reg	PC-Input-Mux-Src	PC-Mux-Src
OPTR r0	1	0	-	00001	-	-	-	-	-	1
LUT r1	0	1	1	01100	r0+LUT addr	1	0	1	-	1

// r1=LUT[r0]. For r0, 0 <= value of r0 <= 8, as index of Look Up Table. Load the value of LookUpTable[value of r0] into r1.

