

## Data path example for ADD r1=r0+r1.

Control signals	Demux	Reg Write	ALU-1st-Src	ALUOp	ALU control	Mem Read	Mem Write	Mem2Reg	PC-Input-Mux-Src	PC-Mux-Src
OPTR r0	1	0	-	00001	-	-	-	-	-	1
ADD r1 // r1=r0+r1	0	1	1	00011	add	0	0	0	-	1

