

Data path example for ADDI r0=3+r0.

Control signals	Demux	Reg Write	ALU-1st-Src	ALUop	ALU control	Mem Read	Mem Write	Mem2Reg	PC-Input-Mux-Src	PC-Mux-Src
OPTR 3	1	0	-	00001	-	-	-	-	-	1
ADDI r0 // r0=3+r0	0	1	0	01110	addi	0	0	0	-	1

