Crashes During Synthesis, Involving Function HARTNDb::map() in Vivado

I'm experiencing a crashing issue with Vivado while synthesizing design files. My system runs on Linux, and I'm using Vivado version 2023.1. The relevant commands I'm using in the Ubuntu terminal are as follows:

vivado -mode tcl

read_verilog rtl.v

synth_design -top top -gated_clock_conversion auto -bufg 16 -max_bram_cascade_height 20 -max_uram_cascade_height 1

During the synthesis process, there's a crash, and a log is generated with information about the cause of the crash.

```
Abnormal program termination (11)
Please check '/home/zou/vivado_project/hs_err_pid1706372.log' for details
segfault in /home/zou/software/vivado/Vivado/2023.1/bin/unwrapped/lnx64.o/vivado
 -exec vivado -mode tcl, exiting...
```

I've attached our design file rtl.v and the log output from Vivado.