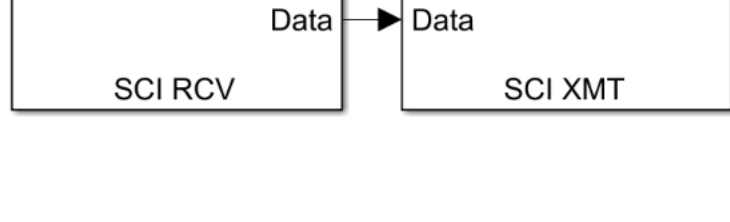
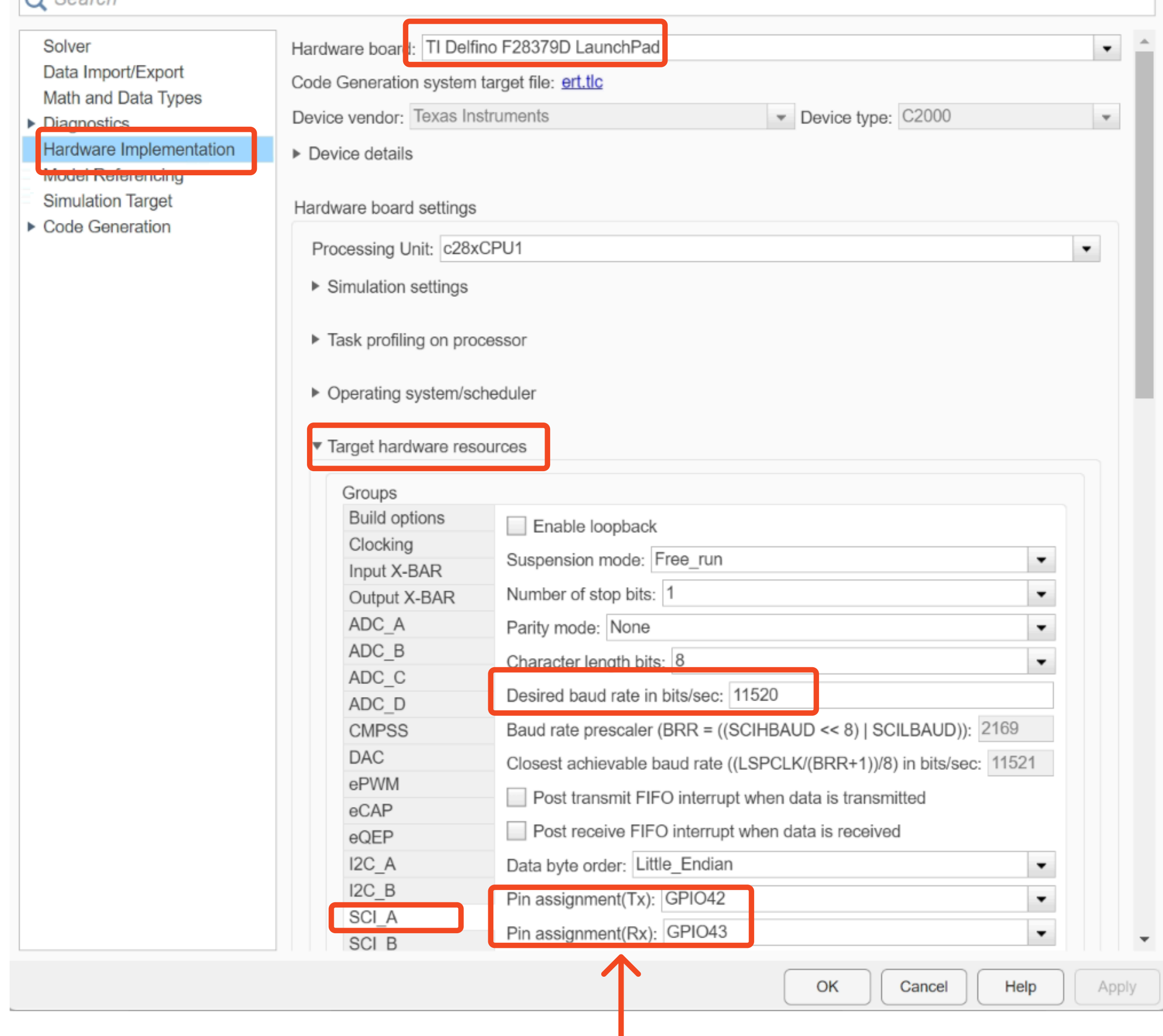


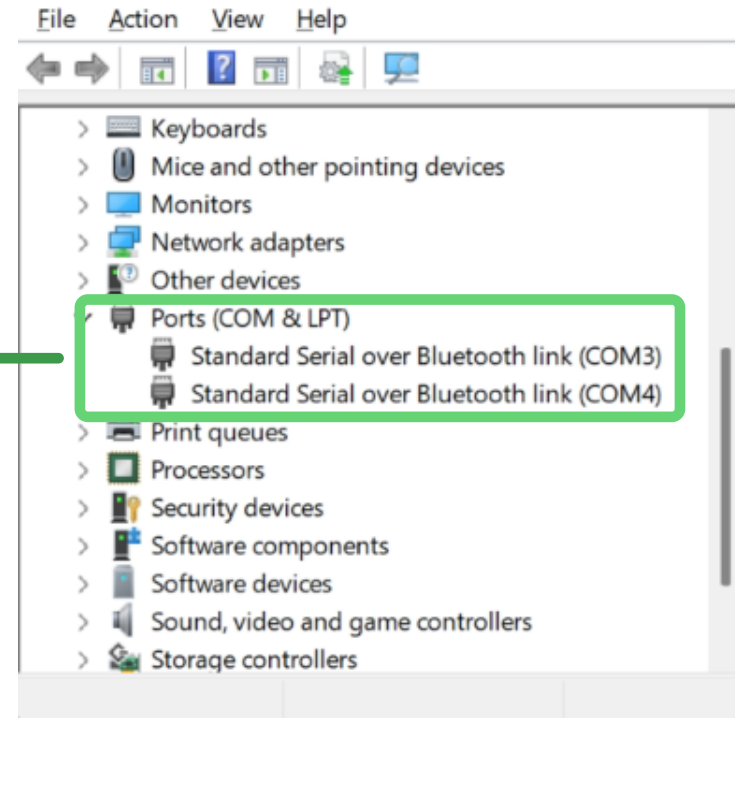
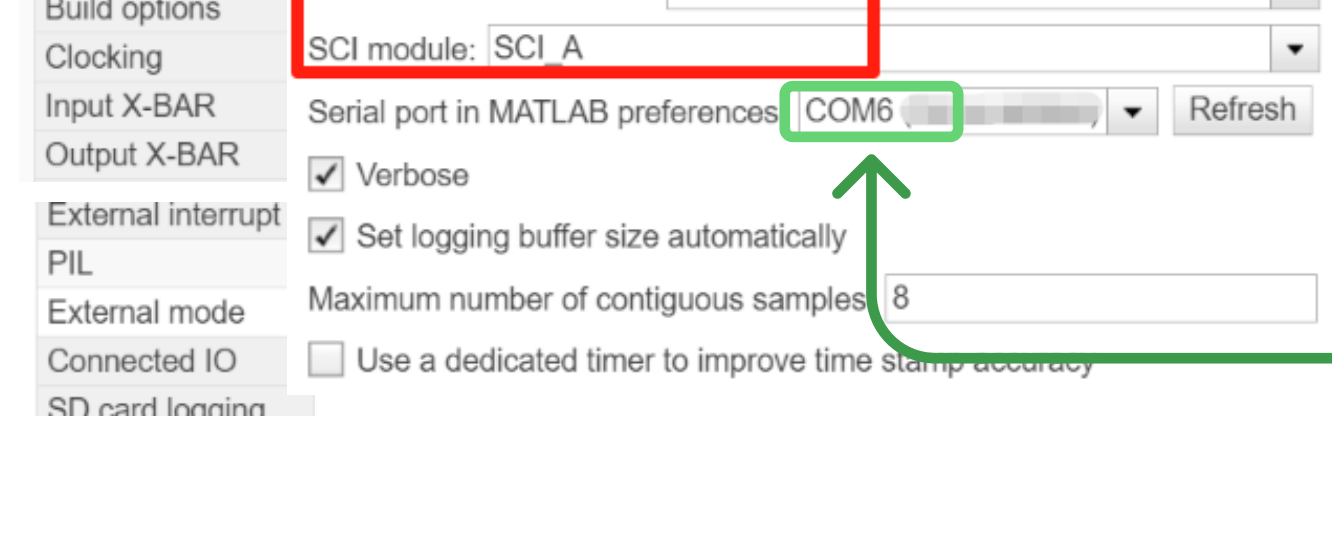
target model



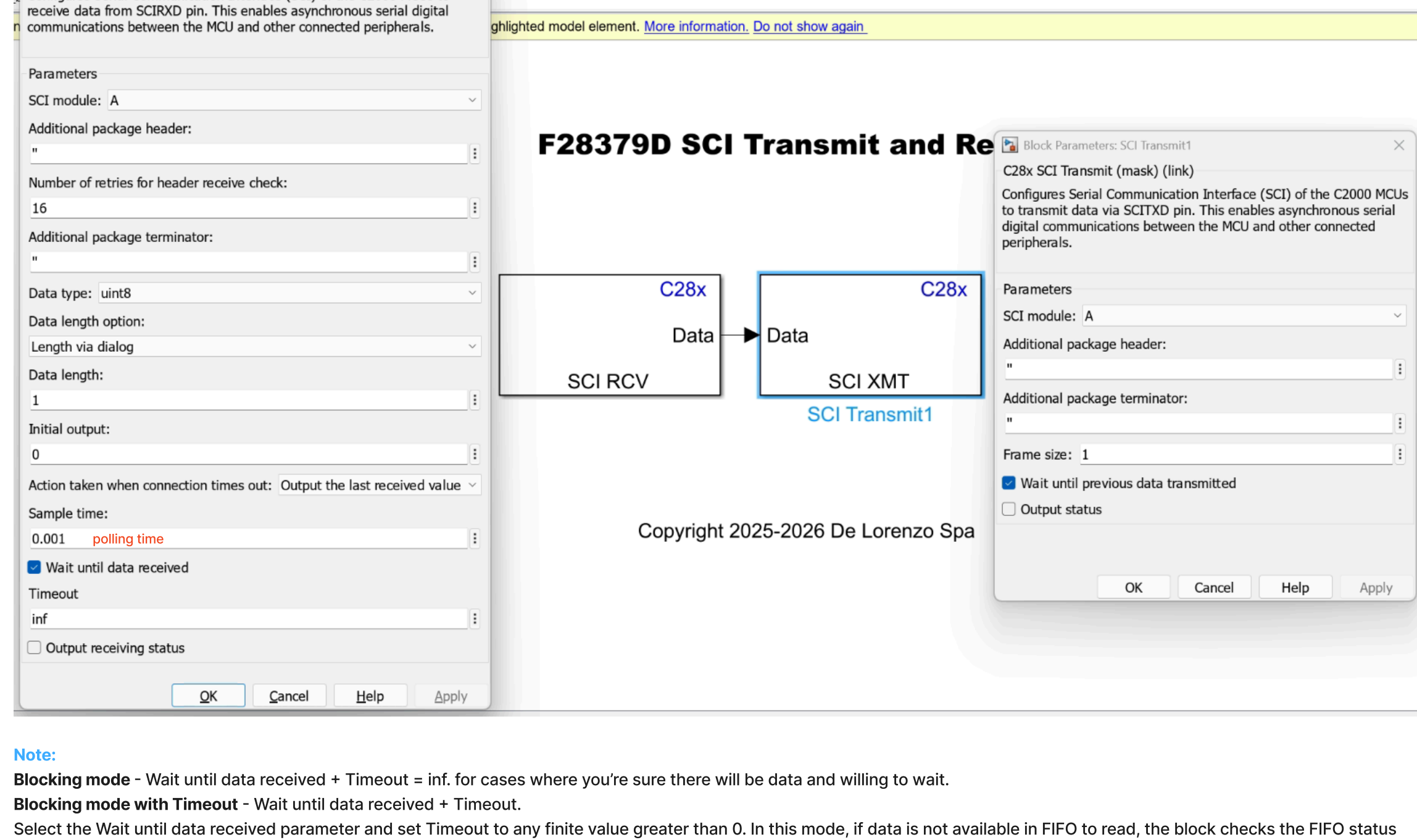
1. configuration



GPIO45/EM1A5	K18	GPIO44	C18
GPIO44/EM1A4	C19	GPIO43/SCIARX(->MCU)	C19
A/SCIRXDA/USB0DP	D19	GPIO42/SCIATX(<-MCU)	D19
A/SCITXDA/USB0DM	U17	GPIO41/I2CSCLB	U17



2. function blocks setting



Note:

Blocking mode - Wait until data received + Timeout = inf. for cases where you're sure there will be data and willing to wait.

Blocking mode with Timeout - Wait until data received + Timeout.

Select the Wait until data received parameter and set Timeout to any finite value greater than 0. In this mode, if data is not available in FIFO to read within that time, then the SCI Receive block outputs its status as timeout.

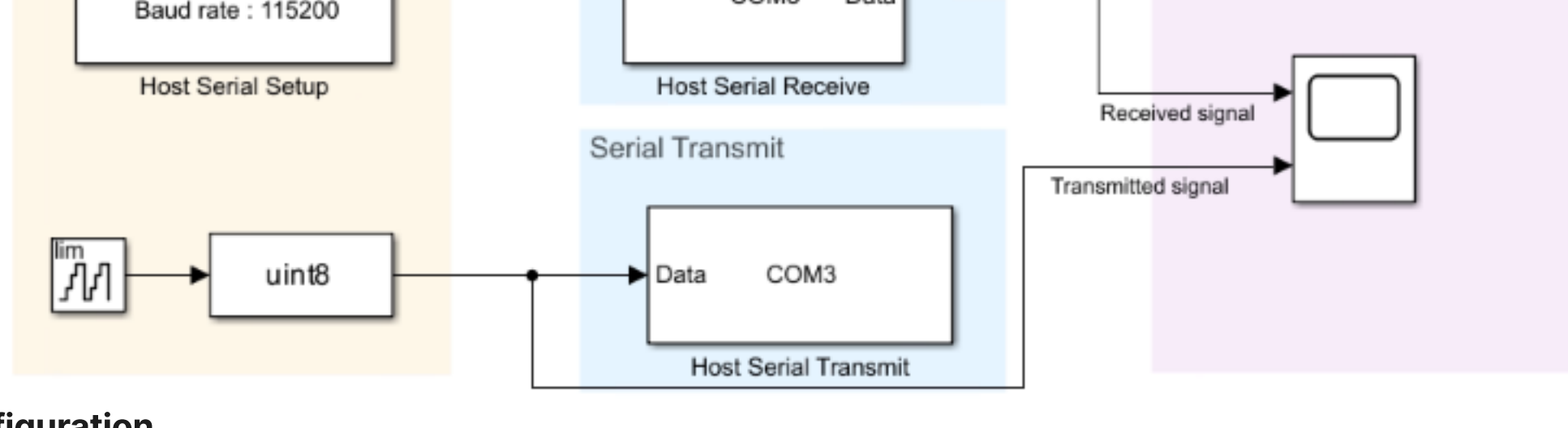
Non-Blocking mode - DO NOT wait until data received. for continuous, non-interrupted systems.

warning:

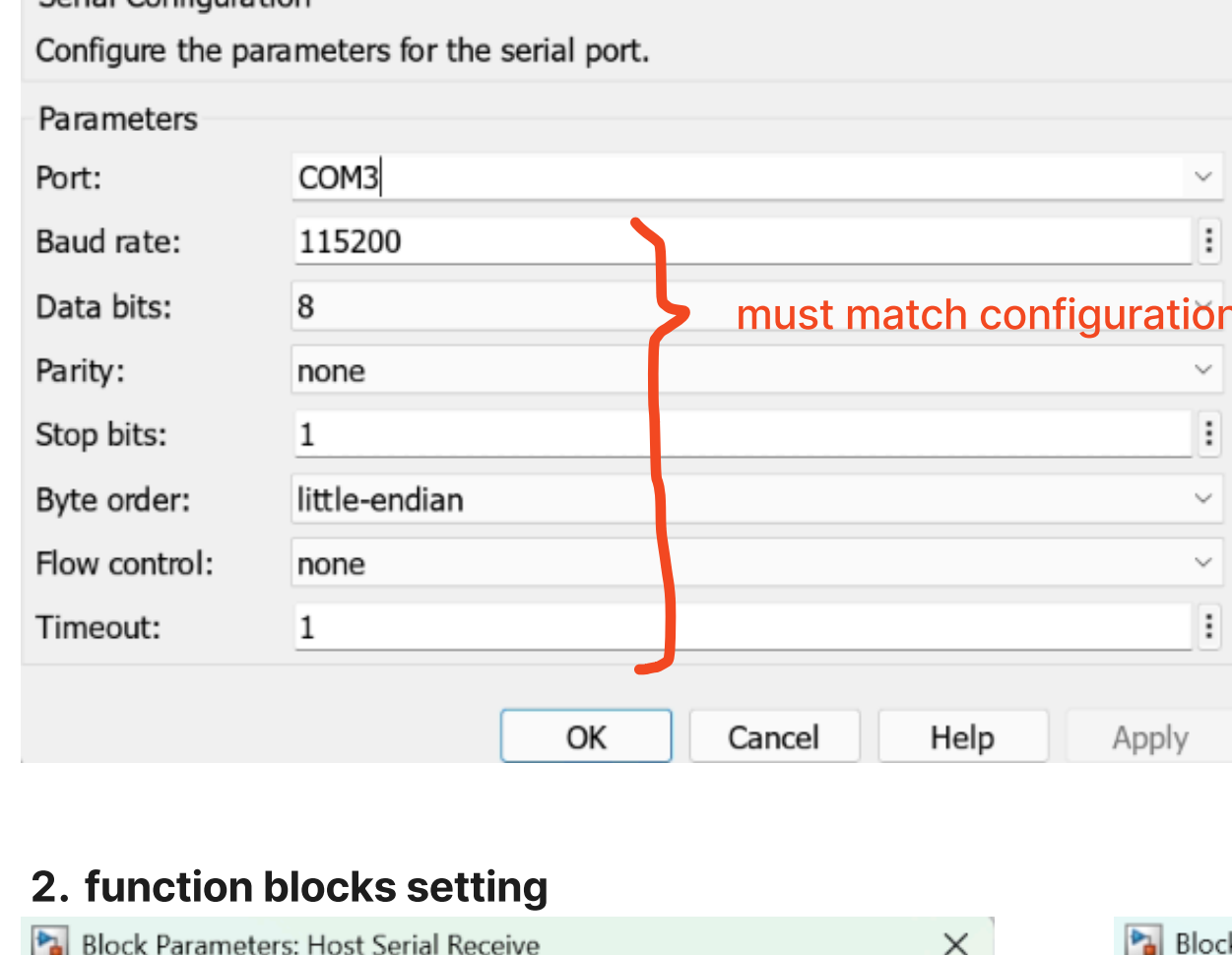
to receive data of length more than the FIFO length, use blocking modes to ensure data integrity.

3. Build, Deploy & Start target model

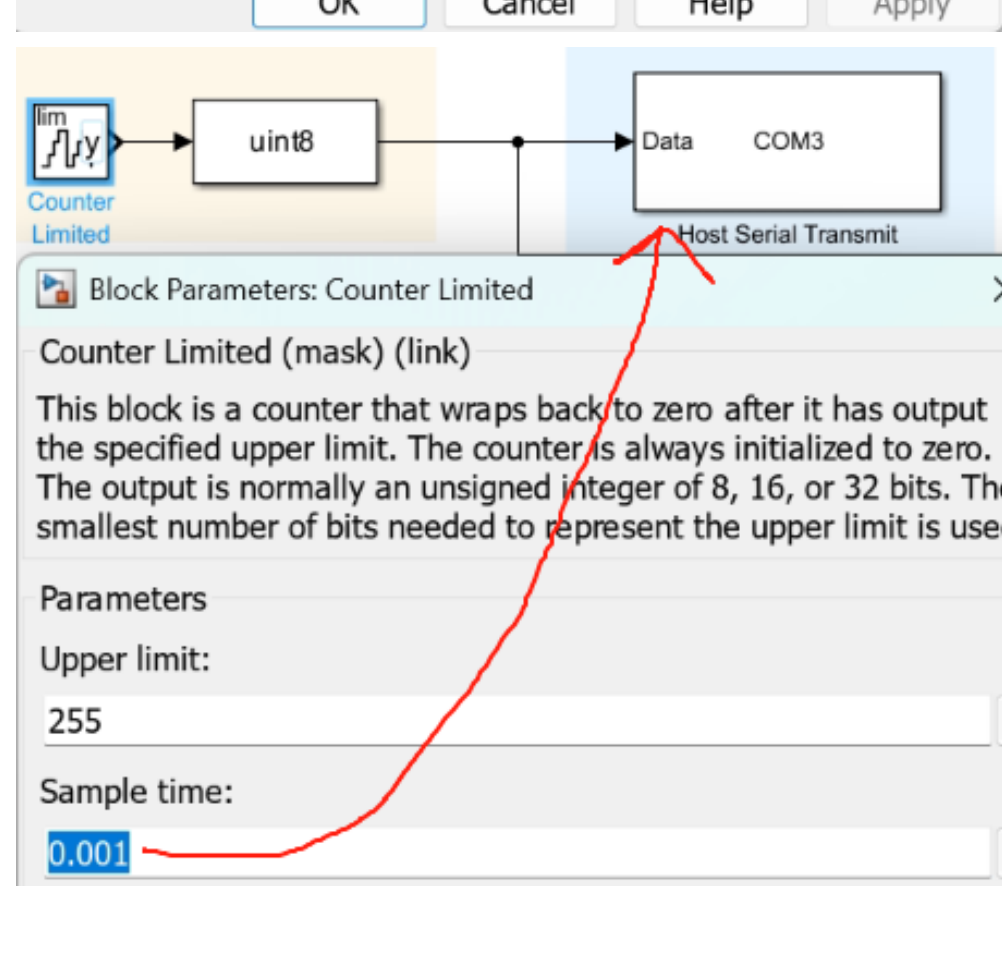
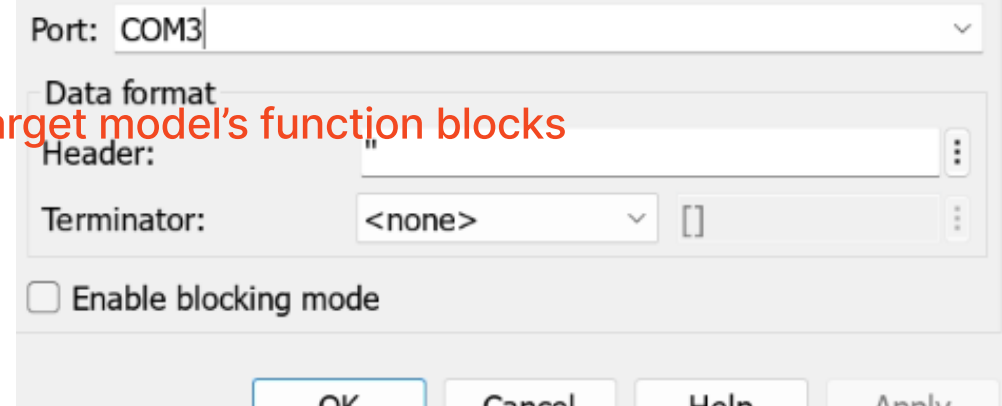
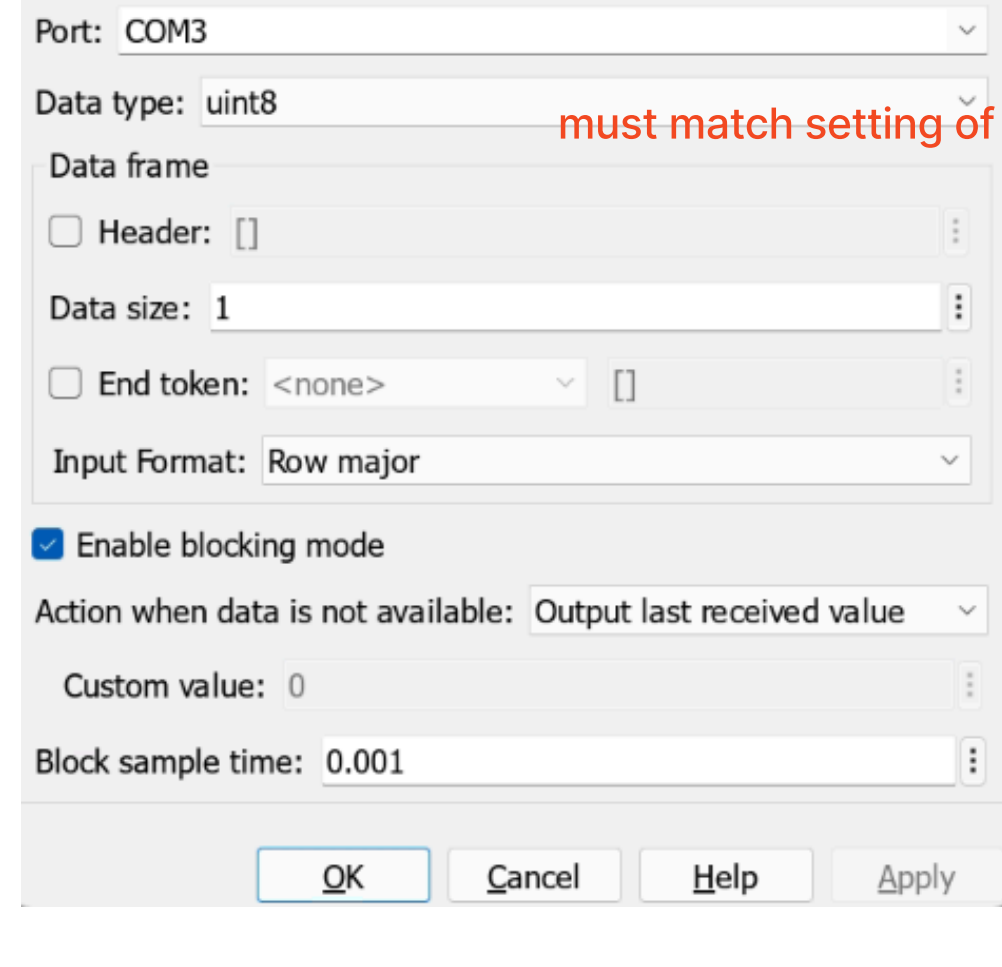
host model



1. configuration



2. function blocks setting



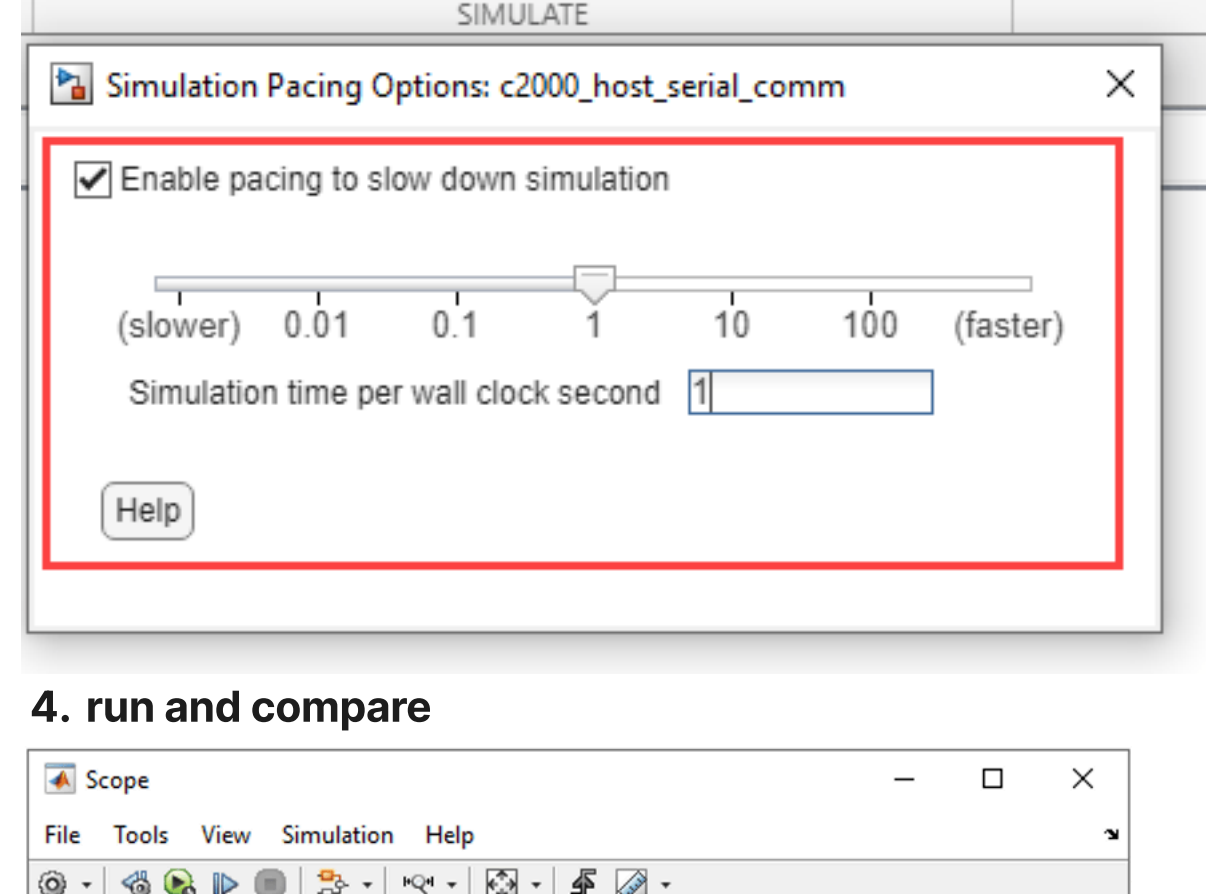
Note:

RX block sample time / data size = TX block sample time in another model.

In this case, target TX block is driven by target RX block, whose polling time is known, i.e. 0.001s. So 0.001(host RX)/1=0.001(target TX) stands.

host TX block is driven by a counter, whose sampling time is 0.001, i.e. 0.001(target RX)/1=0.001(host TX) also stands.

3. simulation run time setting



4. run and compare

