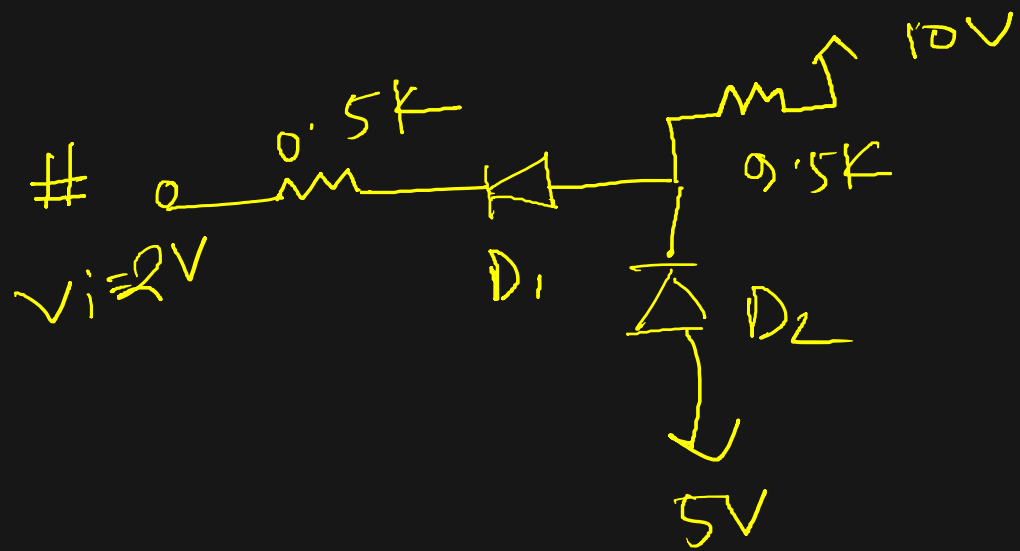


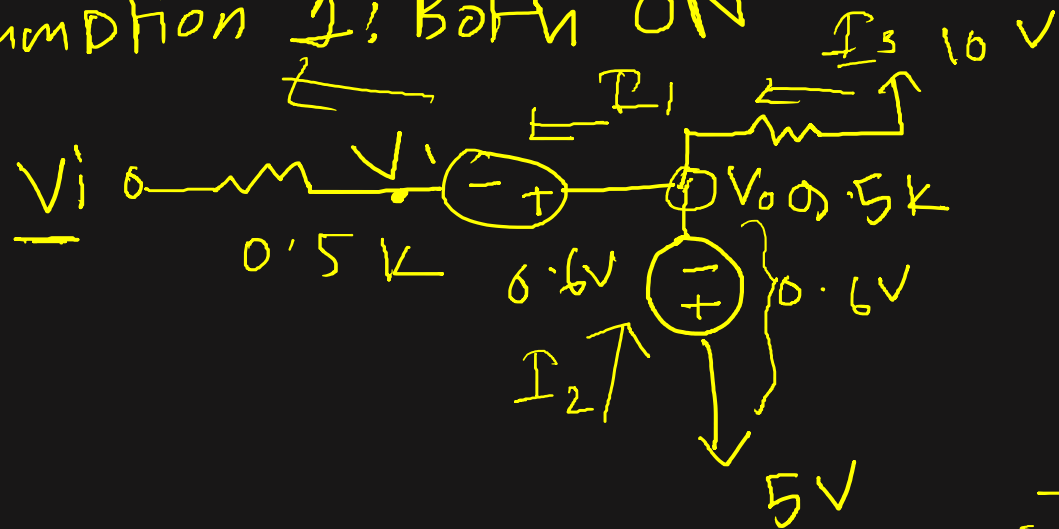
Lecture 8+9



Use CVD model, $V_{D0} = 0.6V$

i) $V_i = 2V$, ii) $V_i = 13V$

Assumption 1: Both ON



$$5 - V_0 = 0.6V$$

$$\Rightarrow V_0 = 4.4V$$

$$V_1 = V_0 - 0.6V$$

$$= 3.8V$$

$$I_3 = \frac{10 - V_0}{0.5} \text{ mA}$$

$$= 0.6 \text{ mA}$$

$$I_1 = \frac{V_1 - V_i}{0.5K}$$

$$= \frac{1.8}{0.5} \text{ mA}$$

$$= 3.6 \text{ mA}$$

KCL at v_0 : $I_1 = I_2 + I_3$

$$\Rightarrow I_2 = I_1 - I_3$$

$$= 3 \text{ mA}$$

I_1, I_2 both positive

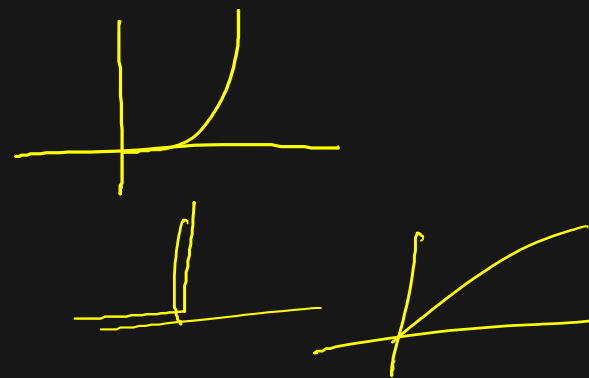
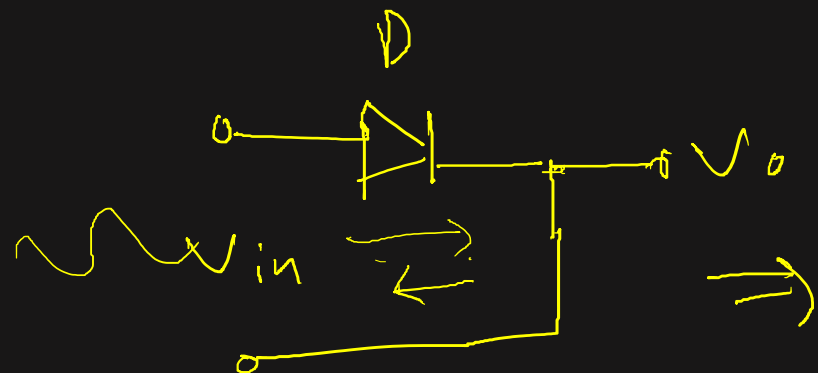
\therefore Assumption Correct

Practise: $V_i = 13 \text{ V}$





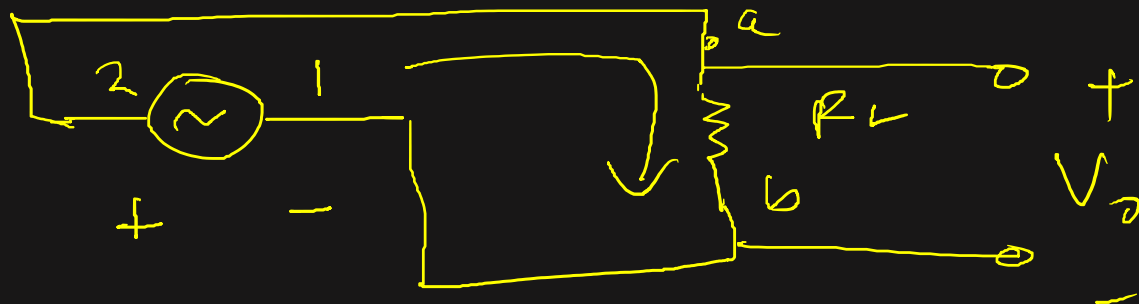
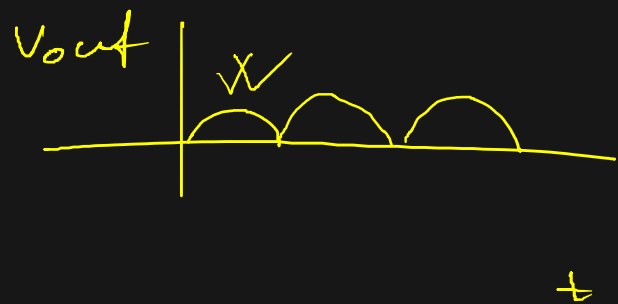
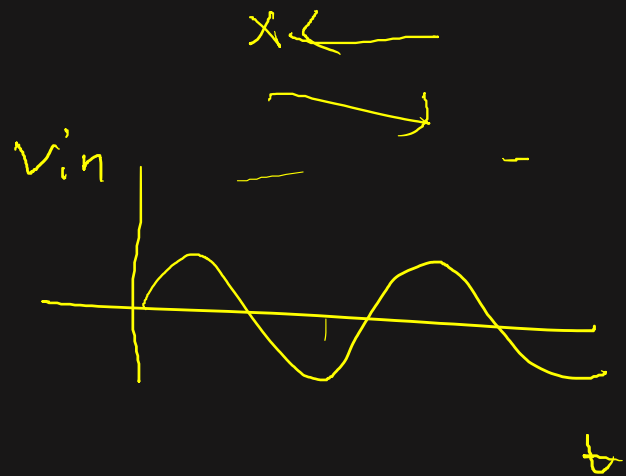
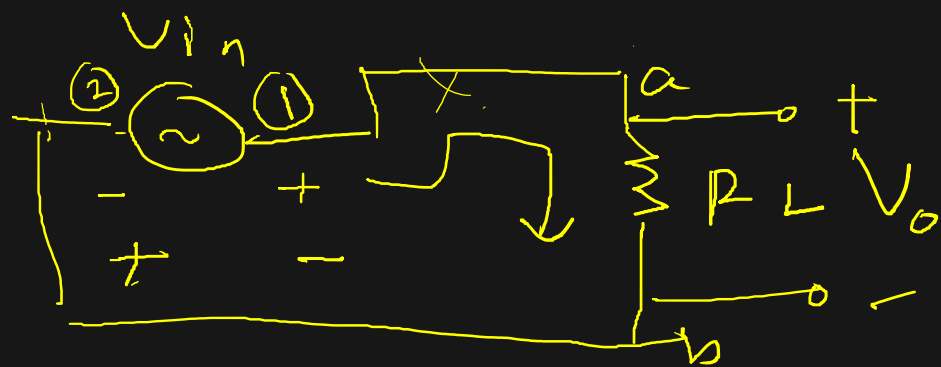
Rectifier

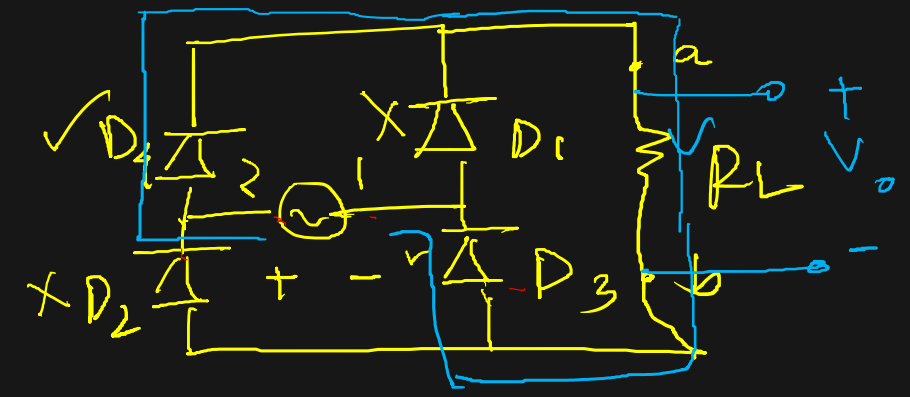
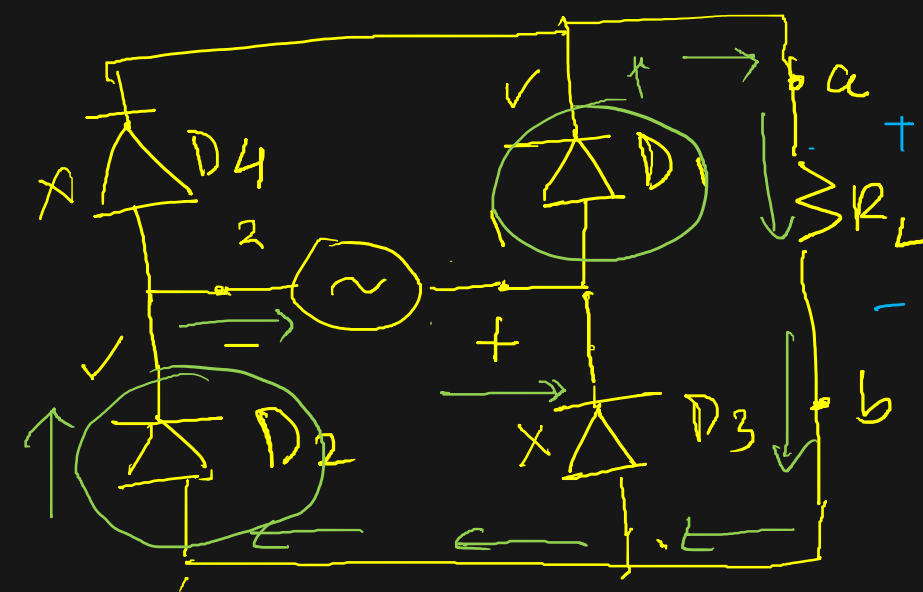
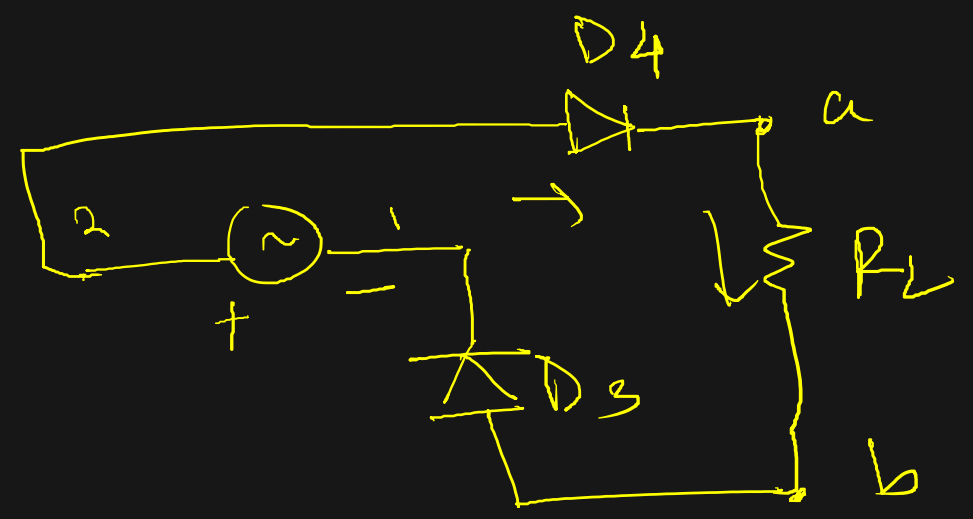
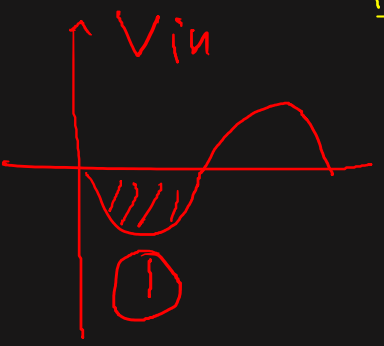
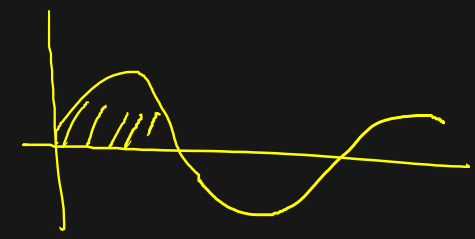
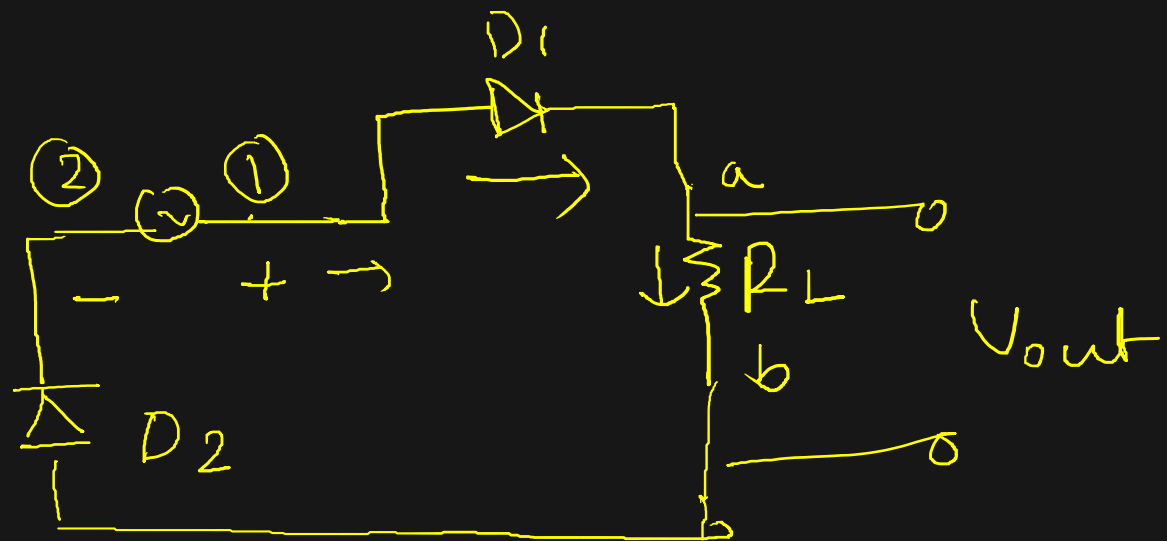


Three types

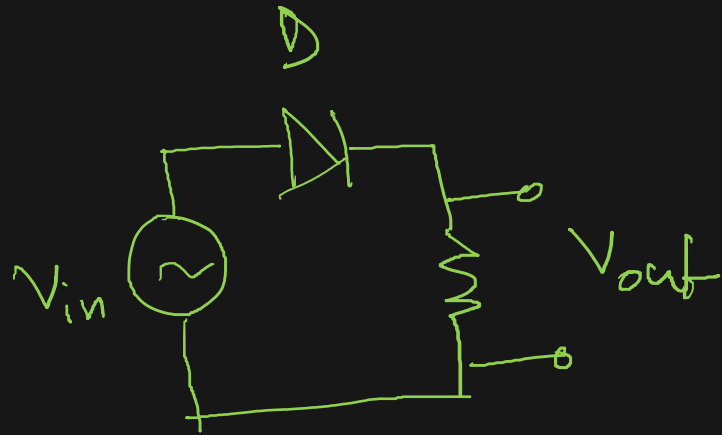
- 1) I-V curves \rightarrow Device / element (I_D vs V_D)
- 2) Transfer characteristic \rightarrow Circuit (out. V_S vs I_{in})
- 3) Waveform (V / I vs t)

#

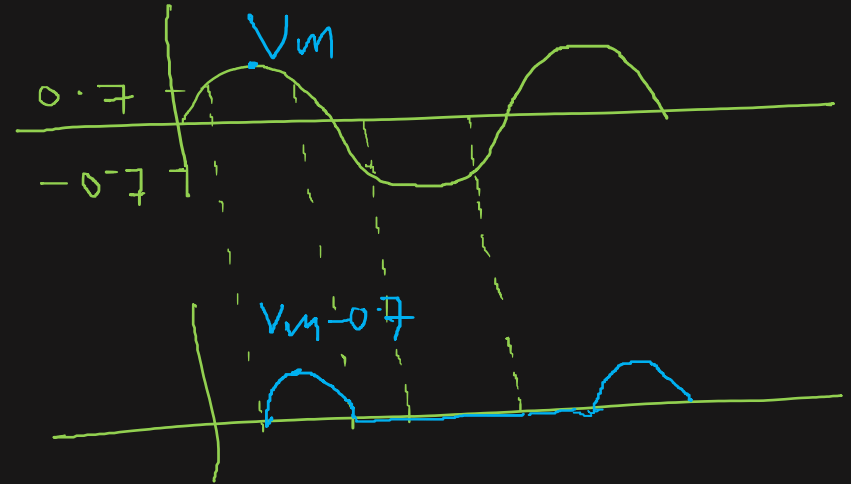




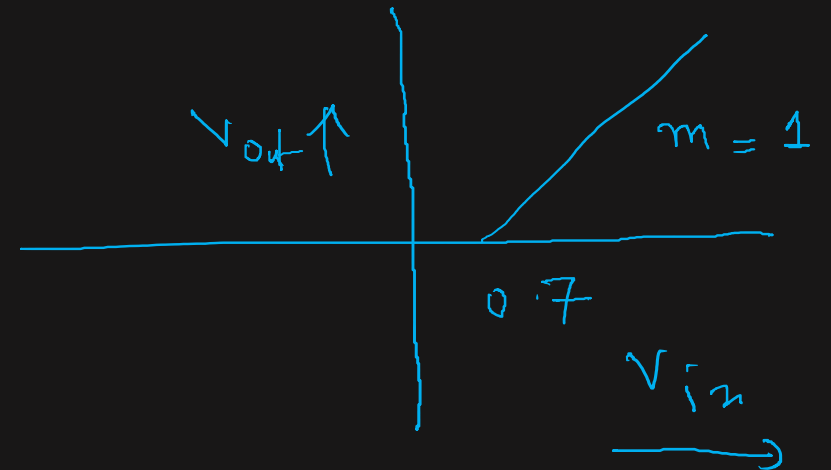
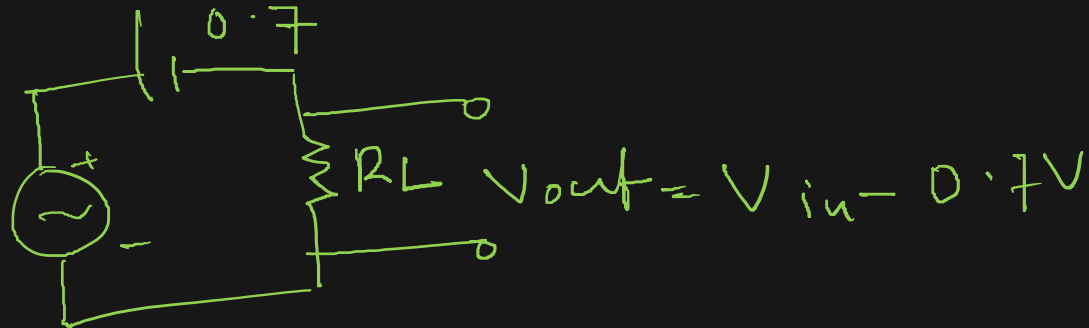
Half-wave Rectifier (Real Diode)



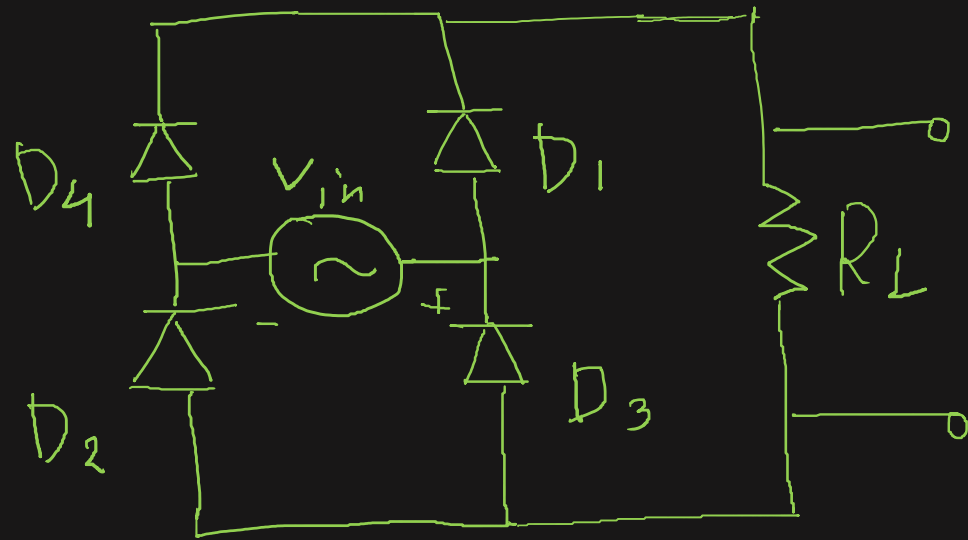
$$\begin{aligned} &\text{CVD} \\ &\underline{\underline{V_{D0} = 0.7V}} \end{aligned}$$



Positive Half cycle



Full Wave Rectifier



$$V_{out} = V_{in} - 0.7V - 0.7V$$

$$= V_{in} - 1.4V$$

