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Lecture 8 + 9SAT SUN MON TUE WED THU FRI  
      

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Diode non-ideality (Real diode)For ideal diode (review)

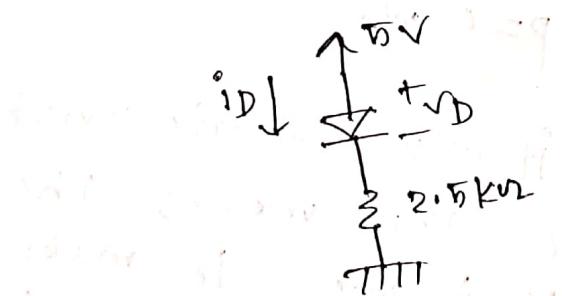
if ON, then act as short circuit

if OFF, then act as open circuit.

But in real life nothing is ideal.

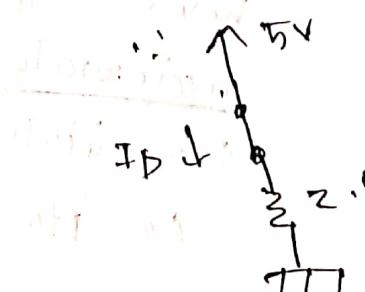
so lets discuss about some non-ideality of real diode has:

if ideal diode if then we have this circuit,



$\therefore$  if Diode is ON  
it will act as short circuit

$$\therefore I_D = 5V / 2.5k\Omega$$



$$\therefore I_D = \frac{5V}{2.5k\Omega} = 2mA$$

$$\therefore V_D = 0V$$

# Ques. & Answer

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Now what about the power consumed by this diode?

$$P = VI$$

[Power consumed by a device]

Forward voltage drop for rectifier diode

So for this circuit (diode)

$$P = V_D I_D$$

$$= 0.7 \times 2 \text{ mA}$$

$$= 0 \text{ W}$$

It's different. But this is odd because diode is a passive device so it must consume power so its not possible for diode to consume power that consumed for diode.

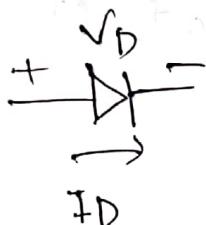
$$\text{power } P = 0 \text{ W}$$

As Again it also indicate that

But here  $V_D = 0$  that means the materials that use to make the diode has no resistance ( $R = 0$ ) so its also not possible.

Now let's consider these two issue.

(A) First the power case,



Now if we measure this  $V_D$  voltage and  $I_D$  current we can find the power consumed by this diode directly proportional to current  $I_D$ .

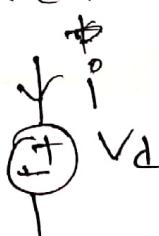
$$\boxed{P \propto I_D}$$

→ Not like resistance for resistors,

$$\begin{aligned} P &= i^2 R \\ \therefore P &\propto i^2 \end{aligned}$$

base about voltage source

Now for voltage source when voltage is constant then



$$P = V_d \cdot i$$

$$\boxed{P \propto i}$$

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so we can tell that when diode is conducting or it is in on mode, the power is proportional to current which is similar to that of a voltage source.

For silicon diode we get  $P = K I_D^2$

if we match diode and voltage source power consuming equation we will get,

when diode is ON, we can replace it with voltage source.

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so, Diode T<sub>silicon</sub> can only J

ON  $\rightarrow$  Will replace the diode  
with voltage source

(\*) Poffne off  $V_D = 0.7 V$

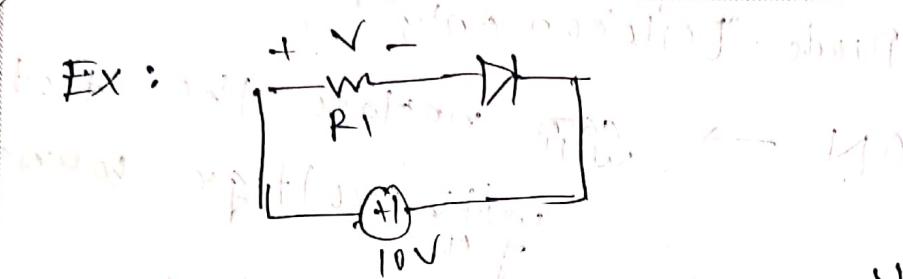
Note that positive side of voltage  
source ~~will~~ should be connected  
to Anode and Negative side to  
cathod.

when OFF  $\rightarrow$  will replace the  
diode with  
open circuit.

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What will be the voltage across  
resistor  $R_1$ ? [Here diode will consume  
approx. 0.7 volt]   
 $V = 10 - 0.7 = 9.3 \text{ V}$  [If diode  
is ON]

Now the material case is

If the diode is off, then  $V = 10 \text{ V}$

• when diode is ON,

can replace it with



voltage source  
series with  
resistor

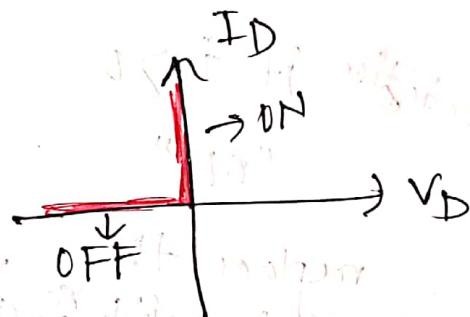
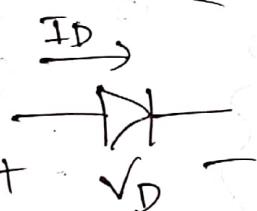
Hence this  $V$  value will be

very small. like 10mV, 12mV or 2mV

Therefore these are two non-idealities of real diode has.

### Review

For Ideal diode model



if  $I_D > 0$  → condition  
this is called ON

$$\text{then } V_D = 0$$

this is called state equation

$$I_D = 0$$

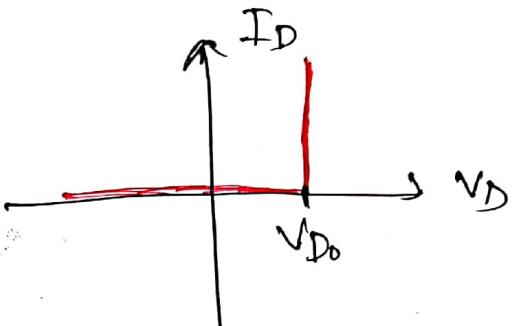
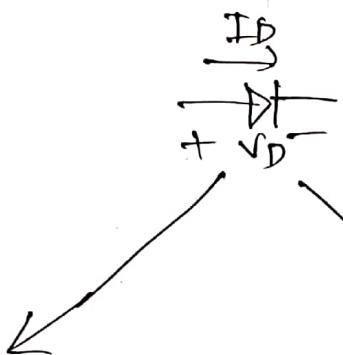
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~~If  $V_D = 0$  is called cut-in voltage.~~  
 the voltage need to ~~is required~~ to turn on the diode.

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Now

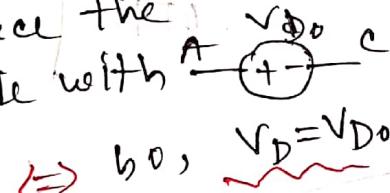
constant voltage drop model (CVD model)



condition

if  $I_D > 0$   
 "ON"

replace the diode with



if  $V_D < V_{D0}$   $\Rightarrow$  condition

"OFF"

so  $I_D = 0$

state equation.

state equation

For silicon (Si)

diode it's about

$$V_{D0} = 0.7 \text{ V}$$

for Germanium

it's about 0.2 V

V

But mostly used Si

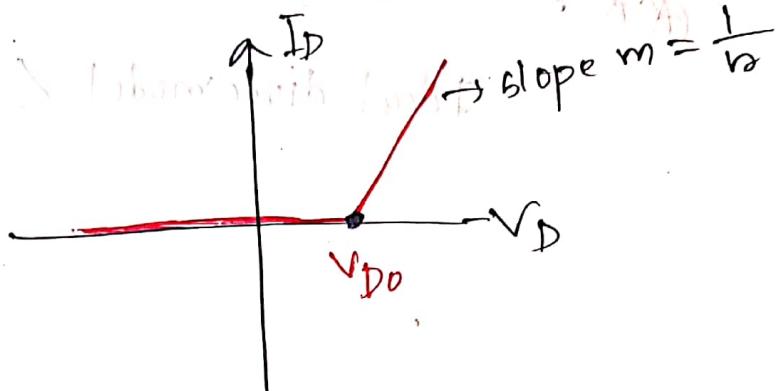
$$\text{so } V_{D0} = 0.7 \text{ V}$$

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## Now the voltage source + resistance model ( $IVD + b$ )



if  $I_D > 0$ , ON  $\leftrightarrow$  condition



then if  $V_D < V_{DD}$   
 $V_D = V_{DD} + iR$   $\leftrightarrow$  state equation

$$V_D = V_{DD} + iR \quad \text{"OFF"}$$

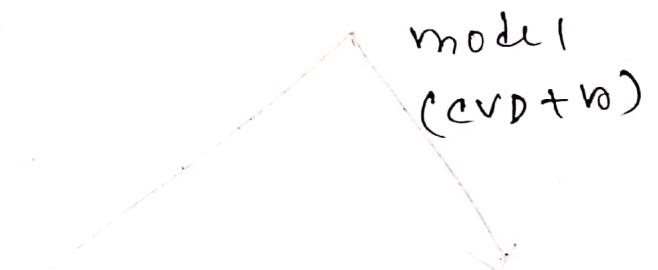
if  $V_D > V_{DD}$   $\leftrightarrow$  effective  $I_D = 0$

and in this case  $V_D = V_{DD}$   
at the cut-off voltage

## Accuracy of the model (in result accuracy)

★ ★

Ideal diode model < CVD model < voltage source + resistor



Efficient & Accurate

# Now where to use which model?

Ans: depends on context of the question.

But mostly we use in this course CVD model.

Handly we CVD + v0 or Ideal model in this course

Ex:

Question: Find  $V_o$  and  $i_o$  using:

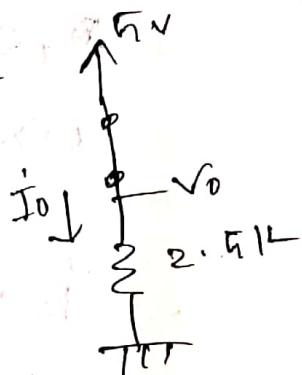
① ideal diode model

② CVD model ( $V_D = 0.7V$ )

③ CVD +  $V_o$  model ( $V_D = 0.7V$ ,  $V_o = 10mV$ )

④ Ideal model

Divide is ON since  $i_{D0} = I_D > 0$

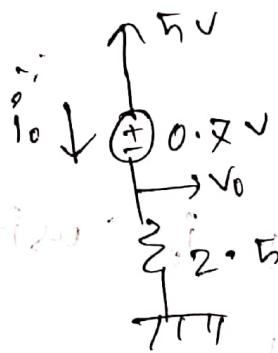


$$\therefore V_o = 5V$$

$$i_o = \frac{5 - 0}{2.5} = 2mA$$

## ② CVD model

Diode ON, since  $I_0 = I_D > 0$



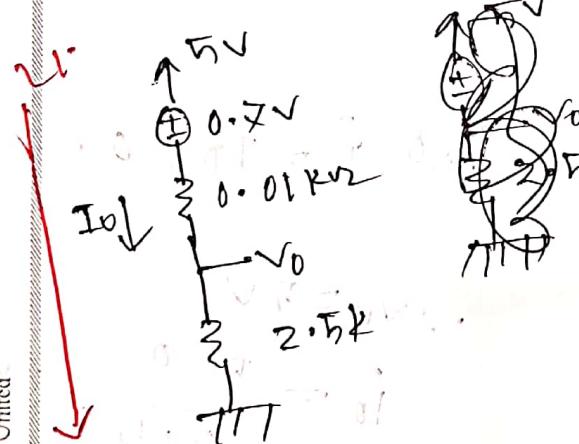
$$\therefore 5 - V_0 = 0.7$$

$$\Rightarrow V_0 = 5 - 0.7 = 4.3 \text{ V}$$

$$\therefore I_D = \frac{V_0 + 0.7}{2.5} = \frac{4.3}{2.5} = 1.72 \text{ mA}$$

## ③ PVDF model

ON, since  $I_0 = I_D > 0$



~~1.72mA load along L1~~

$$0.7 + I_0 \times 0.01 + I_0 \times 2.5 = 5 - 0$$

$$I_0 = 1.713 \text{ mA}$$

$$\text{Now } V_0 - 0 = 2.5 \times I_0$$

$$\therefore V_0 = 2.5 \times 1.713 = 4.288 \text{ V}$$

## Real diode

made with semiconductor (Si, Ge)

intrinsic  
(pure)

extrinsic  
(impure)

mixed A<sub>x</sub>B  
in a controlled  
manner

P  
type

most of charge  
carriers  
holes

[minor  
negative  
carriers]

n  
type

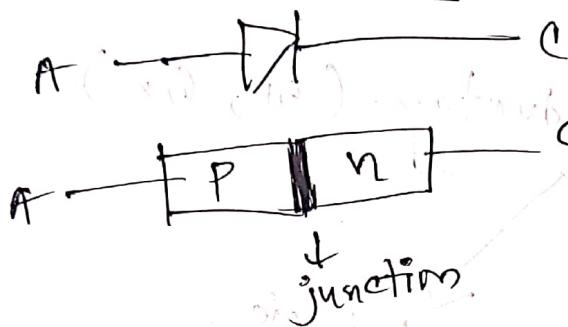
most of  
charge  
carriers are  
electrons.

[minor positive  
carriers]

# Semiconductors  
in between  
conductors  
and insulators

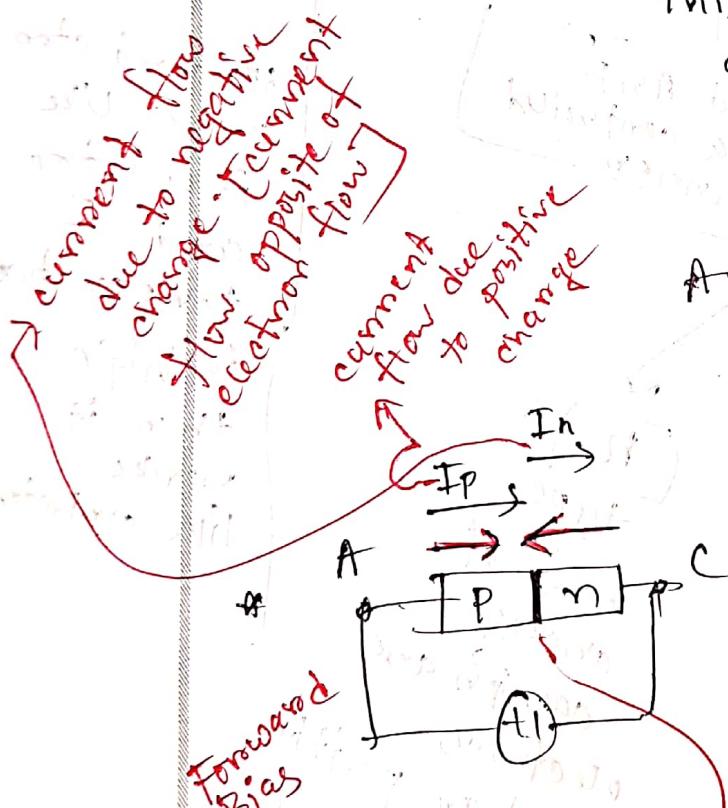
# In lowers  
temperature  
semiconductor  
works like  
insulator  
and if  
temperature  
is high  
then it  
works  
like conductor

## P-N junction diode

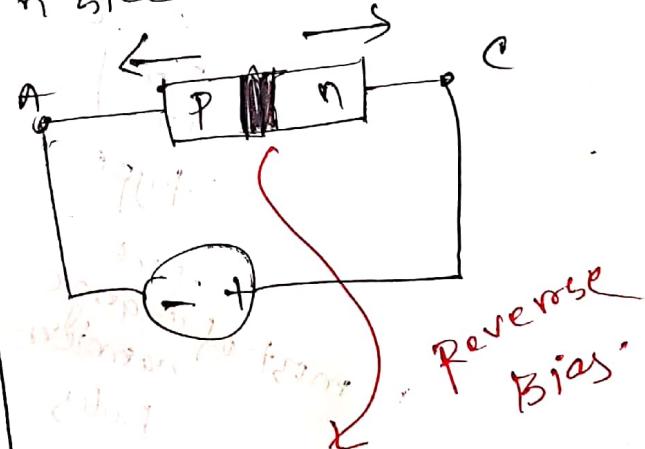


this junction does not allow any positive charge from P side to N side.

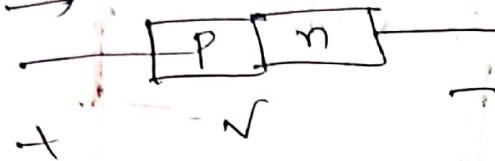
And doesn't allow any negative charges from N side to P side.



positive charge repeat.  
By anode side also negative  
electron also repeated by  
cathode side. So current  
will flow from A to C.  
Junction gets thinner.



attraction A and P  
C and N  
So junction get  
wider. So current  
can't cross it. So current  
will zero

Real diode equation: $I_D$ 

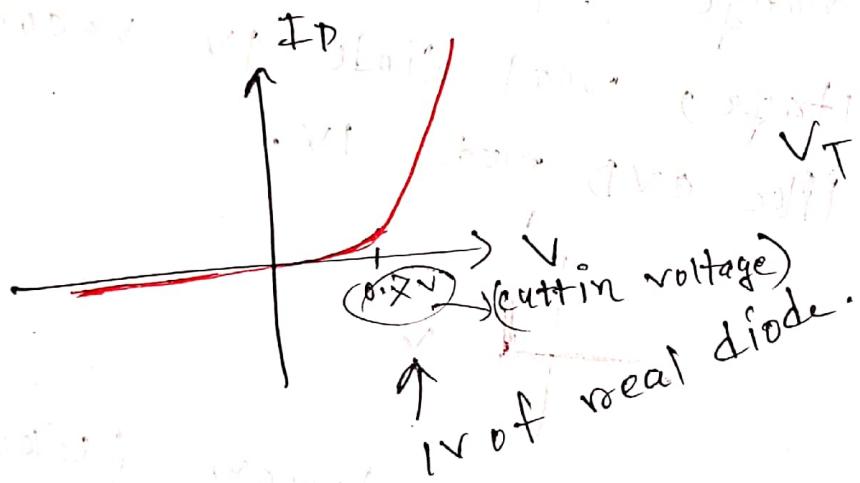
$$I_D = I_S (e^{\frac{V}{V_T}} - 1)$$

$I_S$  = reverse saturation current  $[10^{-12} \text{ to } 10^{10} \text{ Ampere}]$

$V_T$  = thermal voltage

$$= 0.025 \text{ V}$$

& typically



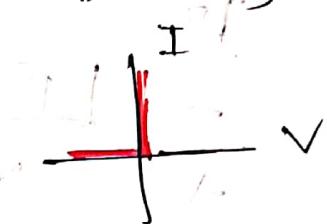
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when higher voltage range, [-50V to +50V]

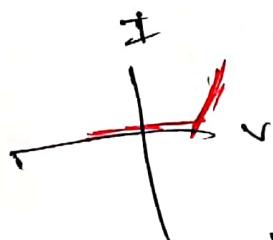
this IV of real diode becomes like ideal diode model



when operating voltage is lower in range (-15V to 15V) than forward voltage, real diode IV becomes like CVD model IV.

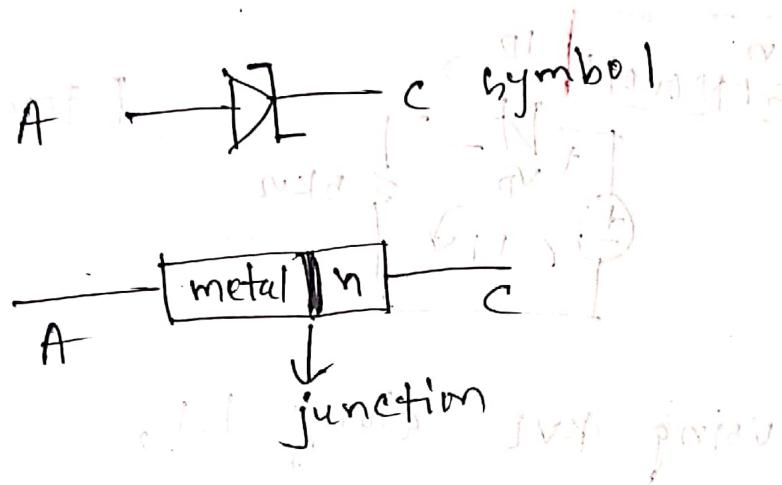


when O.V. is much lower (like -2V to 2V) than it becomes like CVD + model

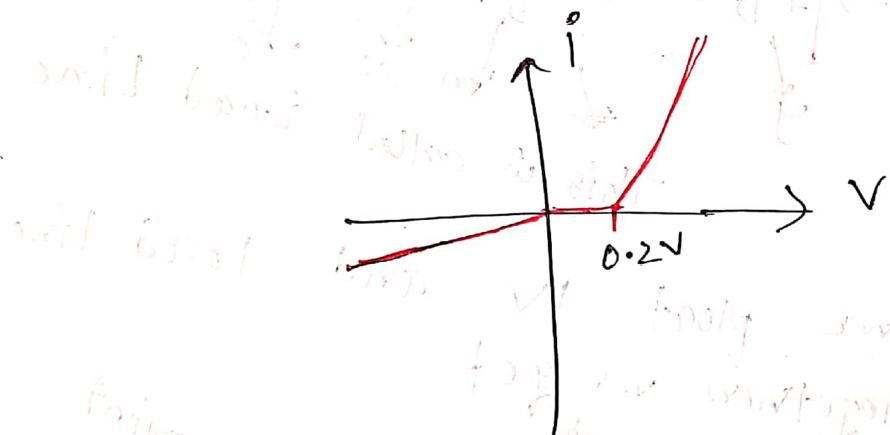


if the voltage too much lower than these on that time for getting accurate result we can use real diode IV curve.

## # Schottky diode

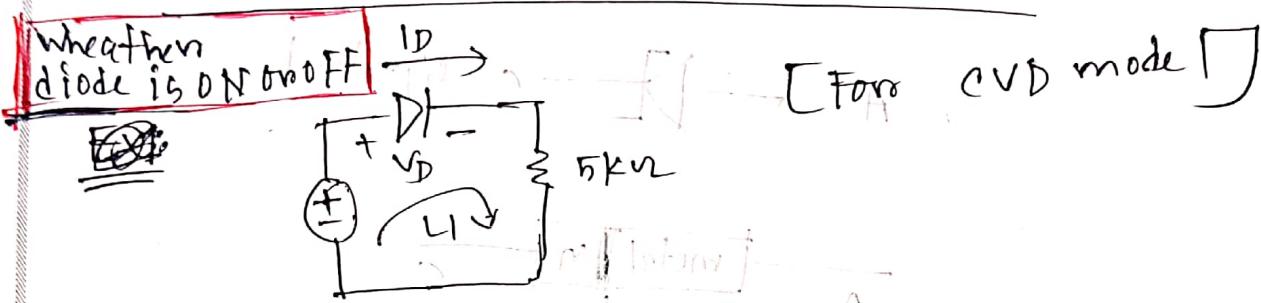


Characteristics of Schottky diode



# Solving diode circuit

(\*)



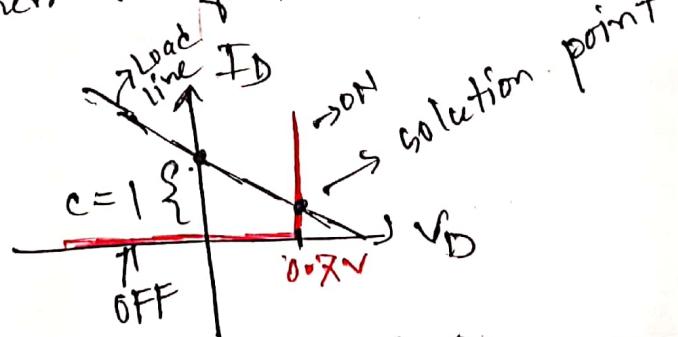
Using KVL along  $L_1$ ,

$$V = V_D + 5I_D \quad \text{[L: } y = mx + c\text{]}$$

$$\Rightarrow I_D = -\frac{1}{5}V_D + 1 \quad \begin{matrix} y \\ m \\ c \end{matrix}$$

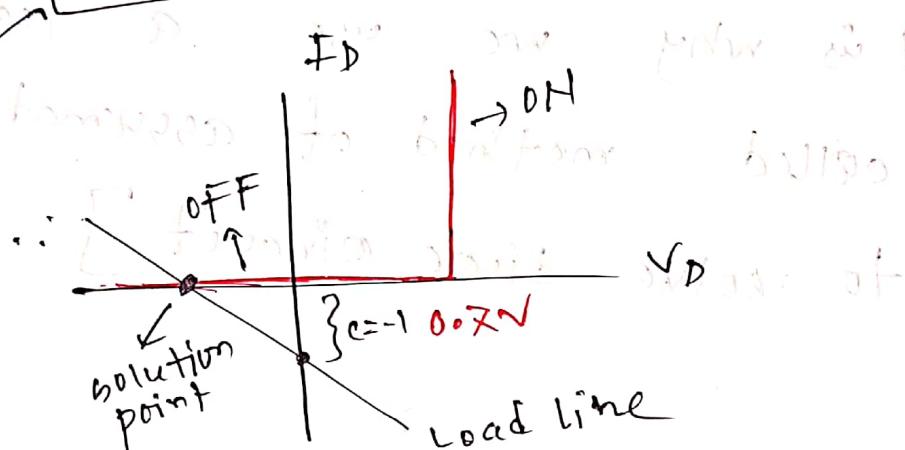
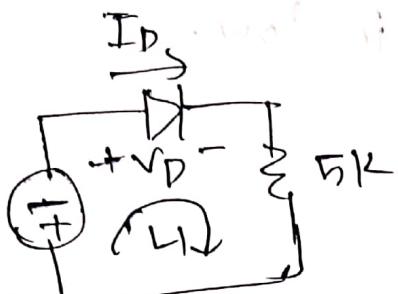
This is called load line

∴ If we plot IV and load line together we get



And since solution point is on segment of diode we can easily assume diode is ON.

Now let's solve the circuit.



→ equation using KVL

$$-V = V_D + 5 I_D$$

$$\therefore I_D = -\frac{1}{5} V_D - 1 \quad [ \text{load line equation} ]$$

$$y = mx + c$$

$\therefore$  Now the solution point is on the off segment. So <sup>we can assume</sup> diode is OFF.

But this approach only works when the number is low.

(\*) That is why we use a technique called method of assumed state to solve diode circuit

and left for next page

Using trial and error method



Diode connected

This will result in forced oscillations with voltage across the diode changing from 0 to 12V at different times depending on the time constant.

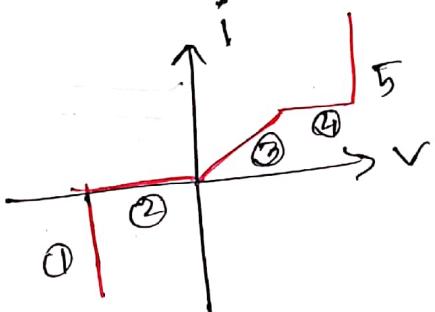
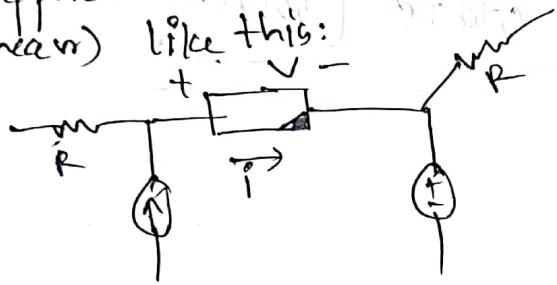
## [ Method of Assumed state ]

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suppose we have a circuit and IV (piece wise linear) like this:



↓  
For solving this circuit we need to  
- know  $i$  and  $v$  across the nonlinear  
device. we can solve it via piece wise  
linear approximation using this IV but that  
is troublesome and lengthy. so we use  
method of assumed state to solve circuit  
with ~~non~~ non-linear device.

Steps :

1. Assume states of the device

For example: for diode assume each diode is off or ON, or both are ON or both are off, one is one another is OFF etc.

2. Solve the linear circuit and find  $V_D$ ,  $i_D$

$$V_D, i_D$$

For example: based on assumption of ON or off of diode we replace it with linear device and solve the circuit to find  $V_D$  and  $i_D$ .

( $V_D$  voltage across diode,  $i_D$  current across diode)

3. Verify

$$\begin{cases} i_D > 0 \\ V_D \leq 0 \end{cases}$$

$$\begin{cases} i_D < 0 \\ V_D \geq V_{D0} \end{cases}$$

ideal model

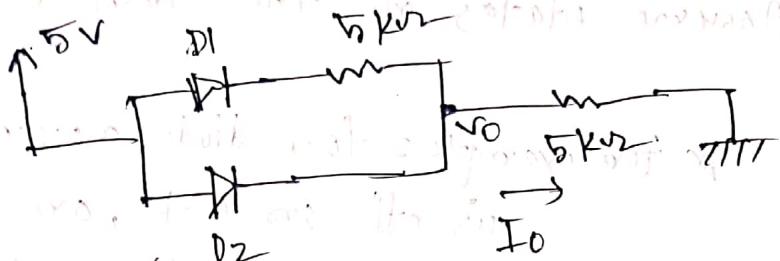
cVD model

For example: if we assumed diode is ON but get  $i_D < 0$ , technically diode is OFF that is why our assumption is wrong.

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Ex 4.

\* To find  $V_0$  and  $I_0$ : use CVD model with

$$V_{D1} = 0.7 \text{ V}$$

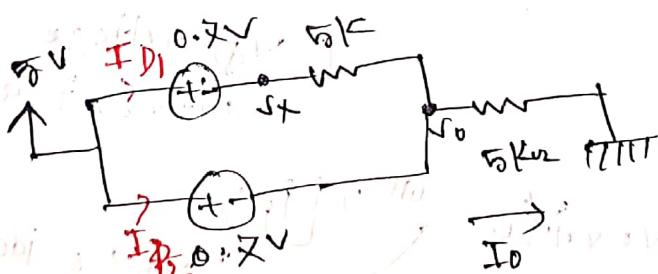
Solve:

$\Rightarrow$  Assumption - I

$$D_1 = \text{ON}$$

$$D_2 = \text{OFF}$$

if both diodes would be ON



$$0.7 = 5 - 0.7 \times V_0$$

$$\therefore V_0 = 5 - 0.7 = 4.3 \text{ V}$$

$$\text{and } I_0 = \frac{V_0 - 0}{5} = \frac{4.3}{5} = 0.86 \text{ mA}$$

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again)

$$0 \cdot x = 5 - \sqrt{x}$$

$$\therefore \sqrt{x} = 5 - 0 \cdot x = 4.3^V$$

Now  $I_{D1} = \frac{\sqrt{x} - \sqrt{0}}{5} = \frac{4.3 - 4.3}{5} = 0$

Now ~~KCL~~ or using KCL

$$\Rightarrow I_{D1} + I_{D2} = I_0$$

$$I_{D2} = I_0 \cancel{=} \\ = 0.86 \text{ mA}$$

Now reiteration.

Hence assumption was  $D1 = D2 = ON$

but  ~~$I_{D1} = 0$~~  but for  $D1$  in  $ON$  mode we need

$I_{D1} > 0$

so assumption is not correct.

Then  $I_{D1} = I_{D2} = 0.43$ .

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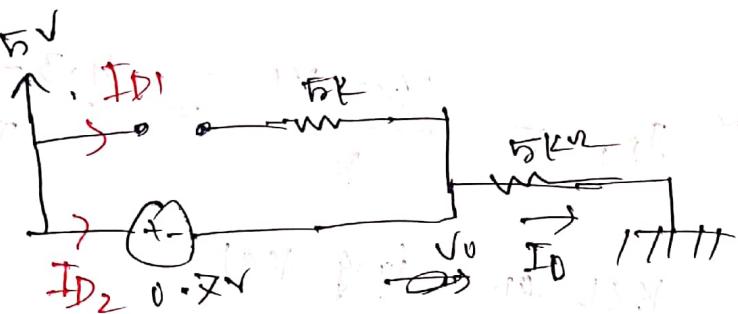
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Assumption - 2

$$D_1 = \text{OFF}$$

$$D_2 = \text{ON}$$



$$\therefore I_{D1} = 0 \text{ mA}$$

$$\therefore 0.2 = 5 - V_0$$

$$\therefore V_0 = 4.3V$$

$$\therefore I_o = \frac{V_0 - 0}{5k\Omega} = \frac{4.3}{5} = 0.86 \text{ mA}$$

using KCL:

$$\therefore I_o = I_{D1} + I_{D2}$$

$$\therefore I_o = 0 + I_{D2}$$

$$\therefore I_{D2} = I_o = 0.86 \text{ mA}$$

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verification

For diode 2.0 & D<sub>2</sub>, ~~we get~~  $I_{D2} > 0$

$I_{D2} = 0.86 \text{ mA}$  ~~> 0~~  $\therefore I_{D2} > 0$   
 i.e. ON b0 for D<sub>2</sub> assumption  
 correct.

For  $\not D_1$ ,

$$\sqrt{A} = 5\sqrt{2}$$

$$\sqrt{C} = \sqrt{0} = 4.3V$$

$$\therefore \sqrt{B} = \sqrt{A} - \sqrt{C} = 0.7V$$

$$\text{and } \sqrt{D_0} = 0.7V$$

$$\therefore \sqrt{B} \leq \sqrt{D_0}$$

$$\therefore D_1 = OFF$$

$\therefore$  b0 For D<sub>1</sub> assumption is  
 correct.

$\therefore$  we can say ~~because~~  $D_1 = OFF$

$D_2 = ON$  assumption is  
 correct.

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$$\therefore I_0 = 0.66 \text{ mA}$$

$$V_D = 4.3 \text{ V}$$

(Ans)

Widely used in ICs.

Junction field effect transistors.

Field effect transistors.

Metal oxide semiconductor field effect transistors.

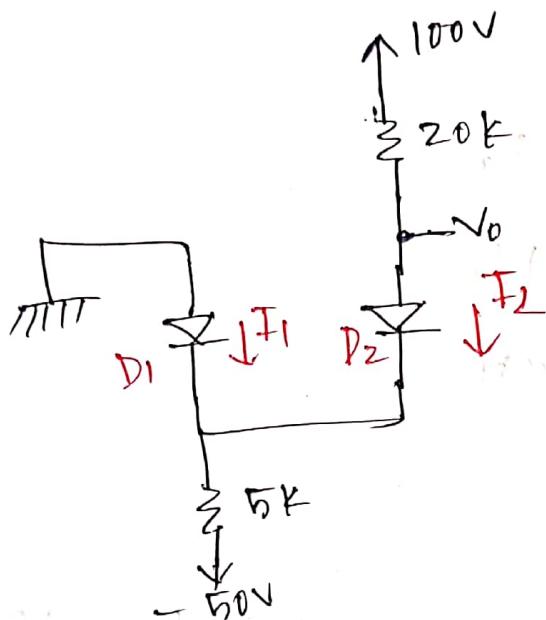
Insulated gate field effect transistors.

Metal insulated gate field effect transistors.

Metal oxide insulated gate field effect transistors.

Metal oxide semiconductor insulated gate field effect transistors.

Metal oxide insulated gate field effect transistors.

Ex 28

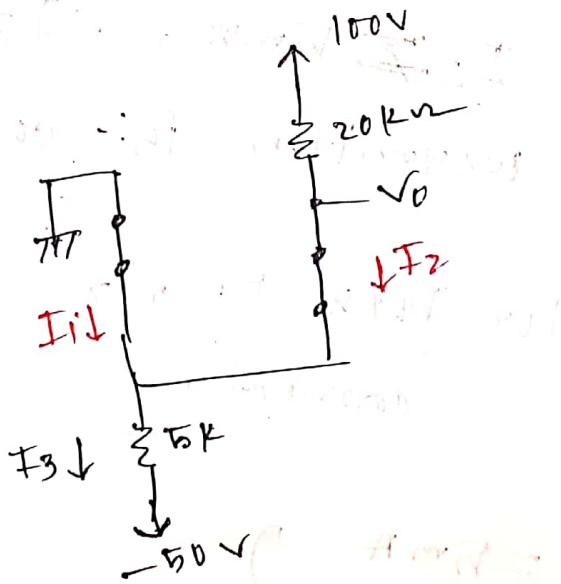
(Ans given)

Find  $V_o, I_1, I_2$   
use Ideal diode model.

Solve %Assumption 1

$$D_1 = ON$$

$$D_2 = ON$$



$$\therefore V_o = 0V$$

$$I_2 = \frac{100 - V_o}{20} = \frac{100 - 0}{20} = 5 \text{ mA}$$

$$I_3 = \frac{0 - (-50)}{5} = \frac{50}{5} = 10 \text{ mA}$$

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Using KCL

$$I_3 = I_1 + I_2$$

$$\Rightarrow 10 = I_1 + 5$$

$$\therefore I_1 = 5 \text{ mA}$$

Verification

For D<sub>1</sub>,  $I_1 \geq 5 \text{ mA} \because I_1 > 0$ , so ON  
assumption is correct

For D<sub>2</sub>,  $I_2 \leq 5 \text{ mA}, \because I_2 > 0$  off: ON  
assumption is correct.

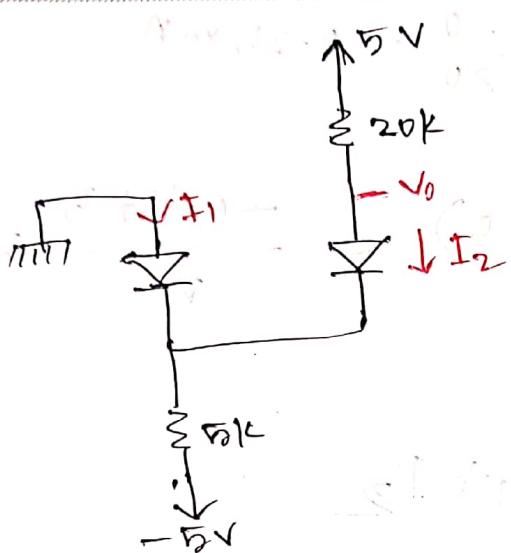
For both D<sub>1</sub>, D<sub>2</sub> assumption is correct.

$$\begin{aligned} \therefore I_1 &= 5 \text{ mA} \\ I_2 &= 5 \text{ mA} \\ V_0 &= 0 \text{ V} \end{aligned} \quad \left. \begin{array}{l} \text{Ans} \\ \text{Left} \end{array} \right\}$$

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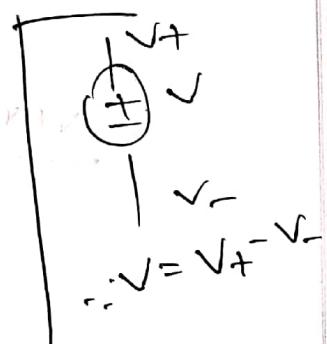
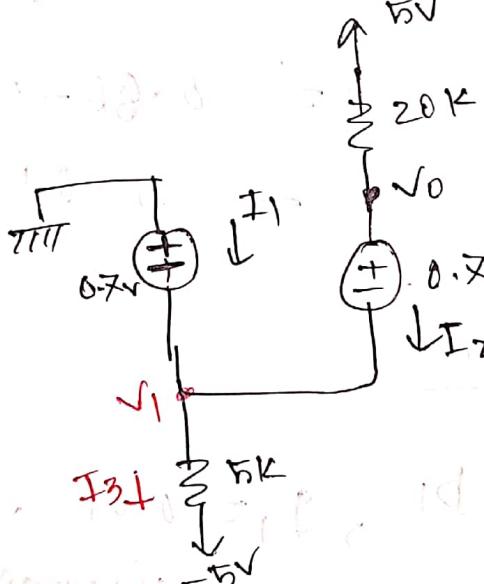
Ex 2:

# Find  $I_1$ ,  $I_2$  and  $V_o$   
use CVD  $[V_{D_s} = 0.7V]$

Solve:Assumption 1:

$$D_1 = ON$$

$$D_2 = OFF$$

Circuit of CVD

$$\text{Now, } V_o = V_1 = 0.7V$$

$$\therefore V_1 = -0.7V \quad [V_o < 0V]$$

for Zener's condition

$$\text{now, } V_o - V_1 = 0.7V$$

$$\therefore V_o = 0.7V + V_1$$

$$\text{But, } V_o = 0.7V + (-0.7) = 0V$$

Conclusion

Sub.: \_\_\_\_\_

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$$I_2 = \frac{5-0}{20} = \frac{5-0}{20} = 0.25 \text{ mA}$$

$$\text{Using KVL, } I_3 = \frac{V_1 - (-5)}{5} = \frac{-0.8 + 5}{5} = 0.86 \text{ mA}$$

KCL

$$I_3 = I_1 + I_2$$

$$\Rightarrow I_1 = I_3 - I_2$$

$$I_1 = 0.86 - 0.25$$

$$= 0.61 \text{ mA}$$

Verification

$$\text{For D}_1, I_1 = 0.61 \therefore I_1 > 0 \therefore \text{ON}$$

$\therefore$  assumption correct

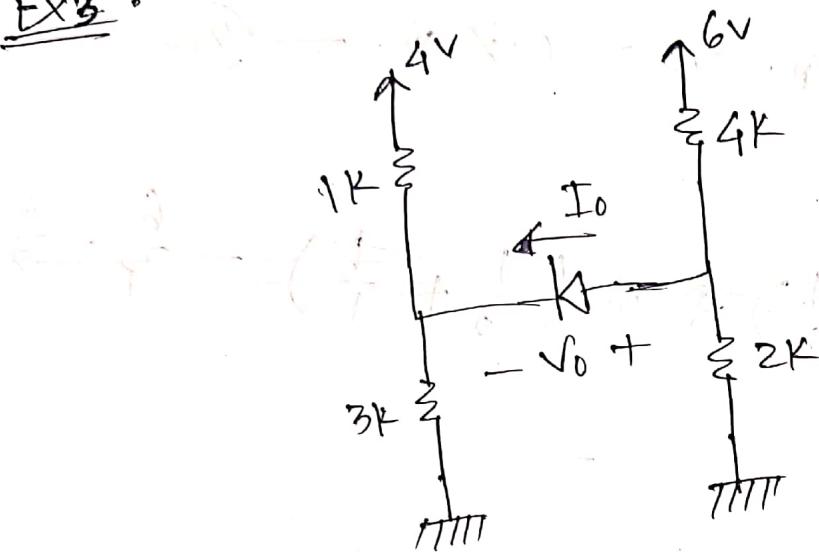
$$\text{For D}_2, I_2 = 0.25 \therefore I_2 < 0 \therefore \text{OFF}$$

$\therefore$  assumption correct

A  $\therefore$  Assumption is correct

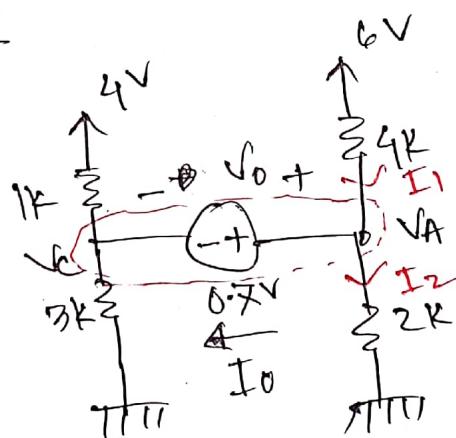
$$\therefore I_1 = 0.61 \text{ mA}, I_2 = 0.25 \text{ mA}$$

$V_o = 0 \text{ V}$  } Answer

Ex 3:find  $I_0$  and  $V_0$ 

use

$$CVDT V_{D0} = 0.7V$$

Assumption, D=ON

$$\therefore V_0 = 0.7V$$

\* using super node [As we're floating voltage source  
we can't use direct nodal analysis, we  
have to use super Node]

$$\frac{V_A - 6}{4} + \frac{V_A - 0}{2} + \frac{V_C - 0}{3} + \frac{V_C - 7}{1} = 0 \quad (1)$$

again

Sub.: \_\_\_\_\_

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OR,

$$\sqrt{A} \left( \frac{1}{4} + \frac{1}{2} \right) = \frac{6}{4} - \frac{0}{2} + \sqrt{C} \left( \frac{1}{3} + \frac{1}{1} \right)$$

$$- \frac{0}{3} - \frac{4}{1} = 0$$

①

Again,  $\sqrt{A} - \sqrt{C} = 0.7$  ②

Now from ① and ② we get,

$$\sqrt{A} = 3.09V$$

$$\sqrt{C} = 2.39V$$

Now

$$I_1 = \frac{6 - \sqrt{A}}{4} = \frac{6 - 3.09}{4} = \cancel{\frac{3}{4}} 0.7275$$

$$I_2 = \frac{\sqrt{A} - 0}{2} = \cancel{\frac{3}{2}} 1.545 \text{ mA}$$

KCL :  $I_1 = I_0 + I_2$

$$\therefore I_0 = I_1 - I_2 = -\cancel{\frac{3}{2}} - 0.8175 \text{ mA}$$

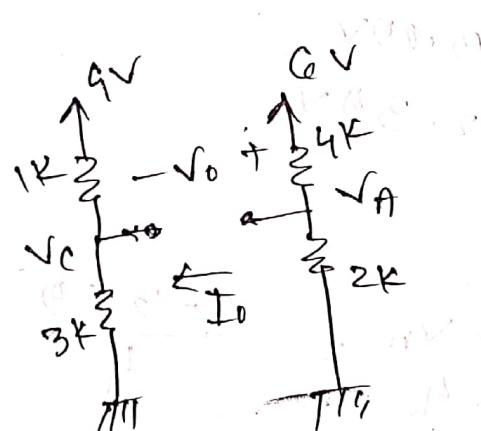
verificationHence for D,  $I_D = -0.8175$ 

$$\therefore I_D < 0$$

 $\therefore D$  is OFF

(B) Assumption wrong

∴ Here diode should be OFF well



$$\therefore I_D = 0 \text{ mA}$$

$$\begin{aligned} \therefore V_D &= V_A - V_C \\ &= 3 - 2 \\ &= -1 \text{ V} \end{aligned}$$

Voltage  
divider  
rule

$$V_A - 0 = \frac{2}{2+4} \times 6 = 2 \text{ V}$$

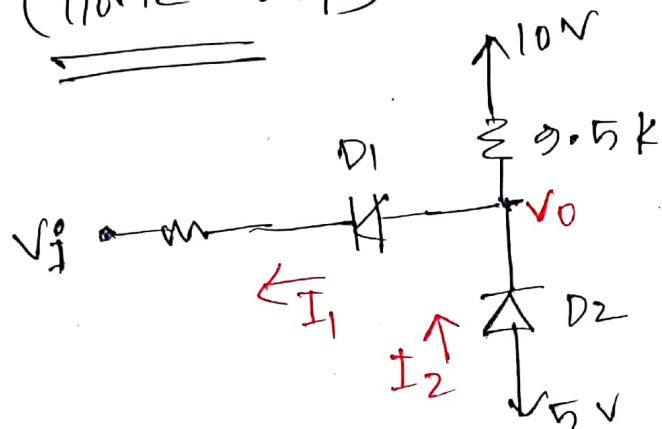
$$V_C - 0 = \frac{3}{3+1} \times 6 = 3 \text{ V}$$

Sub.: \_\_\_\_\_

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Ex: (Home work)



Find  $V_o$  when,

$$\textcircled{1} \quad V_i = 2V$$

$$\textcircled{2} \quad V_i = 4V$$

use CVD  $[V_{D_0} = 0.6V]$  model.