Lecture 5

Why Diodes? Ideal Diodes Anode, A Forward Bias : 2 terminals Lathode, e -> Vanode > V cathode A -> C -> Shont Cincuit Revenue Bias: off 7 non-ideal -> Vanode / Venthode C A C Open

A Cincuit

1)=0

chanacturistics Vo= VA-Ve deal NON Forward Ò Region 1°, VD LO j Revense Bias/ OFF

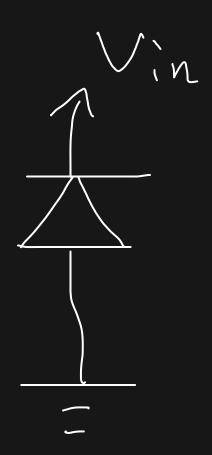
Diode -> 2 terminal, pnjunction -> non-linear -> Valve -> I deal : Piece wise Linear - Application: Rectifiens, Logic gates

Half-wave Rectifier D, = Ideal Input waveform o Diode -5 - t1

chanactenistics : mpnt = Vin, 5 lope=1

voltage actuss What is the Diode? Vin - 3 1 55 105 2 WVin = VD + Vout + D Vp / Vout 3 4 => Vout VA-VC2? QVDP P Vin poly mod Vout VP 2 3 - 5 v J

Examples 1) Vin t



Vait = ?

V; ~ V_i

