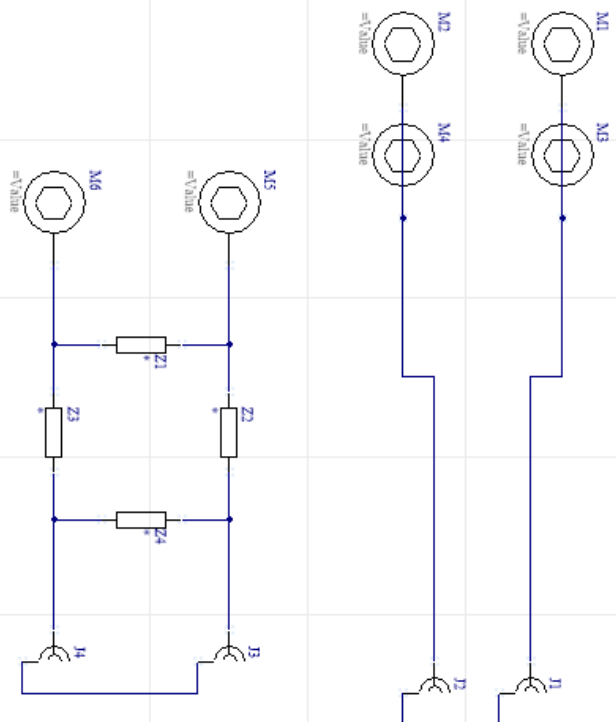
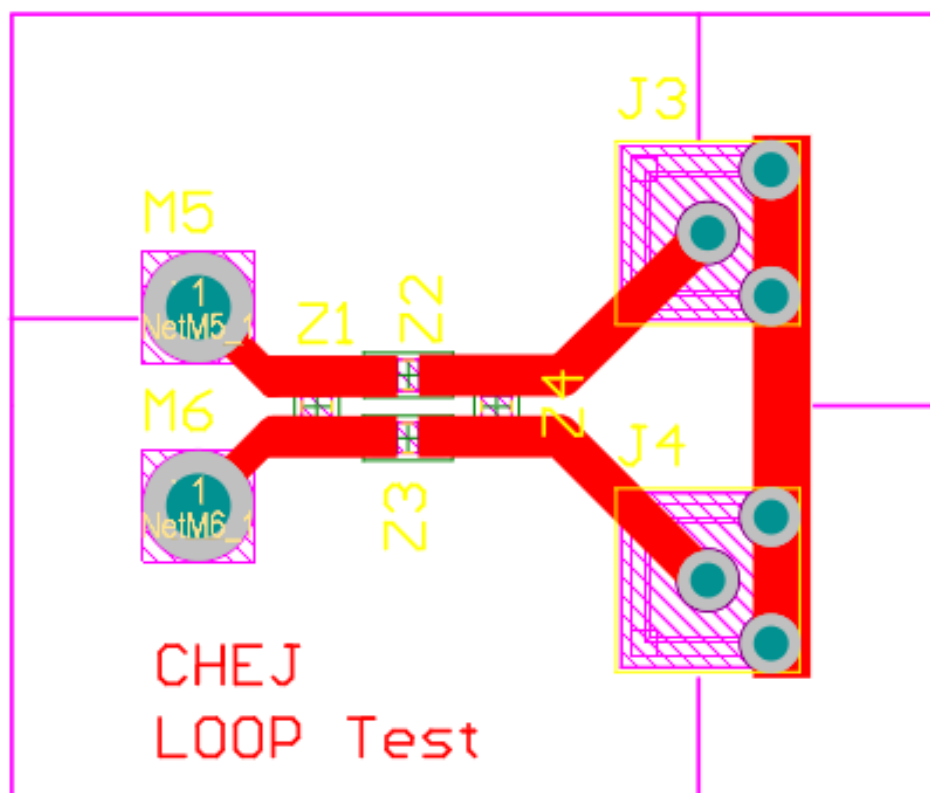
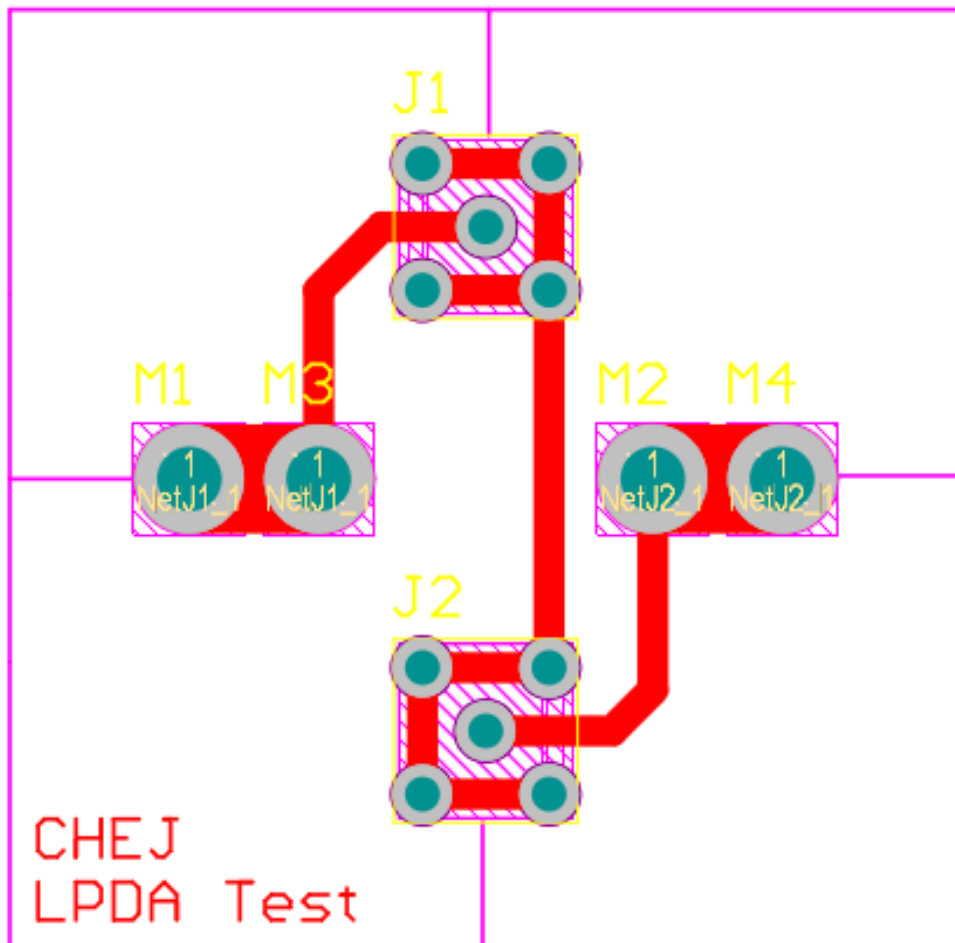


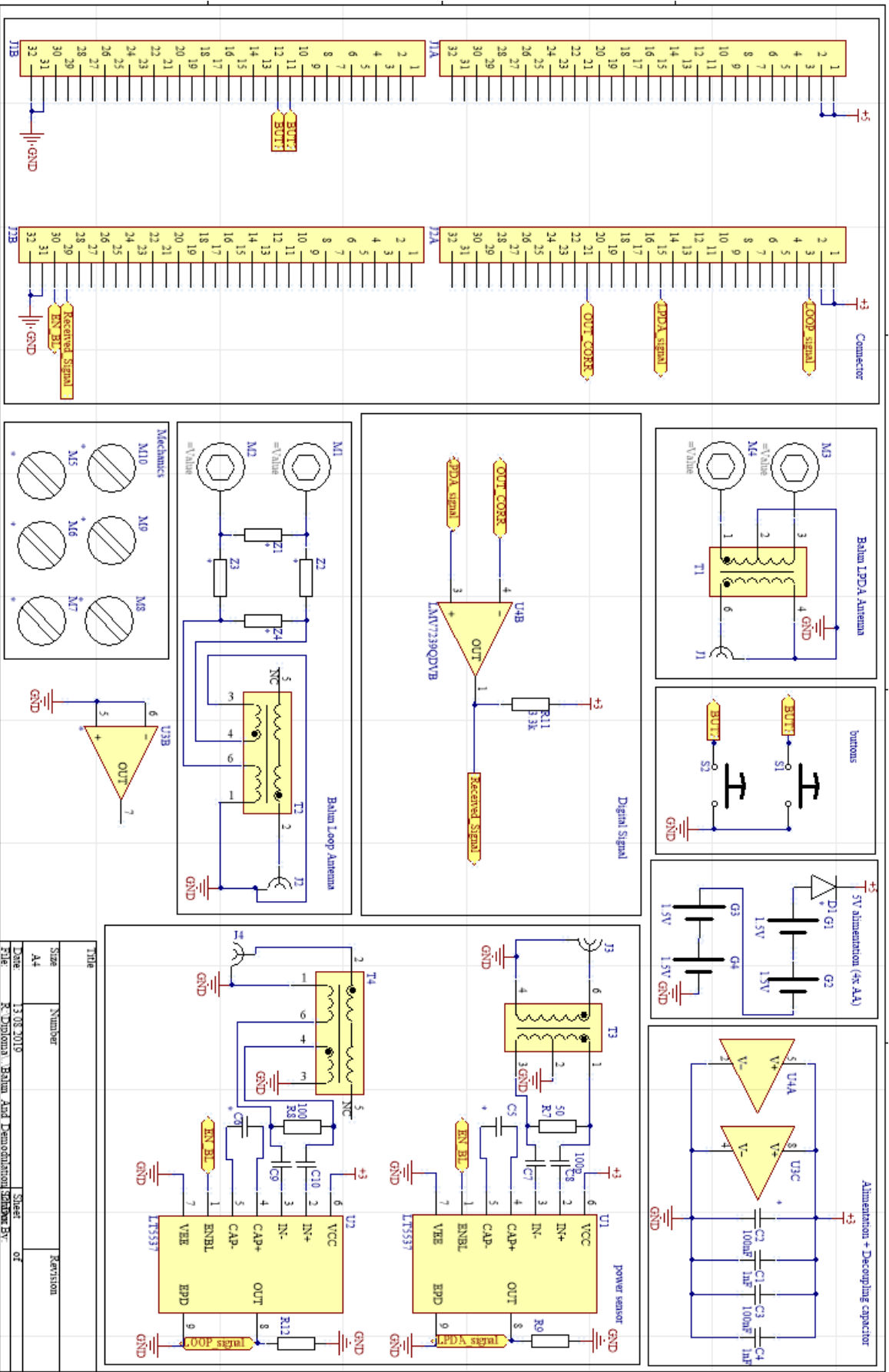
Schématique

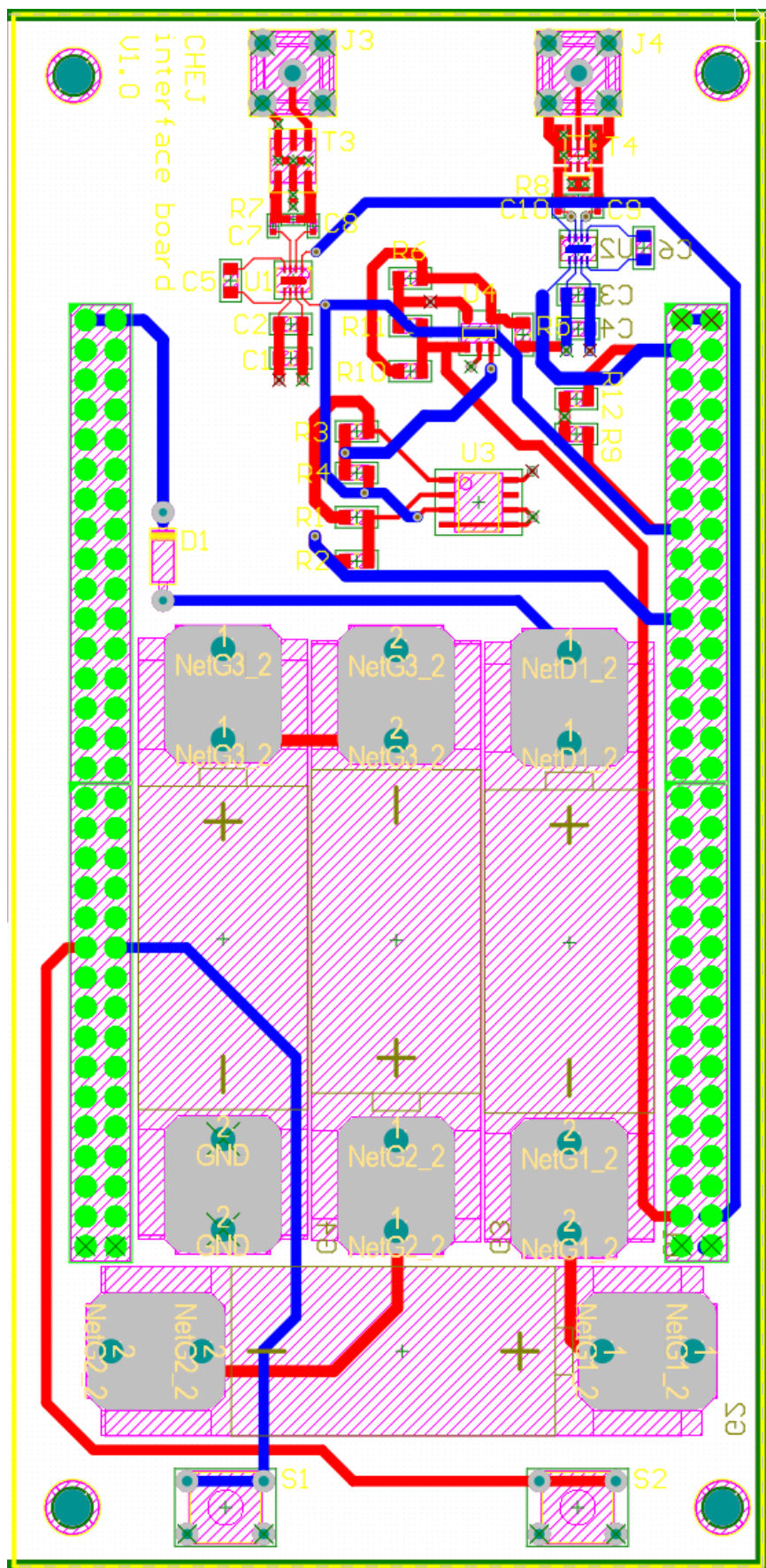


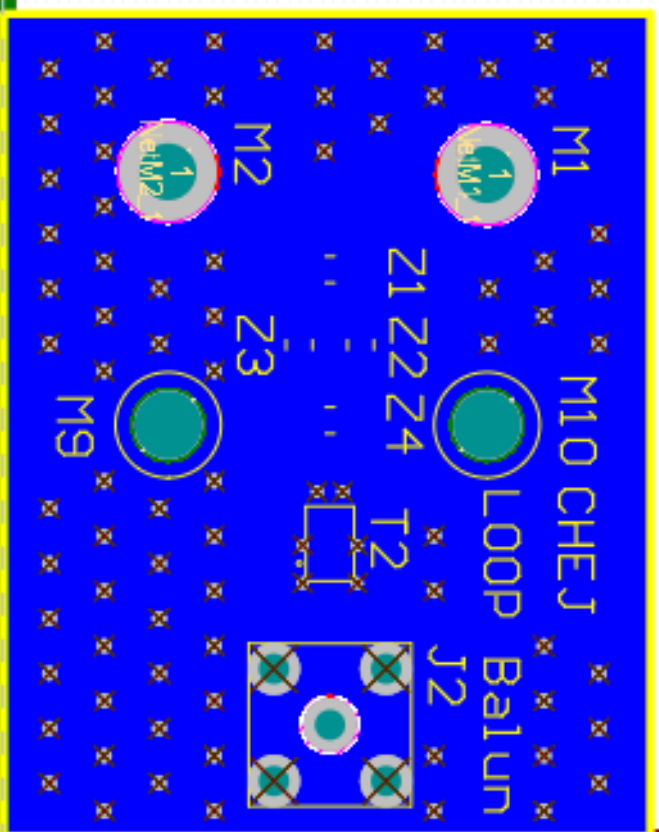
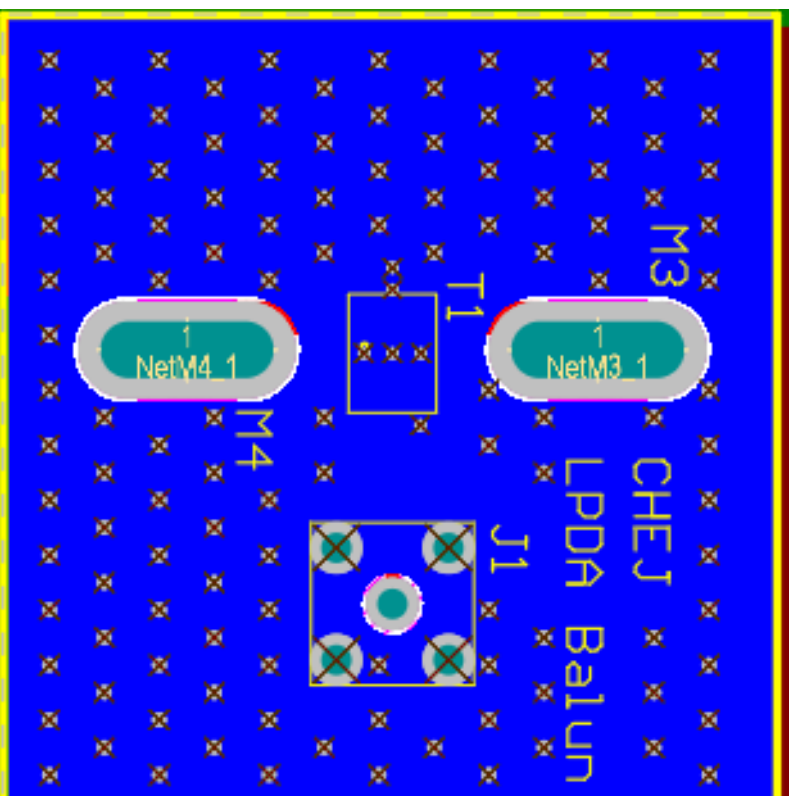
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File	Drawn By
R. Dindanos Test PCB SchDoc	



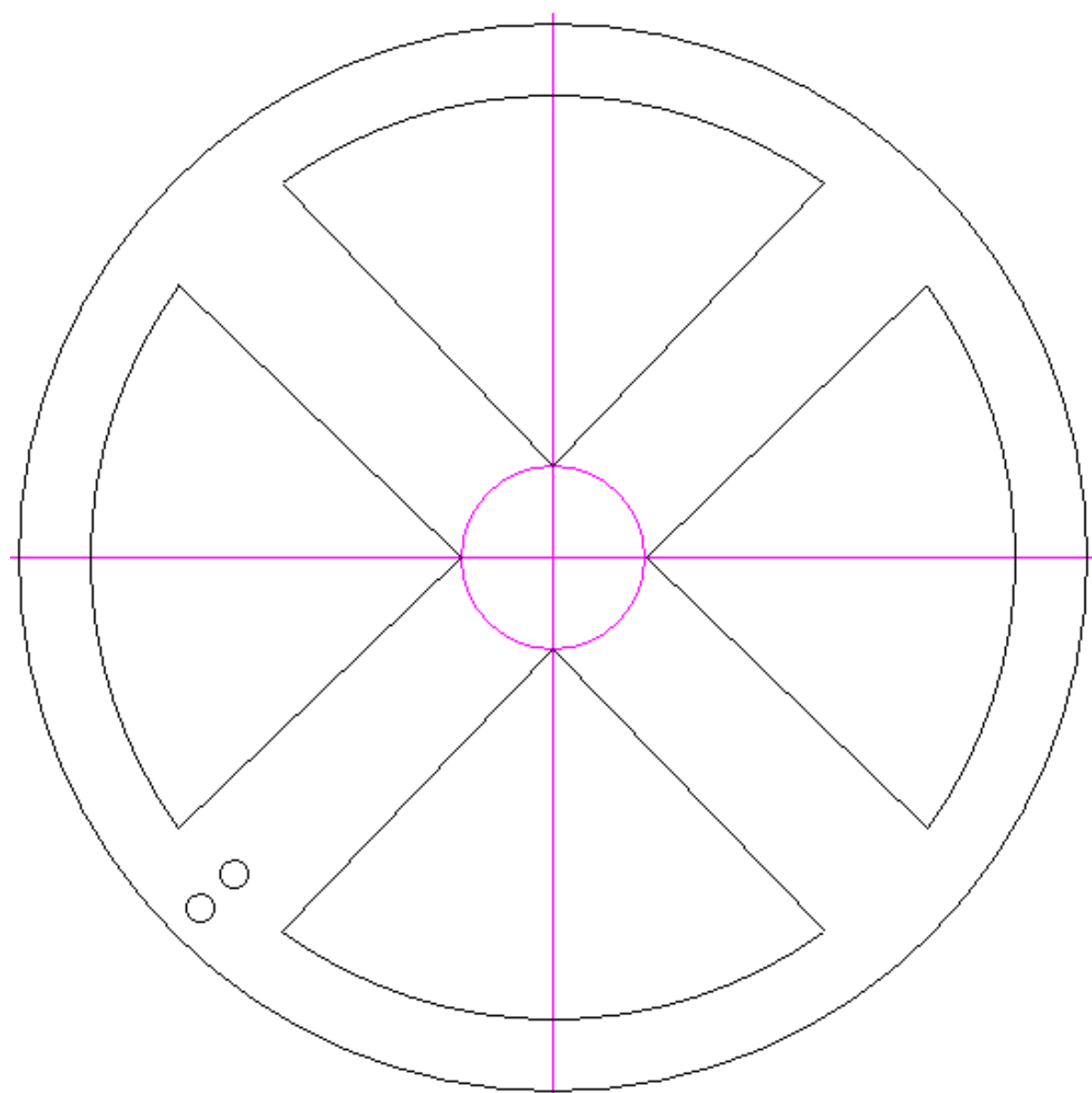
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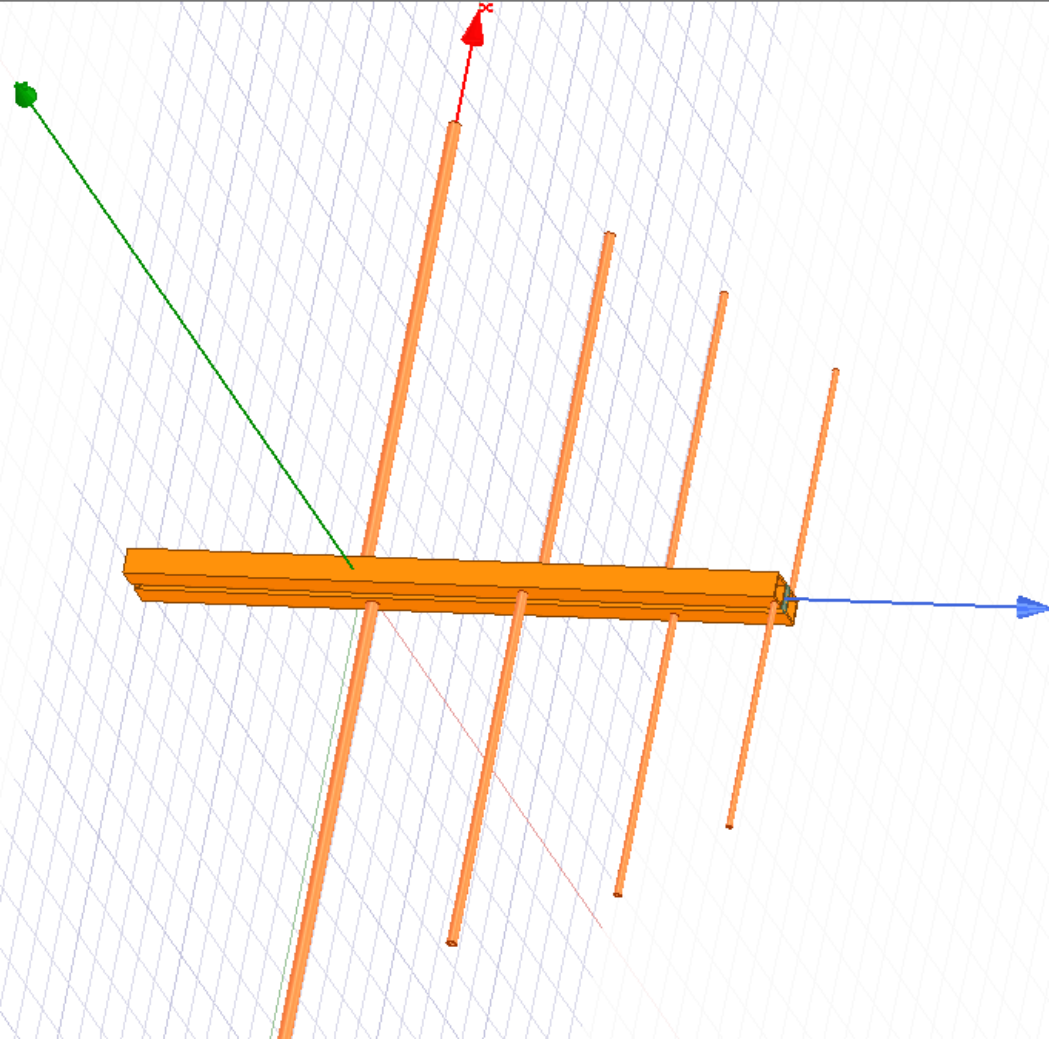


Graveuse



AnSys Simulation

Properties					⌵	✕
Name	Value	Unit	Evaluated Value	Type		
Frequency	434	MHz	434MHz	Design		
LzTerm	0.089*100...		89mm	Design		
Cyl_vol	0.5	mm	0.5mm	Design		
Boom_Vol	2	mm	2mm	Design		
Boom_W	10	mm	10mm	Design		
Boom_L	10	mm	10mm	Design		
spacer	4	mm	4mm	Design		
Sigma	0.095		0.095	Design		
T	0.8		0.8	Design		
R3	1.25	mm	1.25mm	Design		
L0	357	mm	357mm	Design		
L1	T*L0		285.6mm	Design		
L3	T*L2		182.784mm	Design		
L2	T*L1		228.48mm	Design		
lambda0	2*L0		714mm	Design		
lambda1	2*L1		571.2mm	Design		
lambda2	2*L2		456.96mm	Design		
lambda3	2*L3		365.568mm	Design		
D0	Sigma*Ta...		67.83mm	Design		
D1	Sigma*Ta...		122.094mm	Design		
D2	Sigma*Ta...		165.5052mm	Design		
D3	D2+LzTerm		254.5052mm	Design		
Boom_H	D3		254.5052mm	Design		
R0	2.5	mm	2.5mm	Design		
R1	2	mm	2mm	Design		
R2	1.5	mm	1.5mm	Design		
c	299792458		299792458	Design		
wavelength	c/Freque...		0.69076603225806	Design		
Frequ	434	MHz	434MHz	Design		
lambda	c/Frequ		0.69076603225806	Design		



Properties

Name	Value	Unit	Evaluated Value	Type
majorRad	lambda/(...		0.11001493762113	Design
minorRad	sqrt(Sect/p)		0.00056418958354776	Design
rad	2*minorRad		0.0011283791670955	Design
H	10	mm	10mm	Design
Frequ	434	MHz	434MHz	Design
Lambda	c/Frequ		0.69124423963134	Design
c	3000000000	m_per_sec	300000000m_per_sec	Design
Frequency	434	MHz	434MHz	Design
tuning	1		1	Design
BW	1	mm	1mm	Design
Sect	1	mm2	1mm2	Design
Diameter	2*majorRad	m	0.22002987524225m	Design

