

CSEE W4823 Advanced Logic Design Lab Assignment #5

1.	Create the Matla	ab codes (.m)	for the FIR	function	and testbench,	producing t	the files f	for inputs	and
ou	tputs.								

- 2. Provide the pseudo-code for the FIR core
- 3. Provide the ASM Chart for the FIR core
- 4. Provide the datapath block diagram for the FIR core

Deliverables:

Please create and submit a .zip file with the name "your_uni_lab5.zip" through Courseworks. The .zip file should contain the following:

- Report for the above tasks. Please include the pseudo-code, ASM chart, and datapath diagram.
- .m file
- The input and output files produced by .m file