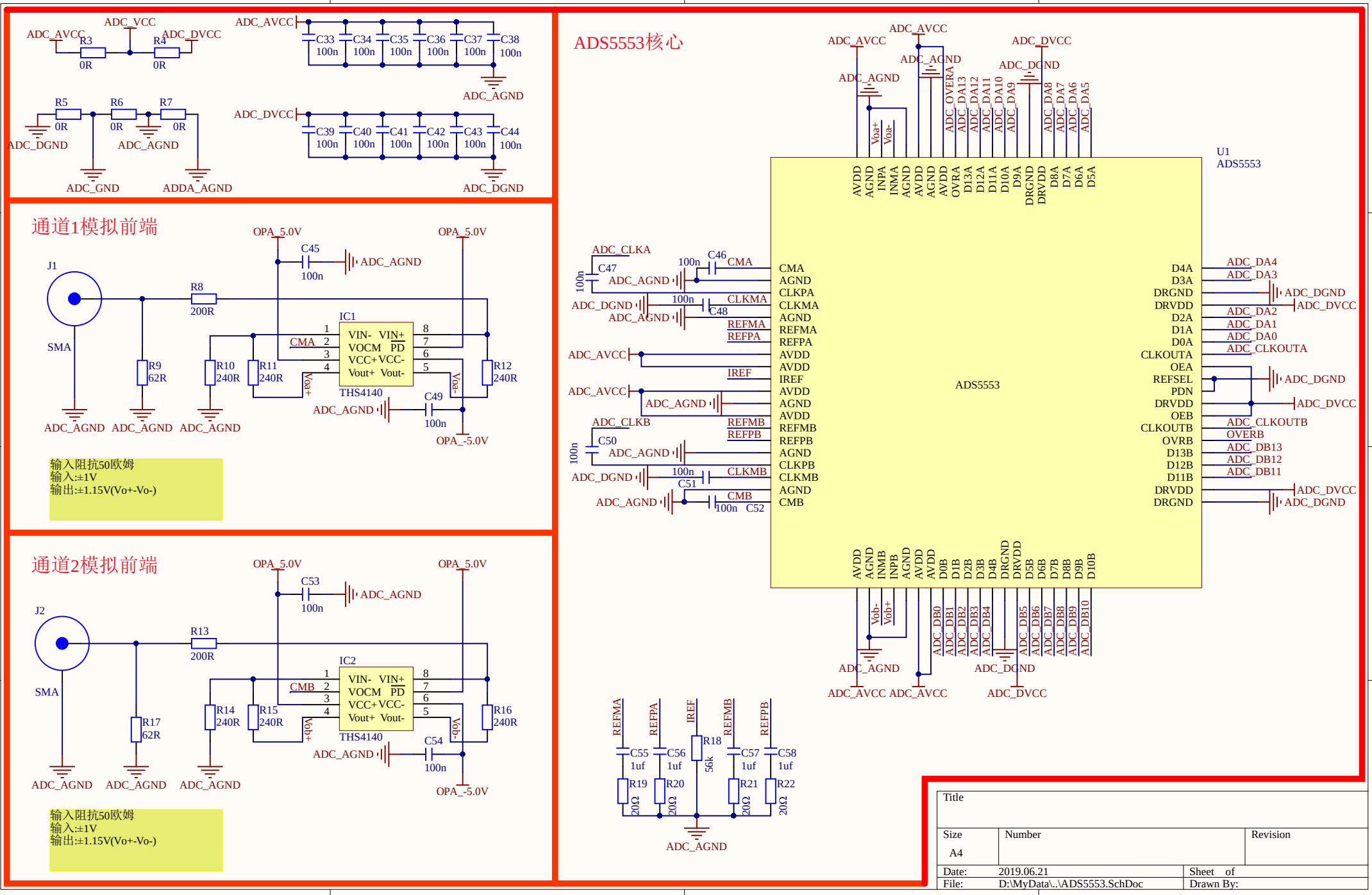
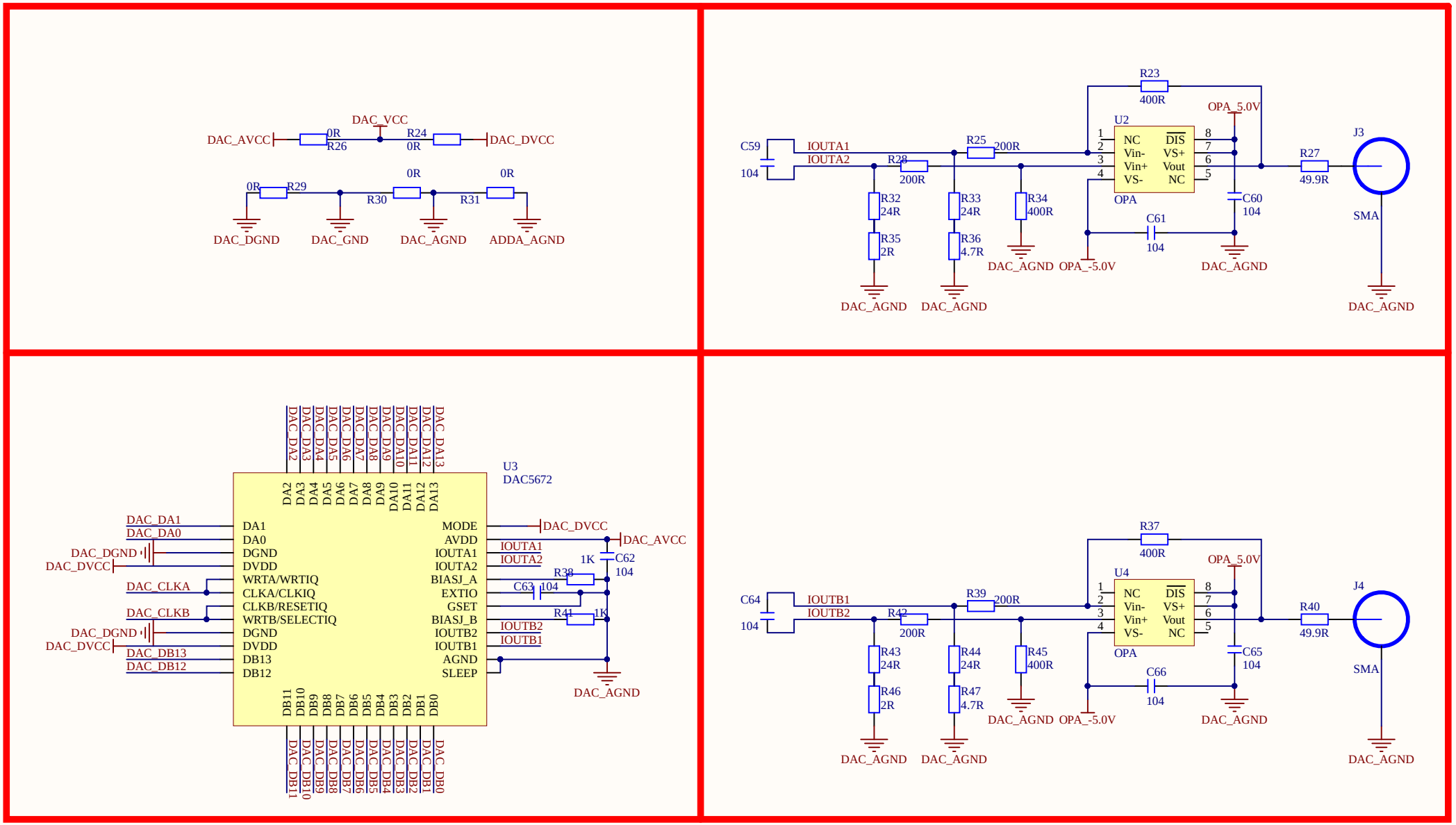
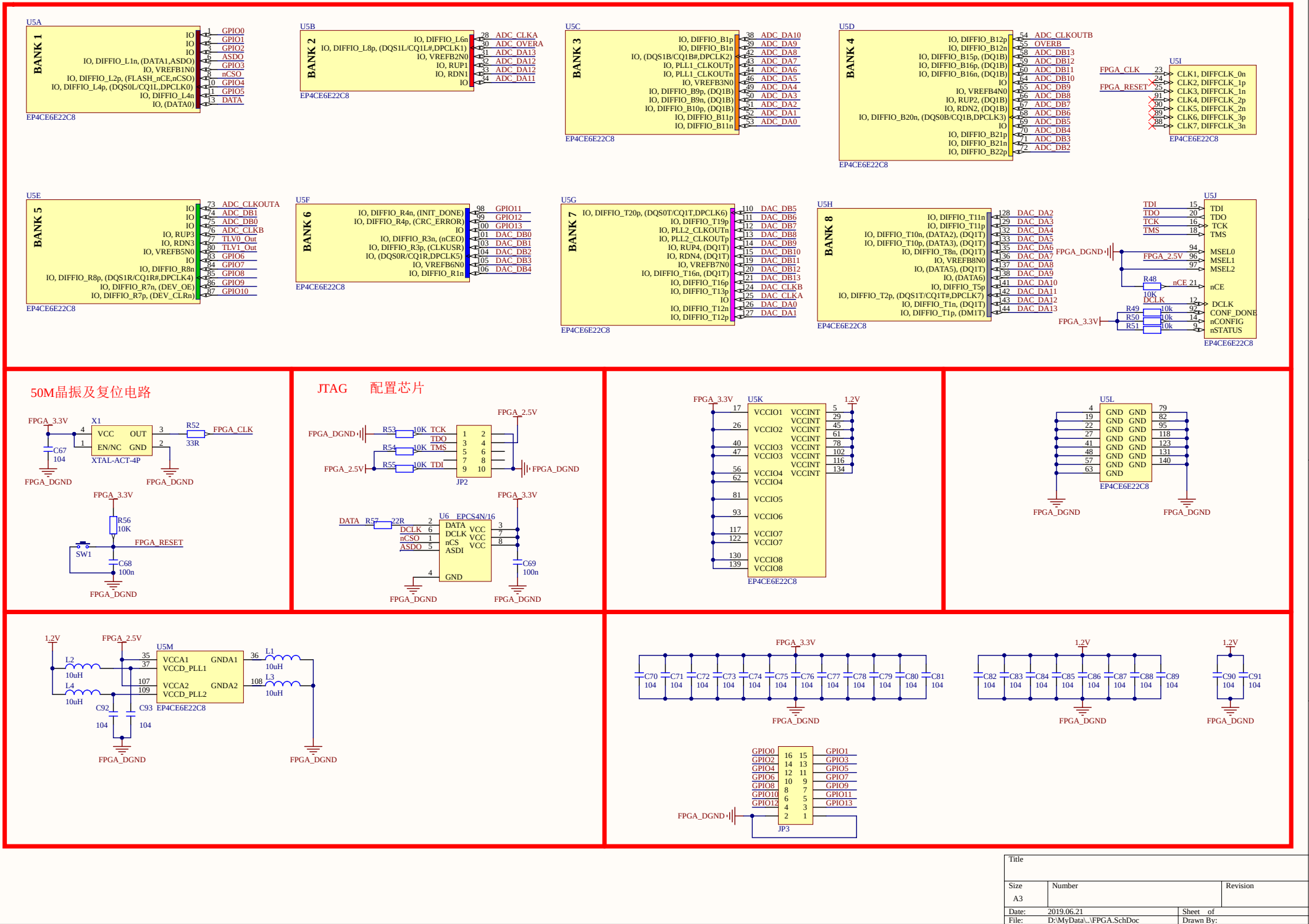
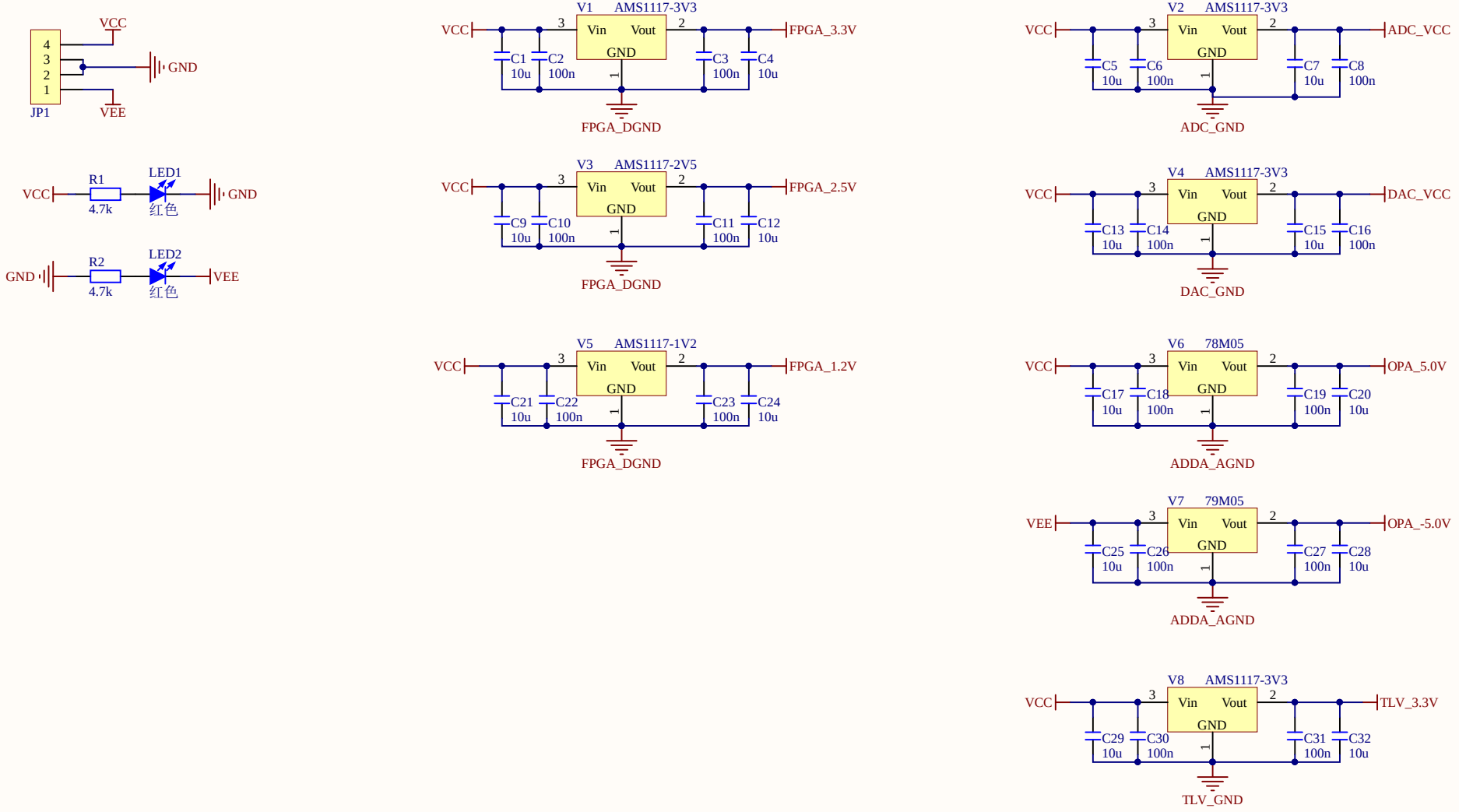

ADC_AVCC
ADC_VCC
ADC_DVCC
R3 0R
R4 0R
R5 0R
R6 0R
R7 0R
ADC_DGND
ADC_AGND
ADC_GND
ADDA_AGND
C33 100n
C34 100n
C35 100n
C36 100n
C37 100n
C38 100n
C39 100n
C40 100n
C41 100n
C42 100n
C43 100n
C44 100n
ADC_DVCC
ADC_DGND
通道1模拟前端
J1
SMA
R8 200R
R9 62R
R10 240R
R11 240R
R12 240R
C45 100n
C46 100n
C47 100n
C48 100n
C49 100n
IC1
THS4140
VIN- VIN+
VOCM PD
VCC+ VCC-
Vout+ Vout-
CMA
Voa+
Voa-
OPA_5.0V
OPA_-5.0V
ADC_AGND
输入阻抗50欧姆
输入:±1V
输出:±1.15V(Vo+-Vo-)
通道2模拟前端
J2
SMA
R13 200R
R14 240R
R15 240R
R16 240R
R17 62R
C53 100n
C54 100n
IC2
THS4140
CMB
Vob+
Vob-
ADS5553核心
U1
ADS5553
ADC_AVCC
ADC_AGND
ADC_DVCC
ADC_DGND
ADC_CLKA
ADC_CLKB
CMA
AGND
CLKPA
CLKMA
REFMA
REFPA
AVDD
IREF
REFMB
REFPB
CLKPB
CLKMB
CMB
C50
C51
C52
D4A
D3A
DRGND
DRVDD
D2A
D1A
D0A
CLKOUTA
OEA
REFSEL
PDN
OEB
CLKOUTB
OVRB
D13B
D12B
D11B
ADC_DA4
ADC_DA3
ADC_DA2
ADC_DA1
ADC_DA0
ADC_CLKOUTA
ADC_CLKOUTB
OVERB
ADC_DB13
ADC_DB12
ADC_DB11
Vob-
Vob+
ADC_DB0
ADC_DB1
ADC_DB2
ADC_DB3
ADC_DB4
ADC_DB5
ADC_DB6
ADC_DB7
ADC_DB8
ADC_DB9
ADC_DB10
REFMA
REFPA
IREF
REFMB
REFPB
C55 1uf
C56 1uf
C57 1uf
C58 1uf
R18 56k
R19 20Ω
R20 20Ω
R21 20Ω
R22 20Ω
Title
Size A4
Number
Revision
Date: 2019.06.21
Sheet of
File: D:\MyData\..\ADS5553.SchDoc
Drawn By:

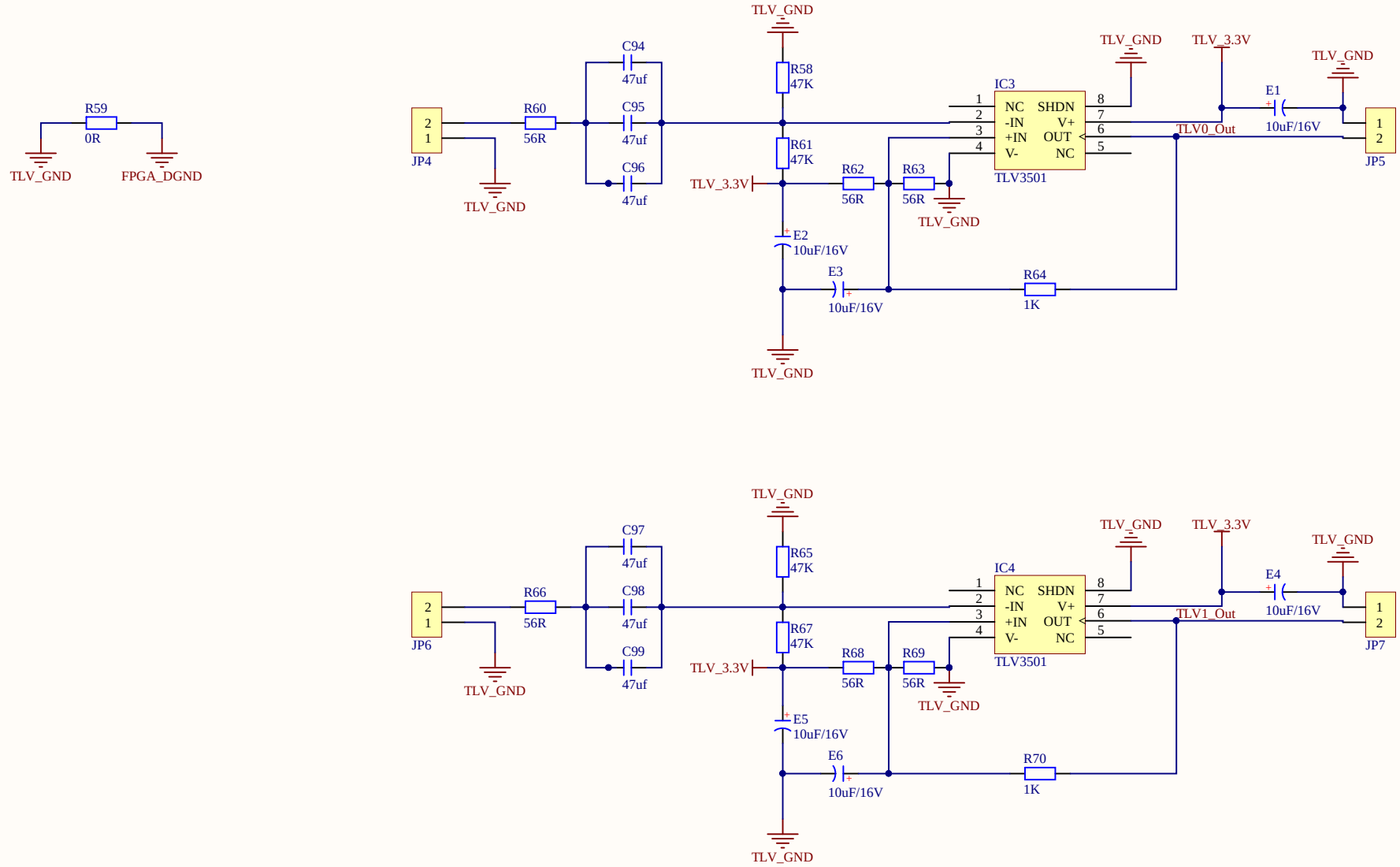


Title		
Size A4	Number	Revision
Date: 2019.06.21		Sheet of
File: D:\MyData\..\DAC5672.SchDoc		Drawn By:

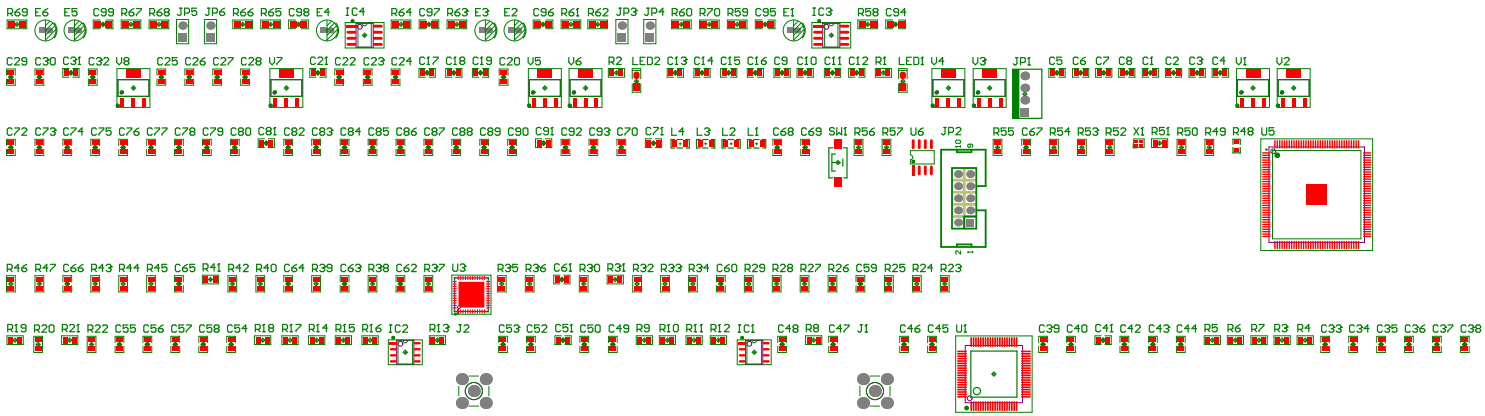

U5A
BANK 1
IO
IO
IO
IO, DIFFIO_L1n, (DATA1,ASDO)
IO, VREFB1N0
IO, DIFFIO_L2p, (FLASH_nCE,nCSO)
IO, DIFFIO_L4p, (DQS0L/CQ1L,DPCLK0)
IO, DIFFIO_L4n
IO, (DATA0)
1 GPIO0
2 GPIO1
3 GPIO2
5 ASDO
9 nCSO
10 GPIO4
11 GPIO5
13 DATA
EP4CE6E22C8
U5B
BANK 2
IO, DIFFIO_L6n
IO, DIFFIO_L8p, (DQS1L/CQ1L#,DPCLK1)
IO, VREFB2N0
IO, RUP1
IO, RDN1
IO
28 ADC_CLKA
30 ADC_OVERA
31 ADC_DA13
32 ADC_DA12
33 ADC_DA12
34 ADC_DA11
EP4CE6E22C8
U5C
BANK 3
IO, DIFFIO_B1p
IO, DIFFIO_B1n
IO, (DQS1B/CQ1B#,DPCLK2)
IO, PLL1_CLKOUTp
IO, PLL1_CLKOUTn
IO, VREFB3N0
IO, DIFFIO_B9p, (DQ1B)
IO, DIFFIO_B9n, (DQ1B)
IO, DIFFIO_B10p, (DQ1B)
IO, DIFFIO_B11p
IO, DIFFIO_B11n
38 ADC_DA10
39 ADC_DA9
42 ADC_DA8
43 ADC_DA7
44 ADC_DA6
46 ADC_DA5
49 ADC_DA4
50 ADC_DA3
51 ADC_DA2
52 ADC_DA1
53 ADC_DA0
EP4CE6E22C8
U5D
BANK 4
IO, DIFFIO_B12p
IO, DIFFIO_B12n
IO, DIFFIO_B15p, (DQ1B)
IO, DIFFIO_B16p, (DQ1B)
IO, DIFFIO_B16n, (DQ1B)
IO
IO, VREFB4N0
IO, RUP2, (DQ1B)
IO, RDN2, (DQ1B)
IO, DIFFIO_B20n, (DQS0B/CQ1B,DPCLK3)
IO
IO, DIFFIO_B21p
IO, DIFFIO_B21n
IO, DIFFIO_B22p
54 ADC_CLKOUTB
55 OVERB
58 ADC_DB13
59 ADC_DB12
60 ADC_DB11
64 ADC_DB10
65 ADC_DB9
66 ADC_DB8
67 ADC_DB7
68 ADC_DB6
69 ADC_DB5
70 ADC_DB4
71 ADC_DB3
72 ADC_DB2
EP4CE6E22C8
U5I
FPGA_CLK 23 CLK1, DIFFCLK_0n
24 CLK2, DIFFCLK_1p
FPGA_RESET 25 CLK3, DIFFCLK_1n
91 CLK4, DIFFCLK_2p
90 CLK5, DIFFCLK_2n
89 CLK6, DIFFCLK_3p
88 CLK7, DIFFCLK_3n
EP4CE6E22C8
U5E
BANK 5
IO
IO
IO
IO, RUP3
IO, RDN3
IO, VREFB5N0
IO
IO, DIFFIO_R8n
IO, DIFFIO_R8p, (DQS1R/CQ1R#,DPCLK4)
IO, DIFFIO_R7n, (DEV_OE)
IO, DIFFIO_R7p, (DEV_CLRn)
73 ADC_CLKOUTA
74 ADC_DB1
75 ADC_DB0
76 ADC_CLKB
77 TLV0_Out
80 TLV1_Out
83 GPIO6
84 GPIO7
85 GPIO8
86 GPIO9
87 GPIO10
EP4CE6E22C8
U5F
BANK 6
IO, DIFFIO_R4n, (INIT_DONE)
IO, DIFFIO_R4p, (CRC_ERROR)
IO
IO, DIFFIO_R3n, (nCEO)
IO, DIFFIO_R3p, (CLKUSR)
IO, (DQS0R/CQ1R,DPCLK5)
IO, VREFB6N0
IO, DIFFIO_R1n
98 GPIO11
99 GPIO12
100 GPIO13
101 DAC_DB0
103 DAC_DB1
104 DAC_DB2
105 DAC_DB3
106 DAC_DB4
EP4CE6E22C8
U5G
BANK 7
IO, DIFFIO_T20p, (DQS0T/CQ1T,DPCLK6)
IO, DIFFIO_T19p
IO, PLL2_CLKOUTn
IO, PLL2_CLKOUTp
IO, RUP4, (DQ1T)
IO, RDN4, (DQ1T)
IO, VREFB7N0
IO, DIFFIO_T16n, (DQ1T)
IO, DIFFIO_T16p
IO, DIFFIO_T13p
IO
IO, DIFFIO_T12n
IO, DIFFIO_T12p
110 DAC_DB5
111 DAC_DB6
112 DAC_DB7
113 DAC_DB8
114 DAC_DB9
115 DAC_DB10
119 DAC_DB11
120 DAC_DB12
121 DAC_DB13
124 DAC_CLKB
125 DAC_CLKA
126 DAC_DA0
127 DAC_DA1
EP4CE6E22C8
U5H
BANK 8
IO, DIFFIO_T11n
IO, DIFFIO_T11p
IO, DIFFIO_T10n, (DATA2), (DQ1T)
IO, DIFFIO_T10p, (DATA3), (DQ1T)
IO, DIFFIO_T8n, (DQ1T)
IO, VREFB8N0
IO, (DATA5), (DQ1T)
IO, (DATA6)
IO, DIFFIO_T5p
IO, DIFFIO_T2p, (DQS1T/CQ1T#,DPCLK7)
IO, DIFFIO_T1n, (DQ1T)
IO, DIFFIO_T1p, (DM1T)
128 DAC_DA2
129 DAC_DA3
132 DAC_DA4
133 DAC_DA5
135 DAC_DA6
136 DAC_DA7
137 DAC_DA8
138 DAC_DA9
141 DAC_DA10
142 DAC_DA11
143 DAC_DA12
144 DAC_DA13
EP4CE6E22C8
U5J
TDI 15 TDI
TDO 20 TDO
TCK 16 TCK
TMS 18 TMS
FPGA_DGND
FPGA_2.5V 94 MSEL0
96 MSEL1
97 MSEL2
R48 10K
nCE 21 nCE
DCLK 12 DCLK
FPGA_3.3V
R49 10k 92 CONF_DONE
R50 10k 14 nCONFIG
R51 10k 9 nSTATUS
EP4CE6E22C8
50M晶振及复位电路
FPGA_3.3V
X1
VCC OUT
EN/NC GND
XTAL-ACT-4P
C67 104
R52 33R
FPGA_CLK
FPGA_DGND
FPGA_3.3V
R56 10K
SW1
FPGA_RESET
C68 100n
FPGA_DGND
JTAG 配置芯片
FPGA_2.5V
FPGA_DGND
R53 10K TCK
R54 10K TMS
R55 10K TDI
TDO
JP2
FPGA_3.3V
DATA R57 22R
U6 EPCS4N/16
DATA VCC
DCLK VCC
nCS VCC
ASDI
GND
C69 100n
U5K
FPGA_3.3V
1.2V
VCCIO1 VCCINT
VCCIO2 VCCINT
VCCIO3 VCCINT
VCCIO4 VCCINT
VCCIO5
VCCIO6
VCCIO7 VCCIO7
VCCIO8 VCCIO8
EP4CE6E22C8
U5L
GND GND
EP4CE6E22C8
FPGA_DGND
1.2V
FPGA_2.5V
U5M
VCCA1 GNDA1
VCCD_PLL1
VCCA2 GNDA2
VCCD_PLL2
L2 10uH
L4 10uH
L1 10uH
L3 10uH
C92 104
C93 104
EP4CE6E22C8
FPGA_DGND
FPGA_3.3V
C70 C71 C72 C73 C74 C75 C76 C77 C78 C79 C80 C81 104
1.2V
C82 C83 C84 C85 C86 C87 C88 C89 104
C90 C91 104
GPIO0 GPIO1
GPIO2 GPIO3
GPIO4 GPIO5
GPIO6 GPIO7
GPIO8 GPIO9
GPIO10 GPIO11
GPIO12 GPIO13
JP3
Title
Size A3
Number
Revision
Date: 2019.06.21
Sheet of
File: D:\MyData\..\FPGA.SchDoc
Drawn By:



Title		
Size A4	Number	Revision
Date: 2019.06.21		Sheet of
File: D:\MyData\..\Power.SchDoc		Drawn By:



Title		
Size: A4	Number	Revision
Date: 2019.06.21		Sheet of
File: D:\MyData\..\TLV3501.SchDoc		Drawn By:


R69 E6 E5 C99 R67 R68 JP5 JP6 R66 R65 C98 E4 IC4 R64 C97 R63 E3 E2 C96 R61 R62 JP3 JP4 R60 R70 R59 C95 E1 IC3 R58 C94
C29 C30 C31 C32 V8 C25 C26 C27 C28 V7 C21 C22 C23 C24 C17 C18 C19 C20 V5 V6 R2 LED2 C13 C14 C15 C16 C9 C10 C11 C12 R1 LED1 V4 V3 JP1 C5 C6 C7 C8 C1 C2 C3 C4 V1 V2
C72 C73 C74 C75 C76 C77 C78 C79 C80 C81 C82 C83 C84 C85 C86 C87 C88 C89 C90 C91 C92 C93 C70 C71 L4 L3 L2 L1 C68 C69 SW1 R56 R57 U6 JP2 R55 C67 R54 R53 R52 X1 R51 R50 R49 R48 U5
R46 R47 C66 R43 R44 R45 C65 R41 R42 R40 C64 R39 C63 R38 C62 R37 U3 R35 R36 C61 R30 R31 R32 R33 R34 C60 R29 R28 R27 R26 C59 R25 R24 R23
R19 R20 R21 R22 C55 C56 C57 C58 C54 R18 R17 R14 R15 R16 IC2 R13 J2 C53 C52 C51 C50 C49 R9 R10 R11 R12 IC1 C48 R8 C47 J1 C46 C45 U1 C39 C40 C41 C42 C43 C44 R5 R6 R7 R3 R4 C33 C34 C35 C36 C37 C38