



## Semester (3rd) Final Exam schedule



Time: 10:00am to 1:00pm (3 hours)

|                       | Subject                     | Syllabus  | Notes   |
|-----------------------|-----------------------------|---|---|
| Wednesday<br>09 April | Math 4341<br>Linear Algebra | Introduction to Linear Algebra (Gilbert Strang): Chapter: 1.1, 1.3, 1.4, 2.1, 2.2, 2.3, 2.4, 3.1, 3.2, 3.3, 3.4, 3.5, 4.1, 4.2, 4.3, 4.4, 5.1, 5.2, 6.1, 6.2, 7.1  Elementary Linear Algebra (Howard Anton, Chris Rorres): Chapter: 1.1, 1.2, 1.3, 1.4, 1.5, 1.7, 3.1, 5.2, 5.3, 5.4, 5.5, 5.6, 9.3, 7.1, 7.2, 7.3  | Practice from<br>the exercises<br>given in the<br>books as<br>much as you<br>can. Your<br>answer<br>should be<br>short, clear<br>and concise. |
| 1 day gap             |                             |   |   |
| Friday<br>11 April    | CSE 4307<br>DBMS            | Chapter: 3,4,5,6,7,10(partial),<br>14(partial)  |   |
| 4 days gap            |                             |   |   |
| Wednesday<br>16 April | CSE 4305<br>COA             | Chapter 6, 7, 8, 12, 13, 14, 20   |   |
| 1 day gap             |                             |   |   |
| Friday<br>18 April    | CSE 4301<br>OOP             | After Mid: Chapter 11,12, 13, 14, 15  Before Mid: Objects and Classes, Operator Overloading, Friend Functions, Inheritance  | After Mid ~5 sets of question  Before Mid ~ 1 set of questions  |
| 3 day gap             |                             |   |   |
| Tuesday<br>22 April   | CSE 4303<br>DS              | Maybe all, waiting for sir's reply  | ТВА   |
| 1 day gap             |                             |   |   |
| Thursday<br>24 April  | EEE 4383                    | FET: Working principle, curves, math shown in class and quiz, drawing graphs, DC and AC biasing.  Diode: nai (no clipper clamper as well) BJT: Slide maths, RE model(very important), biasing related maths, maths in slide, basic theory-based questions(5 marks each).  Op Amp: Maths in assignments, combined circuit, cascaded circuit, differentiator, integrator circuit. | Question<br>Pattern: 2<br>from FET,<br>2 From<br>BJT, 2<br>From OP<br>amp. 70%<br>math and<br>30% theory                                      |