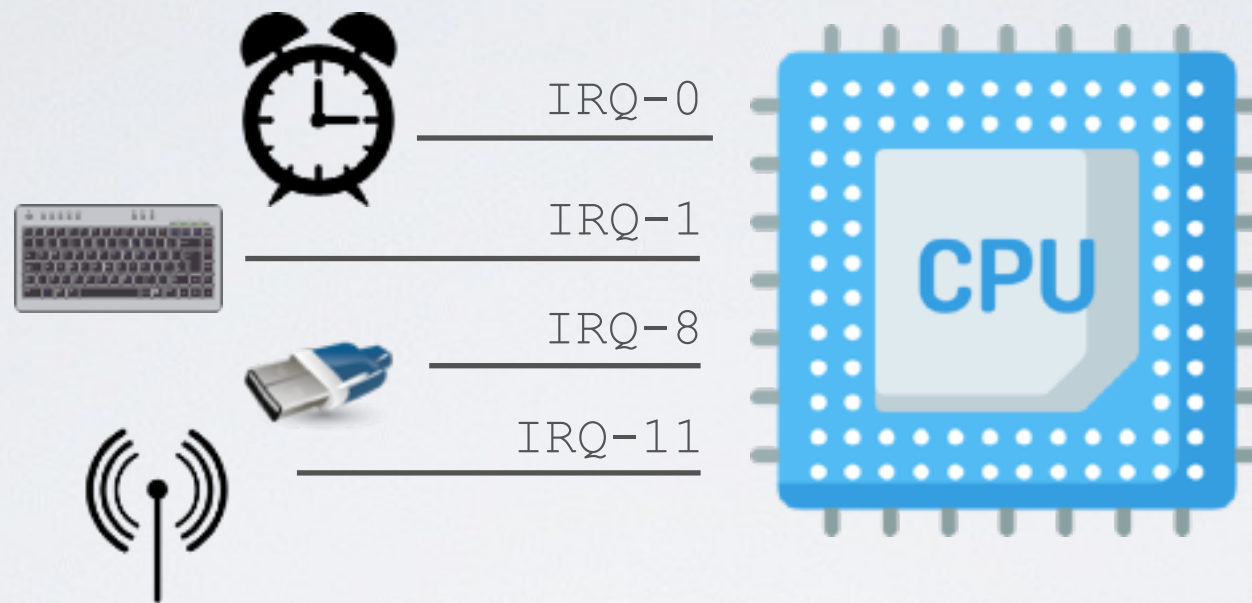
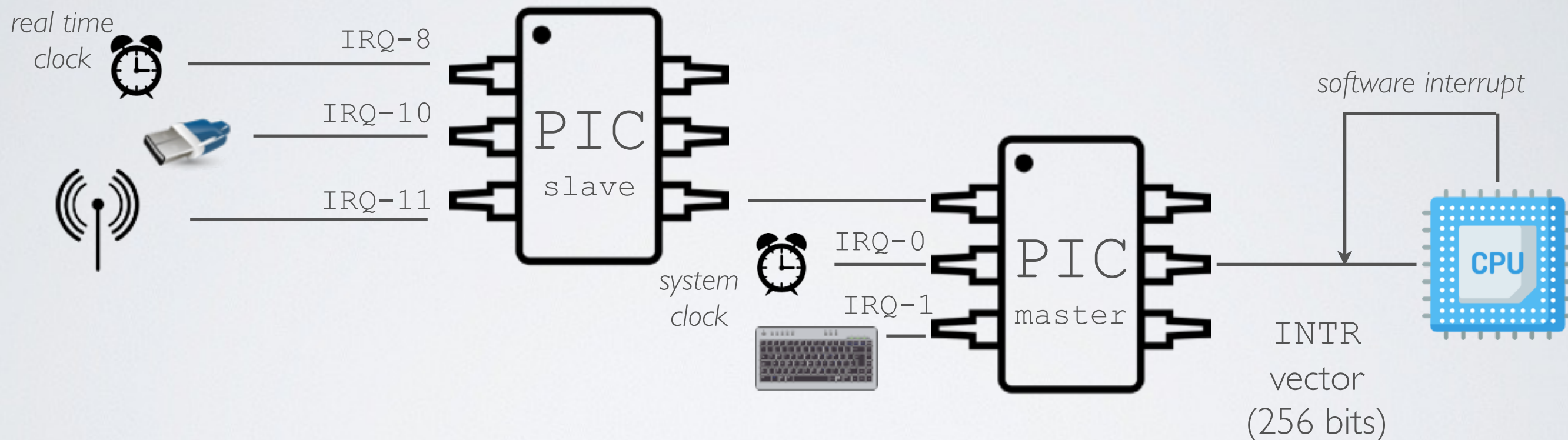


# External Interrupt - the naive implementation



- ➔ I/O devices are wired to **Interrupt Request lines** (IRQs)
- Not flexible (hardwired)
- CPU might get interrupted all the time
- How to handle interrupt priority

# Internal Interrupt and External Interrupt - the real implementation



➔ I/O devices have unique or shared IRQs that are managed by two **Programmable Interrupt Controllers** (PIC)