x86 Page Translation

Linear Address

31	22	21	12	11	0
Directory		Table		Offset	

1024 PDEs

Page Directory

Page Table

4-KByte Page

Physical Address

Page-Table Entry

Directory Entry

1024 PTEs

CR3 (PDBR)
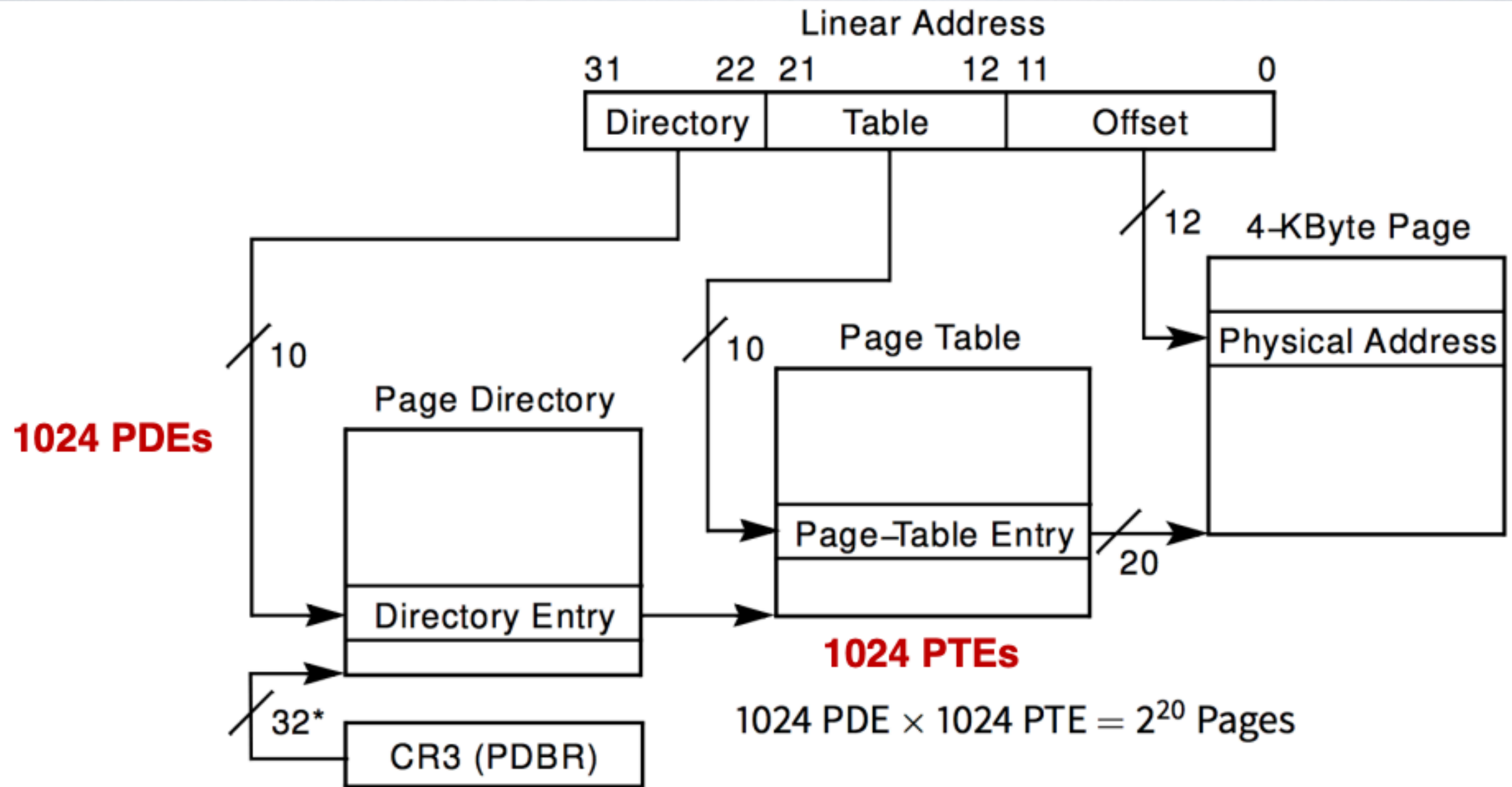
$1024 \text{ PDE} \times 1024 \text{ PTE} = 2^{20} \text{ Pages}$

*32 bits aligned onto a 4-KByte boundary

Faster Address Translation