Two-Level Page Lookup Physical Memory Virtual Address secondary page offset master page Physical Address Master Page Table offset frame page frame page frame Secondary Page Tables

32 bits address space, 4K pages, 4 bytes/PTE

- How many bits in offset? 4K
 so the virtual address requires requires I 2 bits for the offset
- We want a master page table to fit in one page
 4K/4 bytes = IK possible entries
 so the virtual address requires IO bits for the master page index
- We also want a secondary page table to fit in one page so the virtual address requires IO bits for the secondary page index
- \rightarrow 10 + 10 + 12 = 32 bits address This is why 4K page size is recommended