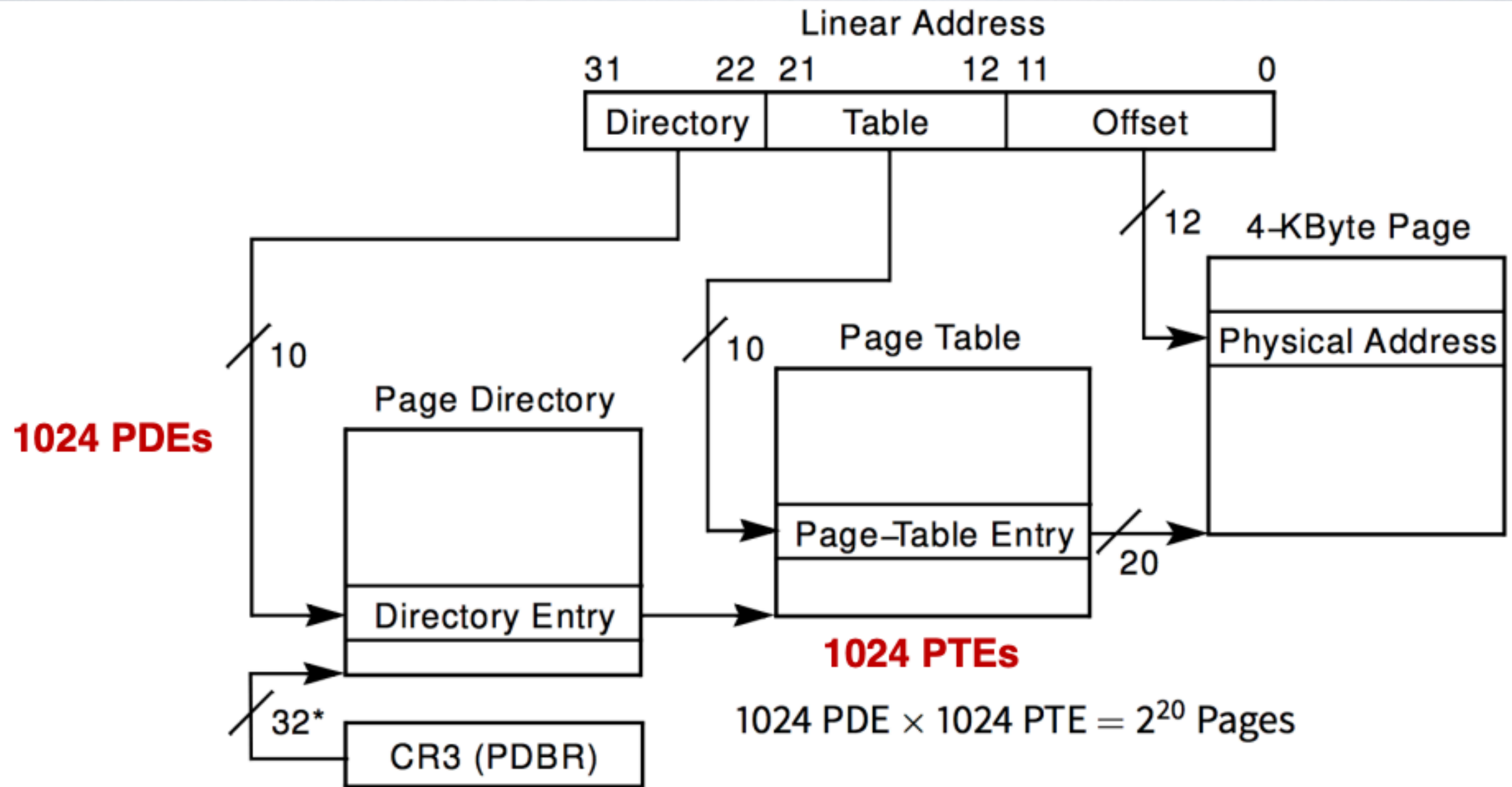


x86 Page Translation



*32 bits aligned onto a 4-KByte boundary

Faster Address Translation