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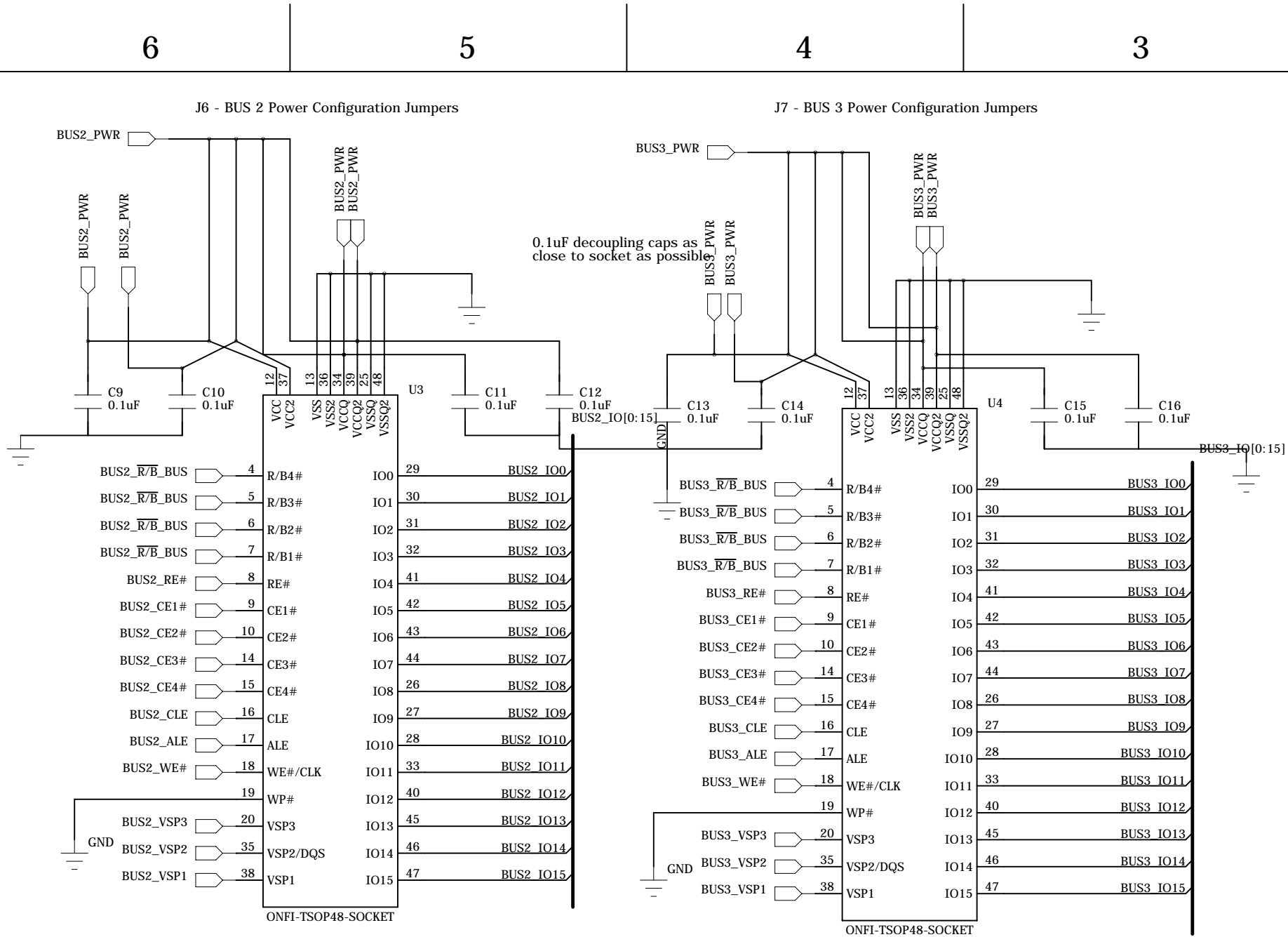
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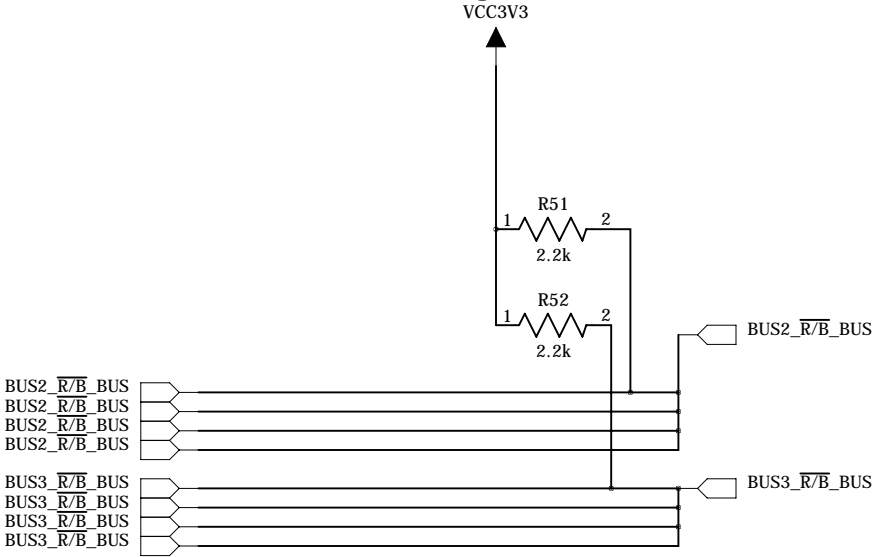
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

BUS 2/3 Configuration DIP Switch



1-3: Connect BUS 2 $\overline{R/B}$ [1:4] to bus
4: Force BUS 2 \overline{WP} off
5-9: Connect BUS 3 $\overline{R/B}$ [1:4] to bus
10: Force BUS 3 \overline{WP} off

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TITLE: Ming the Merciless II

DRAWN: <Drawn By>	DATED: <Drawn Date>
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

CODE:	SIZE:	DRAWING NO:	REV:
<Code>	B	<Drawing Number>	<Revision>
SCALE: <Scale>			SHEET: 2 of 12

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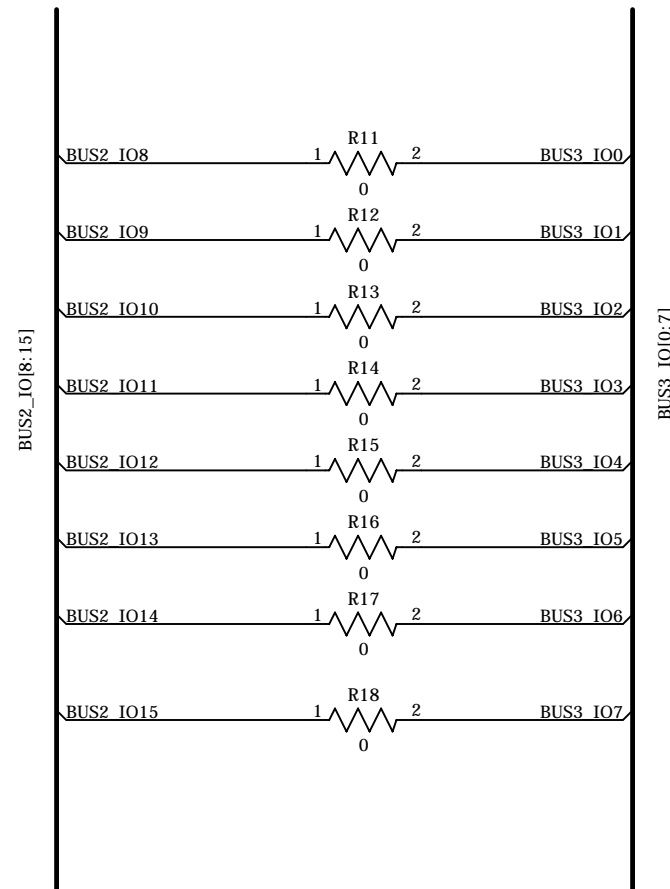
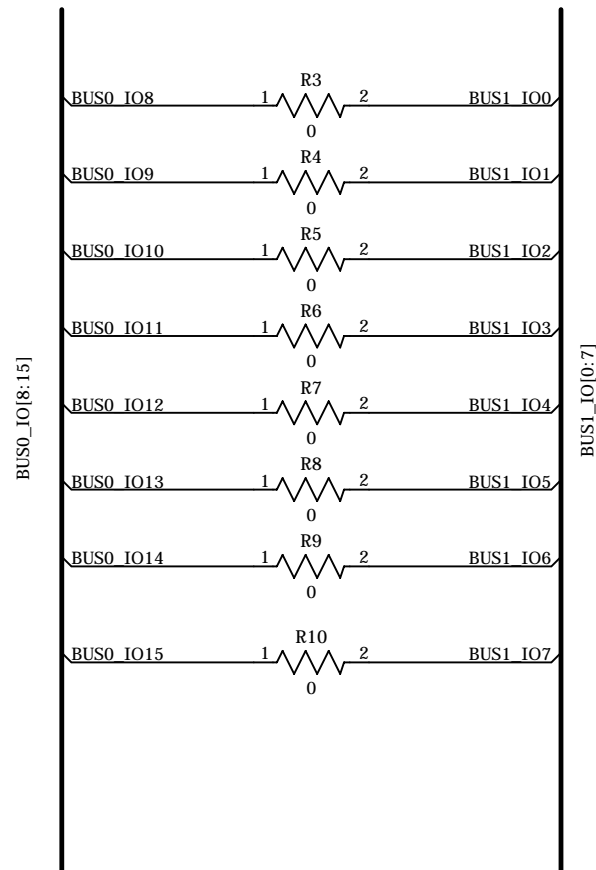
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	REVISION RECORD		
LTR	ECO NO:	APPROVED:	DATE:



0 Ohm Resistor Optional
Connect to enable x16 support on BUS0 and BUS2

DRAWN: <Drawn By>		DATED: <Drawn Date>		Ming the Merciless II				A
CHECKED: <Checked By>		DATED: <Checked Date>		CODE:	SIZE:	DRAWING NO:	REV:	
QUALITY CONTROL: <QC By>		DATED: <QC Date>		<Code>	B	<Drawing Number & Revision>		
RELEASED: <Released By>		DATED: <Release Date>						
SCALE: <Scale>				SHEET: 3 OF 12				

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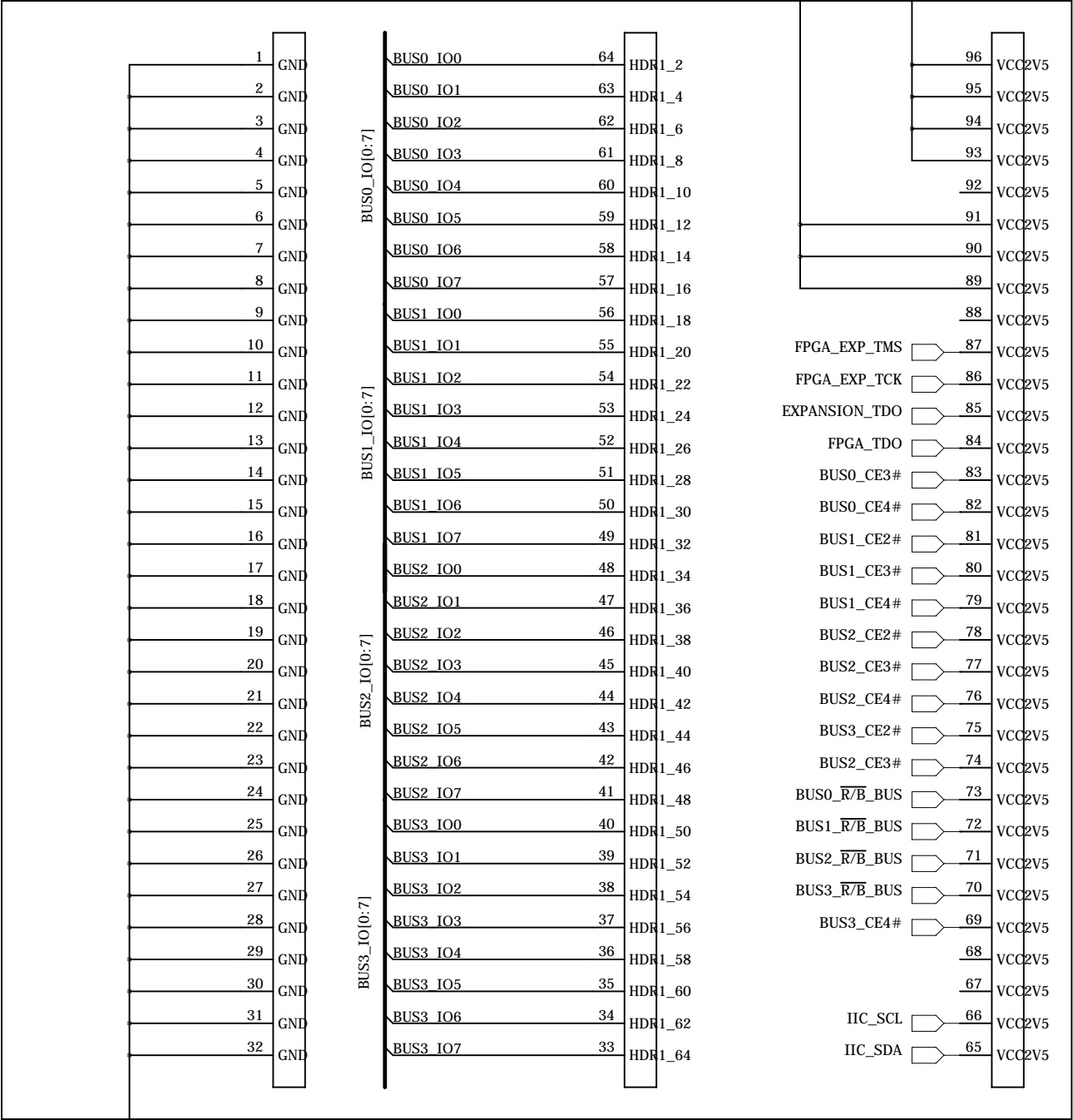
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XUPV5_XGL_J6

REVISION RECORD

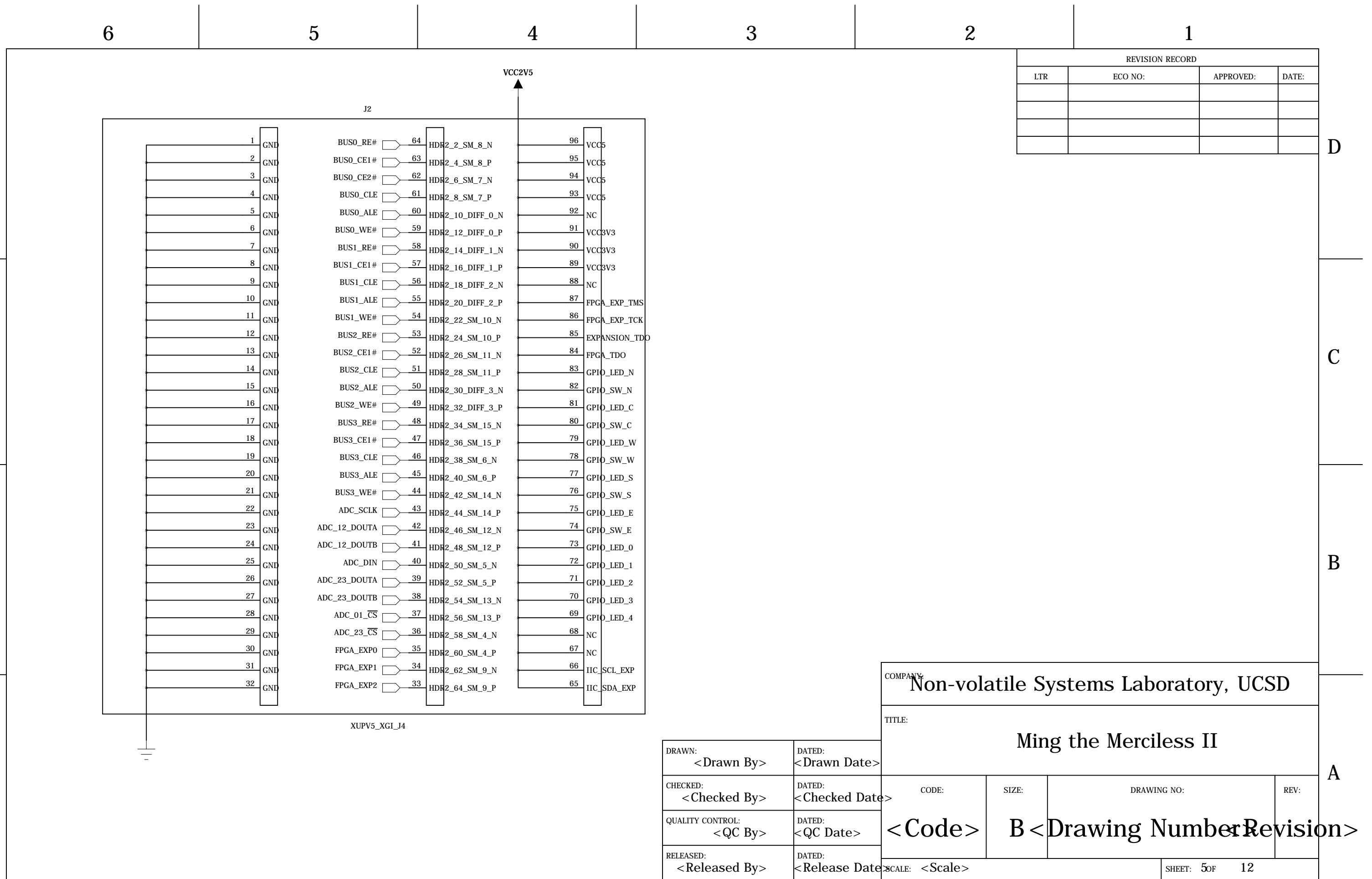
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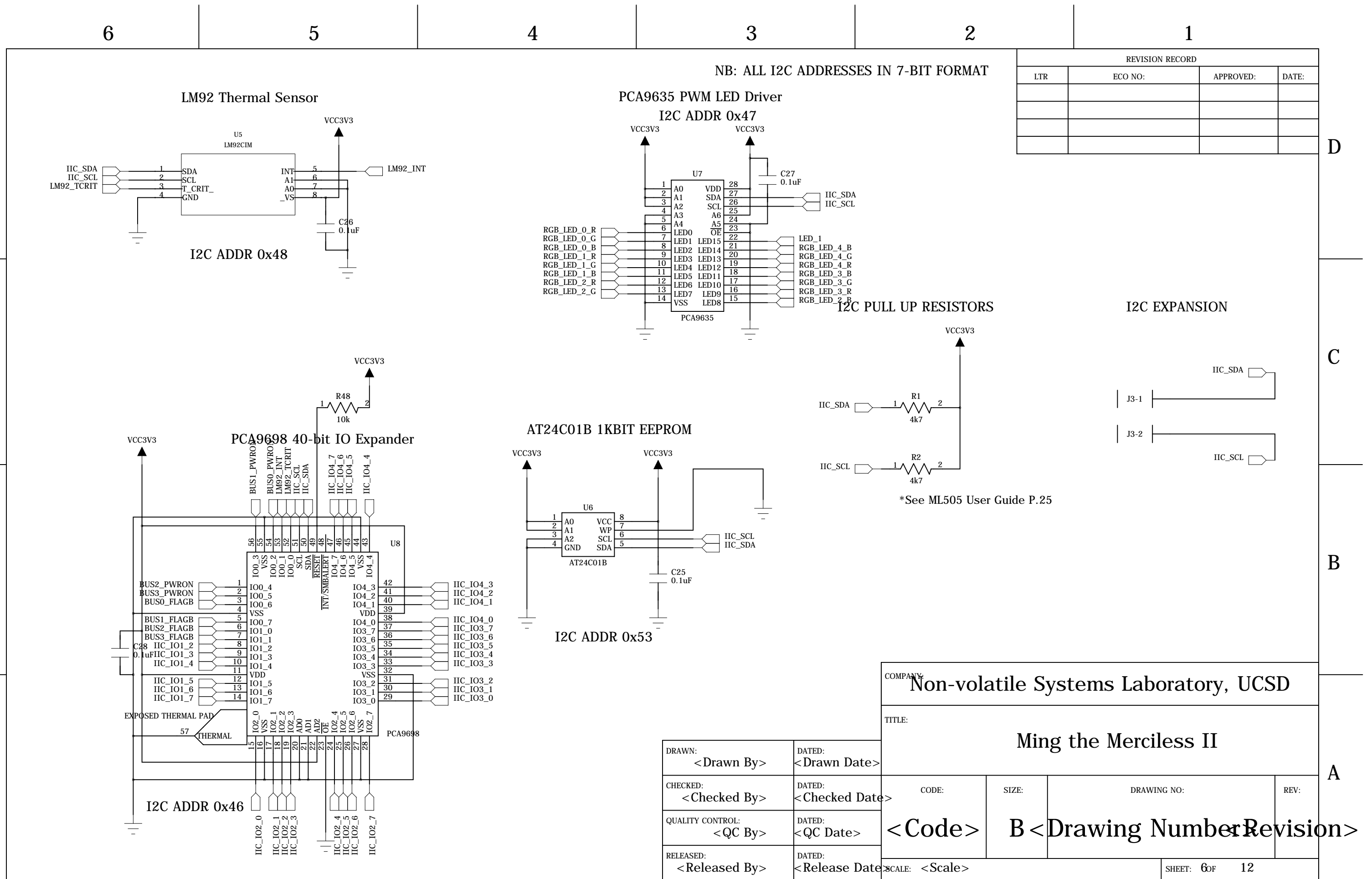
COMPANY: Non-volatile Systems Laboratory, UCSD

TITLE: Ming the Merciless II

DRAWN: <Drawn By>	DATED: <Drawn Date>
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SCALE: <Scale>			SHEET: 4 of 12





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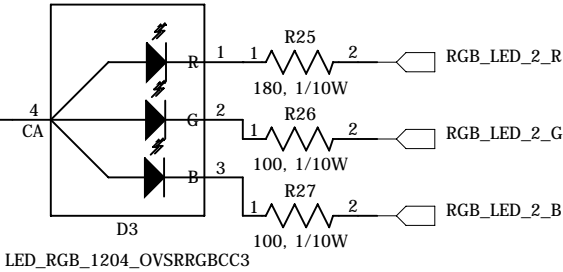
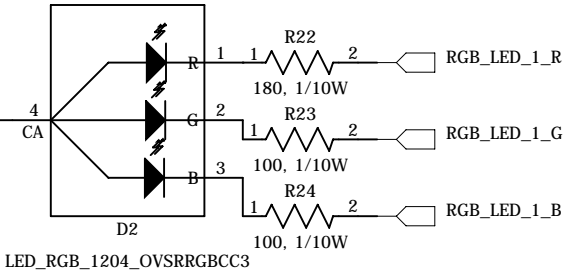
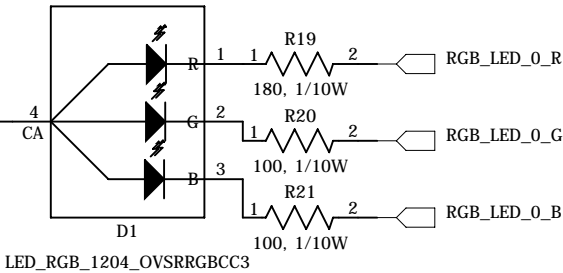
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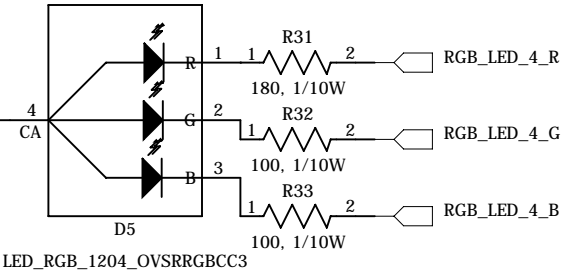
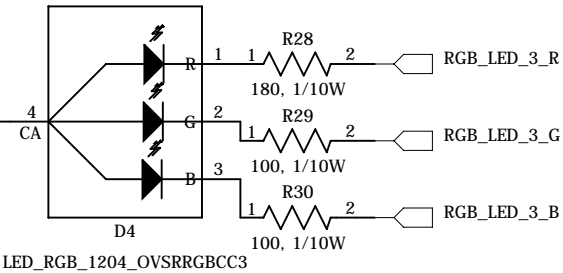
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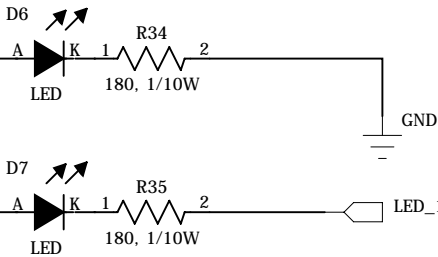
VCC5



VCC5



LED0: Power Indicator



REVISION RECORD

LTR	ECO NO:	APPROVED:	DATE:

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Non-volatile Systems Laboratory, UCSD

TITLE:

Ming the Merciless II

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CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

CODE:

SIZE:

DRAWING NO:

REV:

<Code>

B <Drawing Number> <Revision>

SCALE: <Scale>

SHEET: 7 of 12

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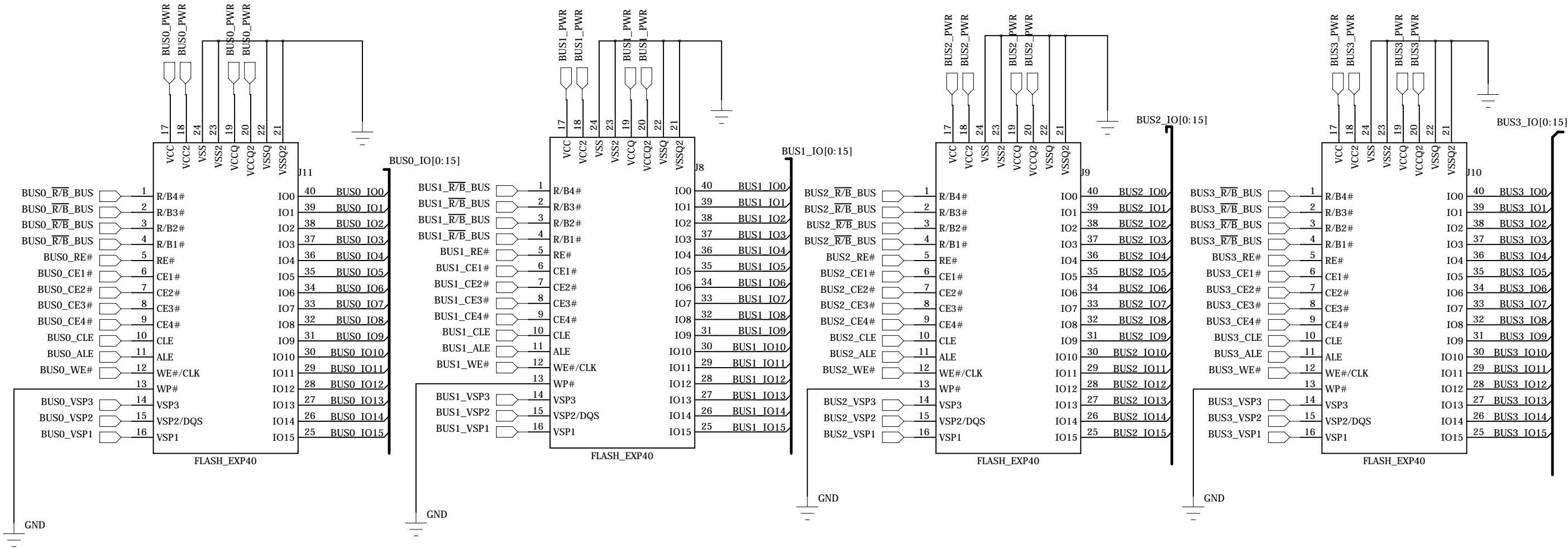
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

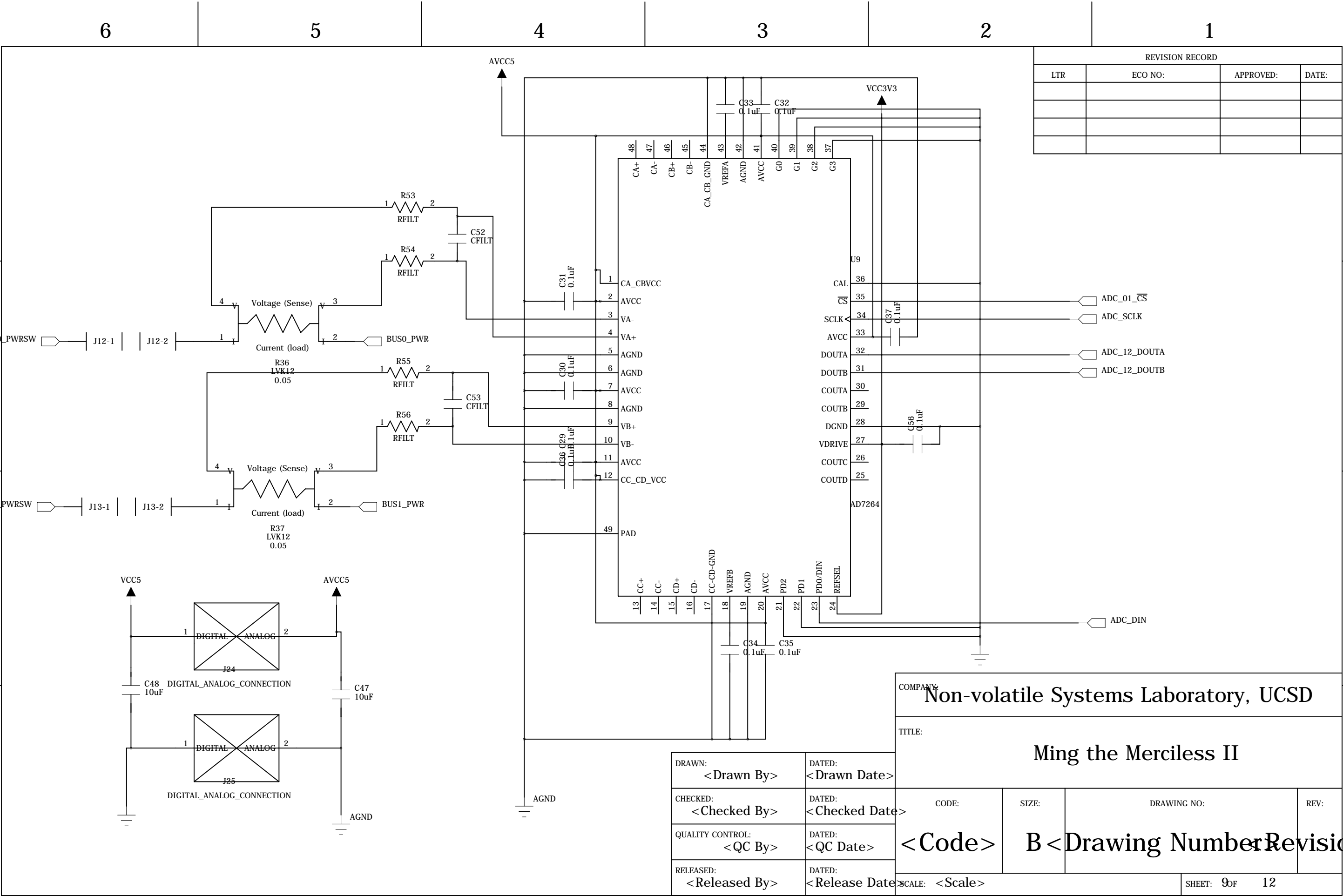
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COMPANY: Non-volatile Systems Laboratory, UCSD			
TITLE: Ming the Merciless II			
DRAWN: <Drawn By>		DATED: <Drawn Date>	
CHECKED: <Checked By>		DATED: <Checked Date>	
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SCALE: <Scale>			SHEET: 8 of 12



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Non-volatile Systems Laboratory, UCSD			
TITLE: Ming the Merciless II			
CODE:	SIZE:	DRAWING NO:	REV:
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DRAWN: <Drawn By>	DATED: <Drawn Date>
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

